



STB80NF55-06 STB80NF55-06-1 STP80NF55-06 STP80NF55-06FP

N-CHANNEL 55V - 0.005 Ω - 80A TO-220/TO-220FP/I²PAK/D²PAK

STripFET™ II POWER MOSFET

| TYPE | V _{DSS} | R _{D(on)} | I _D |
|----------------|------------------|--------------------|----------------|
| STB80NF55-06-1 | 55 V | <0.0065 Ω | 80 A |
| STP80NF55-06 | 55 V | <0.0065 Ω | 80 A |
| STP80NF55-06FP | 55 V | <0.0065 Ω | 60 A |

- TYPICAL R_{D(on)} = 0.005 Ω
- EXCEPTIONAL dv/dt CAPABILITY
- 100% AVALANCHE TESTED
- APPLICATION ORIENTED CHARACTERIZATION
- SURFACE-MOUNTING D²PAK (TO-263) POWER PACKAGE IN TUBE (NO SUFFIX) OR IN TAPE & REEL (SUFFIX "T4")

DESCRIPTION

This Power MOSFET is the latest development of STMicroelectronics unique "Single Feature Size™" strip-based process. The resulting transistor shows extremely high packing density for low on-resistance, rugged avalanche characteristics and less critical alignment steps therefore a remarkable manufacturing reproducibility.

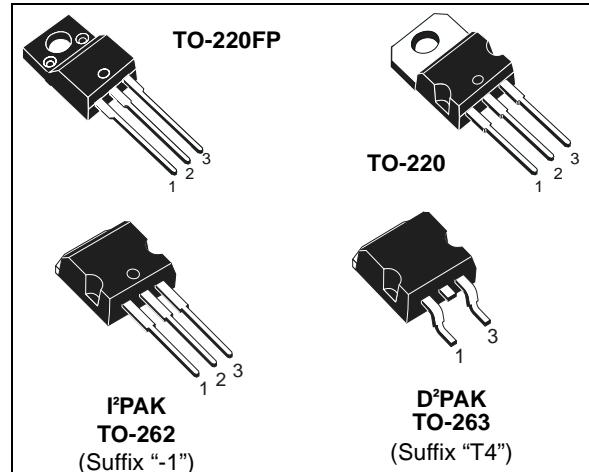
APPLICATIONS

- HIGH-EFFICIENCY DC-DC CONVERTERS
- UPS AND MOTOR CONTROL
- DC-DC CONVERTERS
- AUTOMOTIVE ENVIRONMENT

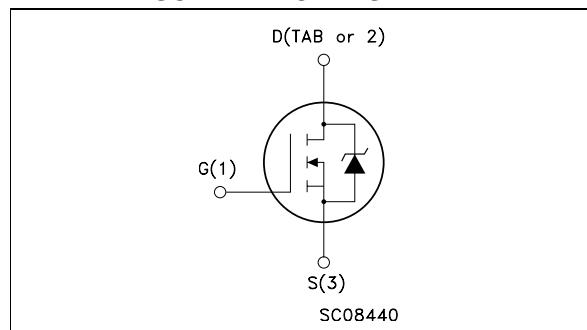
ABSOLUTE MAXIMUM RATINGS

| Symbol | Parameter | Value | | Unit |
|--------------------------------|--|--------------------------------|----------------|------|
| | | STB80NF55-06-1 STP80NF55-06 | STP80NF55-06FP | |
| V _{DS} | Drain-source Voltage (V _{GS} = 0) | 55 | | V |
| V _{DGR} | Drain-gate Voltage (R _{GS} = 20 kΩ) | 55 | | V |
| V _{GS} | Gate- source Voltage | ± 20 | | V |
| I _D | Drain Current (continuous) at T _C = 25°C | 80 | 60 | A |
| I _D | Drain Current (continuous) at T _C = 100°C | 80 | 42 | A |
| I _{DM(•)} | Drain Current (pulsed) | 320 | 240 | A |
| P _{tot} | Total Dissipation at T _C = 25°C | 300 | 45 | W |
| | Derating Factor | 2 | 0.30 | W/°C |
| dv/dt ⁽¹⁾ | Peak Diode Recovery voltage slope | 7 | | V/ns |
| E _{AS} ⁽²⁾ | Single Pulse Avalanche Energy | 1.3 | | J |
| V _{ISO} | Insulation Withstand Voltage (DC) | ----- | 2500 | V |
| T _{stg} | Storage Temperature | -55 to 175 | | °C |
| T _j | Operating Junction Temperature | | | |

(•) Pulse width limited by safe operating area.



INTERNAL SCHEMATIC DIAGRAM



(1) I_{SD} ≤ 80A, di/dt ≤ 400A/μs, V_{DD} ≤ V_{(BR)DSS}, T_j ≤ T_{JMAX}

(2) Starting T_j = 25 °C, I_D = 40A, V_{DD}= 35V

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THERMAL DATA

| | | D ² PAK/I ² PAK/ TO-220 | TO-220FP | |
|----------------------------|---|--|-------------|------------|
| Rthj-case | Thermal Resistance Junction-case | Max | 0.5 | 3.33 °C/W |
| Rthj-amb T _I | Thermal Resistance Junction-ambient Maximum Lead Temperature For Soldering Purpose | Max | 62.5 300 | °C/W °C |

ELECTRICAL CHARACTERISTICS (T_{case} = 25 °C unless otherwise specified)

OFF

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|------------------|---|---|------|------|---------|----------|
| V(BR)DSS | Drain-source Breakdown Voltage | I _D = 250 µA, V _{GS} = 0 | 55 | | | V |
| I _{DSS} | Zero Gate Voltage Drain Current (V _{GS} = 0) | V _{DS} = Max Rating V _{DS} = Max Rating T _C = 125°C | | | 1 10 | µA µA |
| I _{GSS} | Gate-body Leakage Current (V _{DS} = 0) | V _{GS} = ± 20 V | | | ±100 | nA |

ON (*)

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|---------------------|-----------------------------------|---|------|-------|--------|------|
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} = V _{GS} I _D = 250 µA | 2 | 3 | 4 | V |
| R _{DS(on)} | Static Drain-source On Resistance | V _{GS} = 10 V I _D = 40 A | | 0.005 | 0.0065 | Ω |

DYNAMIC

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|--|---|---|------|---------------------|------|----------------|
| g _{fs} (*) | Forward Transconductance | V _{DS} = 15 V I _D = 40 A | | 150 | | S |
| C _{iss} C _{oss} C _{rss} | Input Capacitance Output Capacitance Reverse Transfer Capacitance | V _{DS} = 25V, f = 1 MHz, V _{GS} = 0 | | 4400 1020 350 | | pF pF pF |

ELECTRICAL CHARACTERISTICS (continued)

SWITCHING ON

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|-------------------------------|--|--|------|-------------------|------|----------------|
| $t_{d(on)}$ t_r | Turn-on Delay Time Rise Time | $V_{DD} = 27 \text{ V}$ $I_D = 40 \text{ A}$ $R_G = 4.7 \Omega$ $V_{GS} = 10 \text{ V}$ (Resistive Load, Figure 3) | | 27 155 | | ns ns |
| Q_g Q_{gs} Q_{gd} | Total Gate Charge Gate-Source Charge Gate-Drain Charge | $V_{DD} = 44 \text{ V}$ $I_D = 80 \text{ A}$ $V_{GS} = 10 \text{ V}$ | | 142 29 60.5 | 193 | nC nC nC |

SWITCHING OFF

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|-----------------------|----------------------------------|--|------|-----------|------|----------|
| $t_{d(off)}$ t_f | Turn-off Delay Time Fall Time | $V_{DD} = 27 \text{ V}$ $I_D = 40 \text{ A}$ $R_G = 4.7 \Omega$, $V_{GS} = 10 \text{ V}$ (Resistive Load, Figure 3) | | 125 65 | | ns ns |

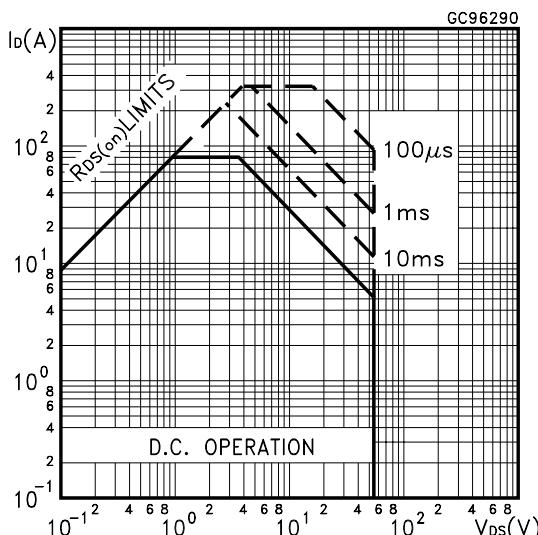
SOURCE DRAIN DIODE

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|-----------------------------------|--|--|------|--------------------|-----------|--------------------------|
| I_{SD} $I_{SDM} (\bullet)$ | Source-drain Current Source-drain Current (pulsed) | | | | 80 320 | A A |
| $V_{SD} (*)$ | Forward On Voltage | $I_{SD} = 80 \text{ A}$ $V_{GS} = 0$ | | | 1.5 | V |
| t_{rr} Q_{rr} I_{RRM} | Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current | $I_{SD} = 80 \text{ A}$ $di/dt = 100 \text{ A}/\mu\text{s}$ $V_{DD} = 35 \text{ V}$ $T_j = 150^\circ\text{C}$ (see test circuit, Figure 5) | | 100 0.32 6.5 | | ns μC A |

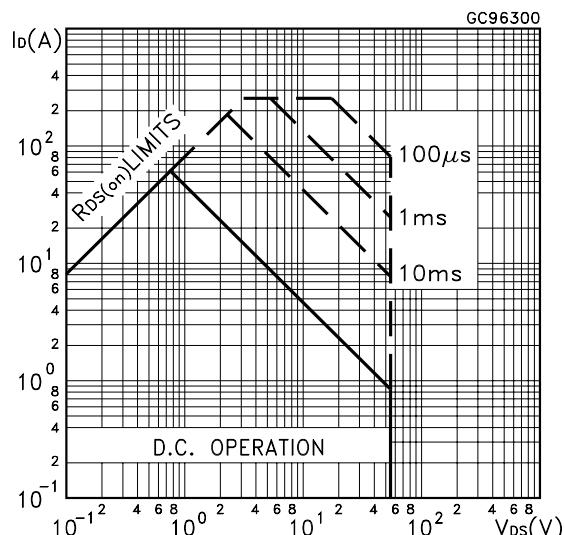
(*)Pulsed: Pulse duration = 300 μs , duty cycle 1.5 %.

(•)Pulse width limited by safe operating area.

Safe Operating Area for TO-220

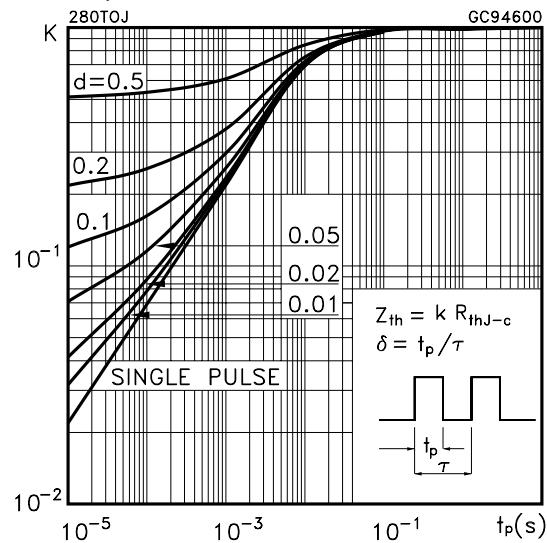


Safe Operating Area for TO-220FP

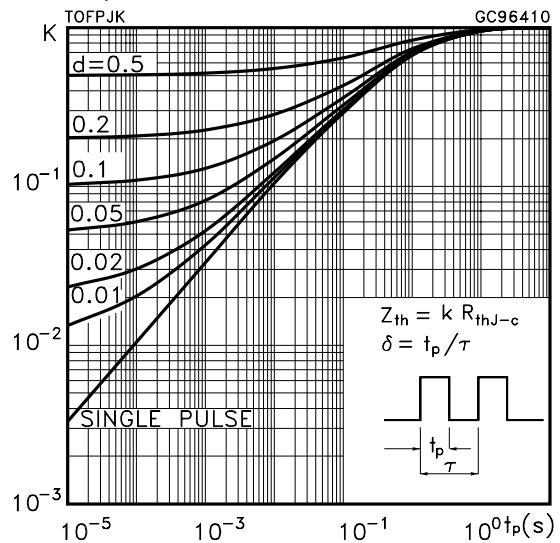


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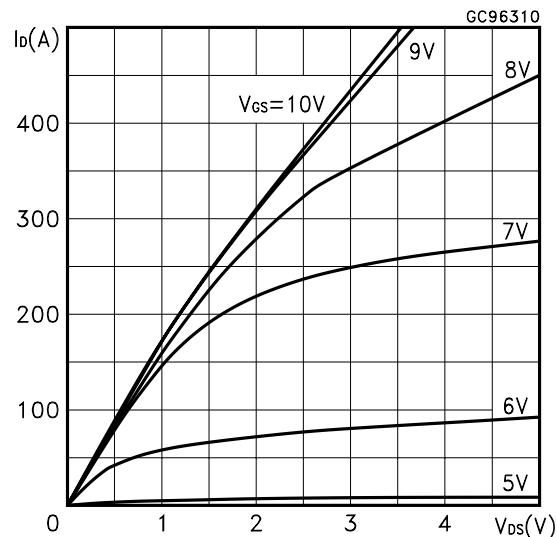
Thermal Impedance



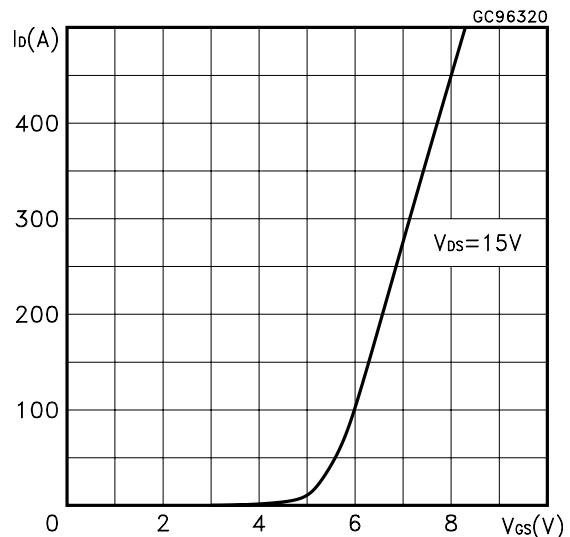
Thermal Impedance for TO-220FP



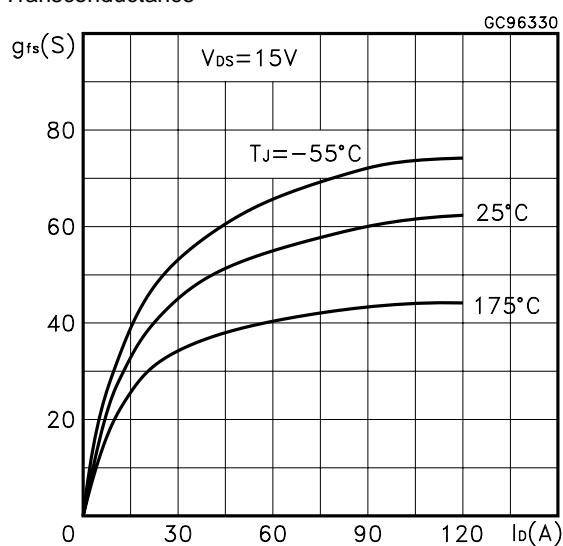
Output Characteristics



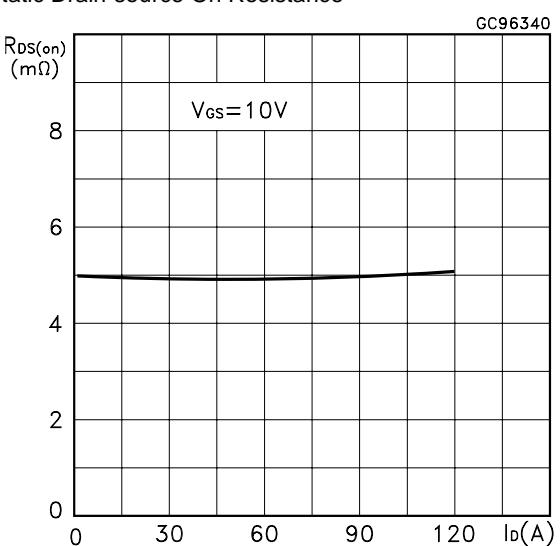
Transfer Characteristics



Transconductance

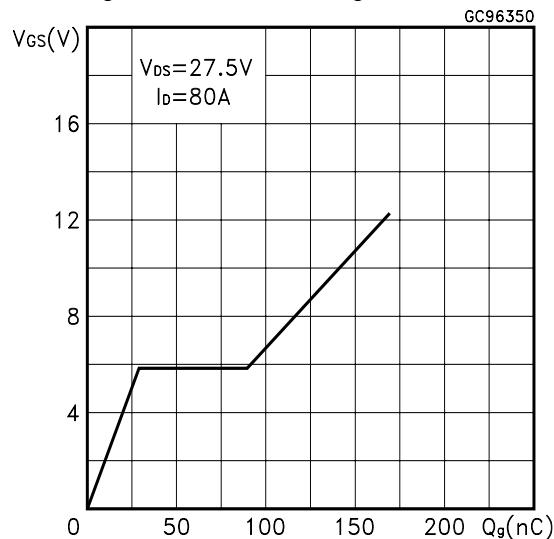


Static Drain-source On Resistance

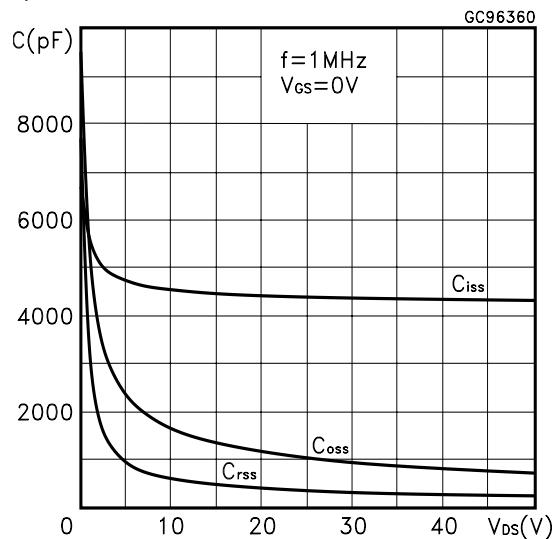


STB80NF55-06/-1 STP80NF55-06 STP80NF55-06FP

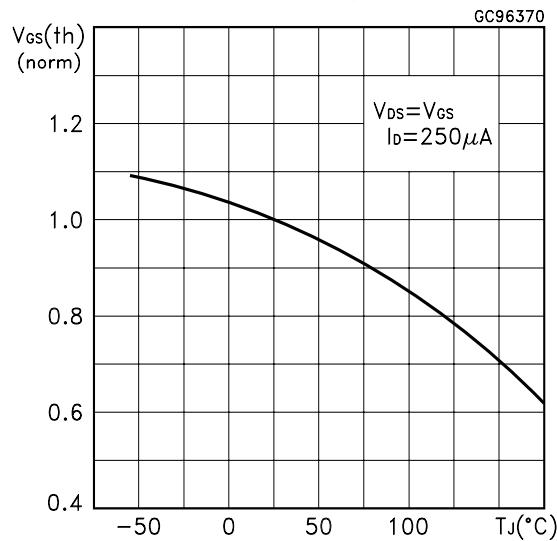
Gate Charge vs Gate-source Voltage



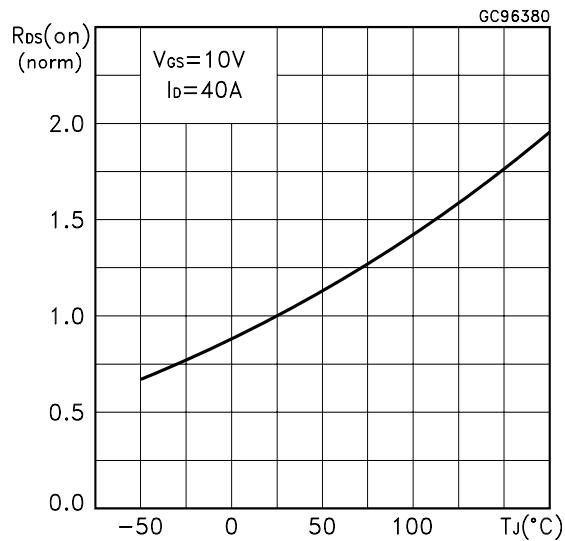
Capacitance Variations



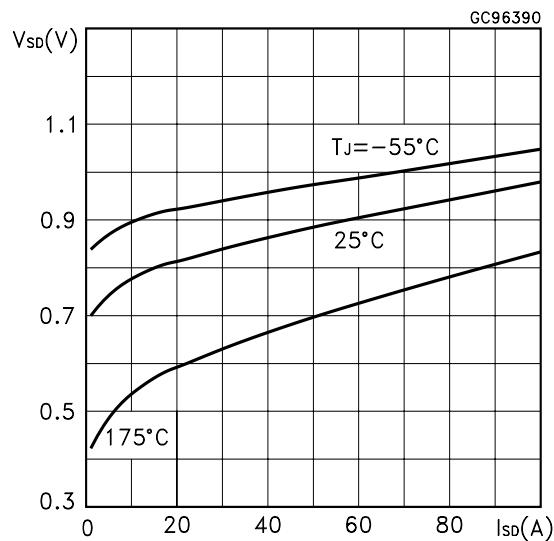
Normalized Gate Threshold Voltage vs Temperature



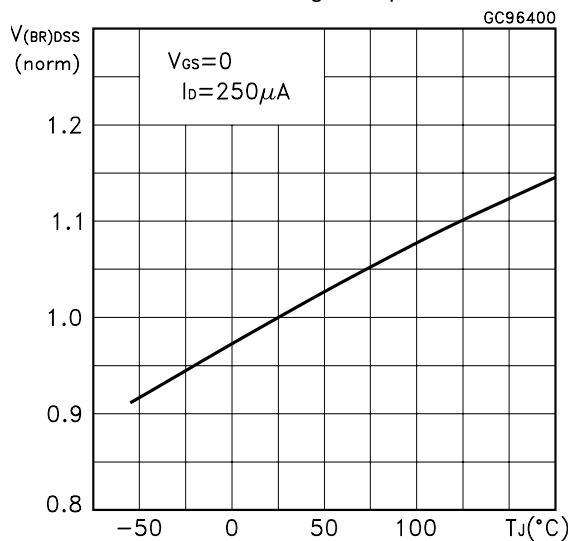
Normalized on Resistance vs Temperature



Source-drain Diode Forward Characteristics



Normalized Breakdown Voltage Temperature



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Fig. 1: Unclamped Inductive Load Test Circuit

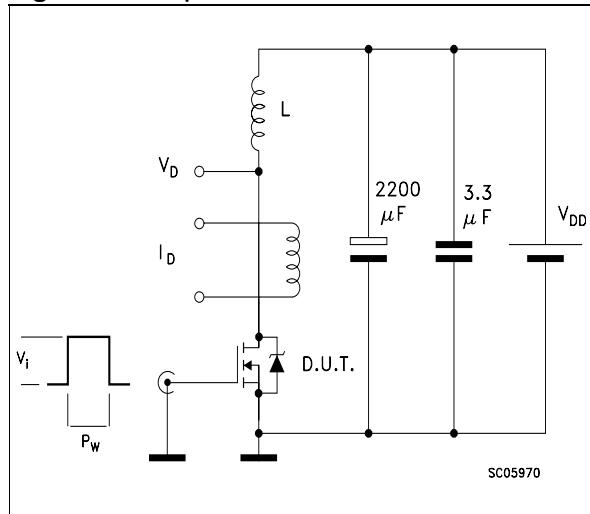


Fig. 2: Unclamped Inductive Waveform

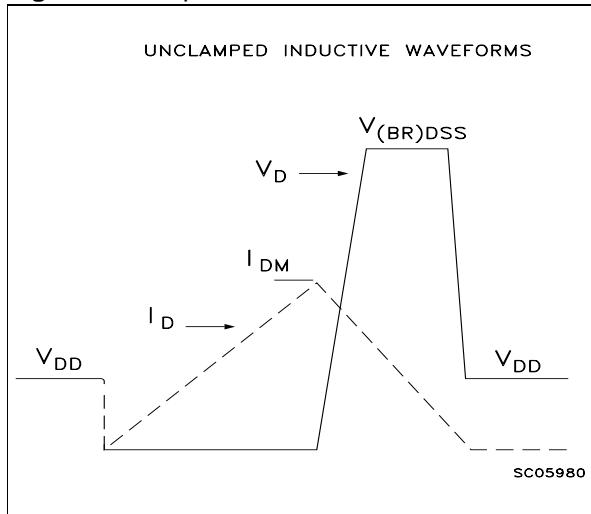


Fig. 3: Switching Times Test Circuits For Resistive Load

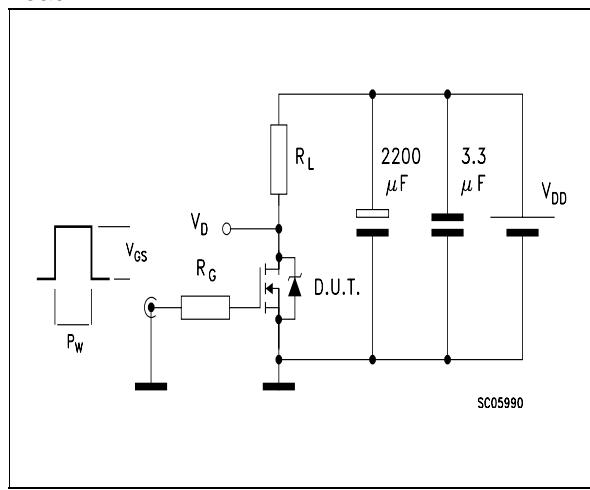


Fig. 4: Gate Charge test Circuit

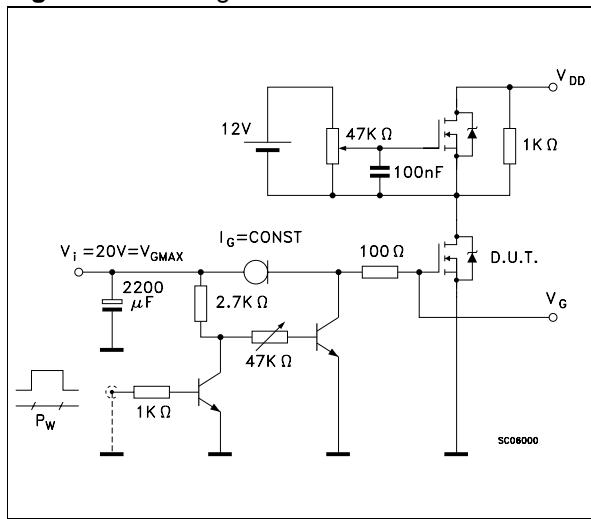
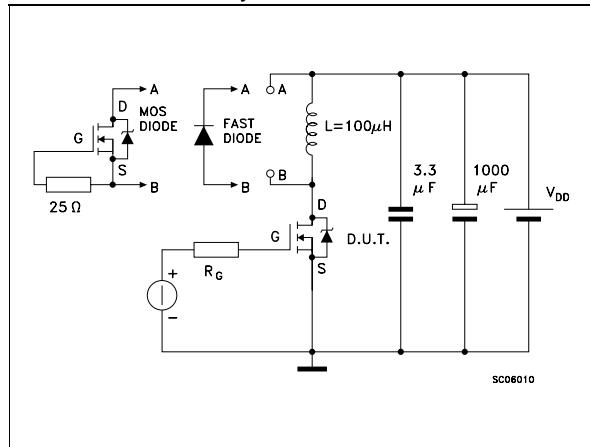
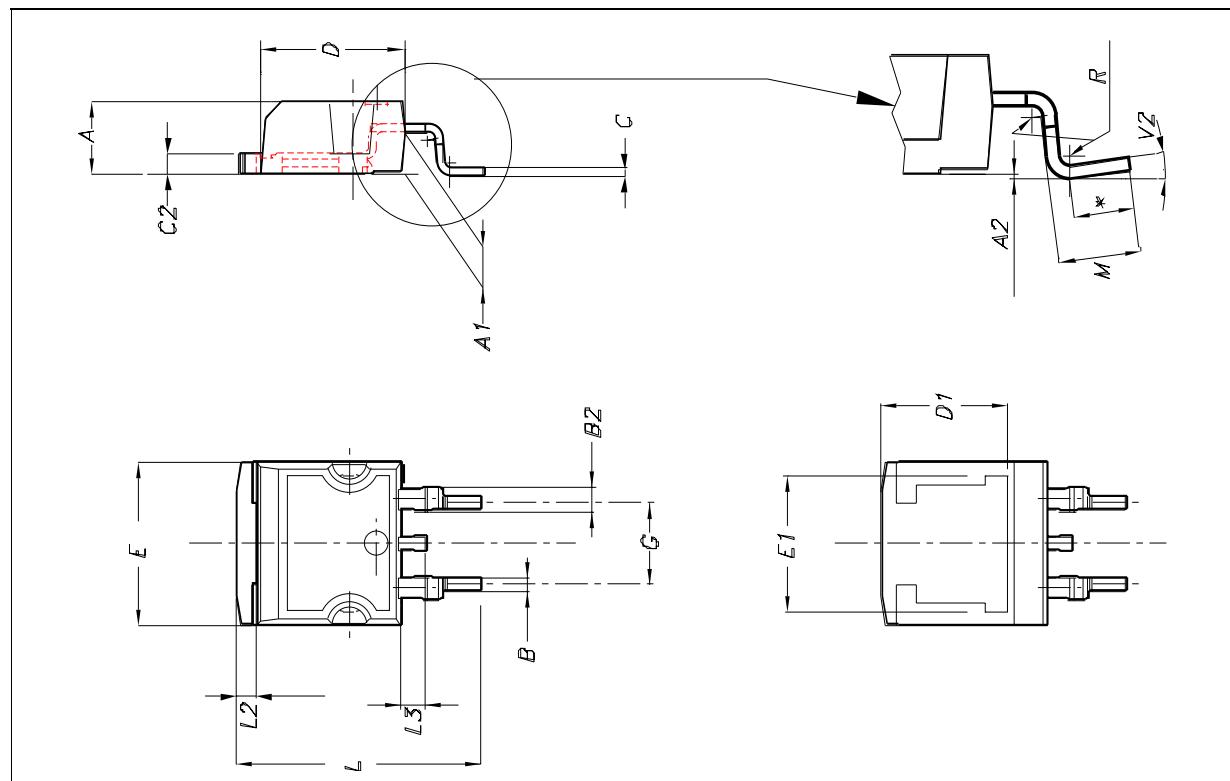


Fig. 5: Test Circuit For Inductive Load Switching And Diode Recovery Times



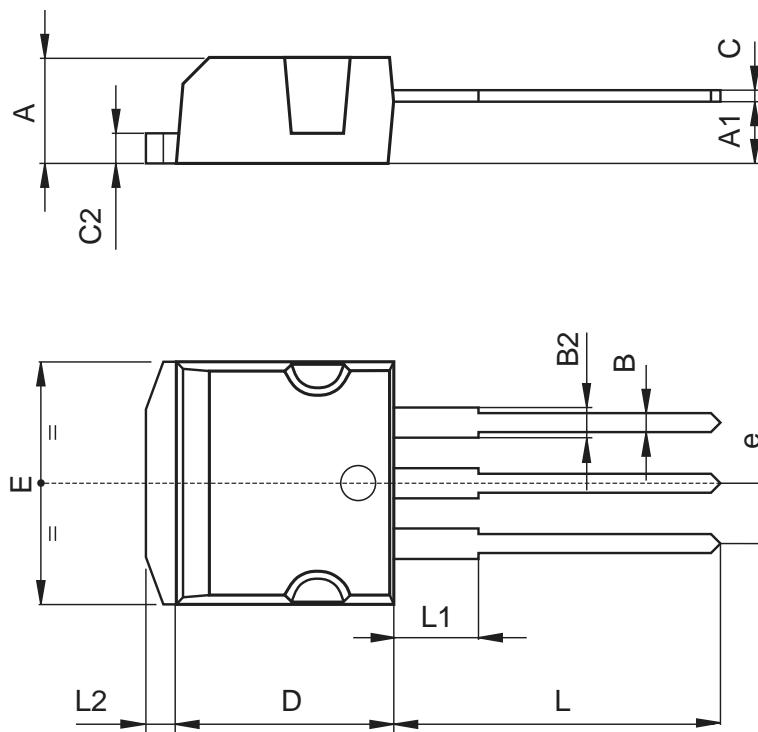
D²PAK MECHANICAL DATA

| DIM. | mm. | | | inch. | | |
|-----------|------|------|-------|-------|-------|-------|
| | MIN. | TYP. | MAX. | MIN. | TYP. | TYP. |
| A | 4.4 | | 4.6 | 0.173 | | 0.181 |
| A1 | 2.49 | | 2.69 | 0.098 | | 0.106 |
| A2 | 0.03 | | 0.23 | 0.001 | | 0.009 |
| B | 0.7 | | 0.93 | 0.028 | | 0.037 |
| B2 | 1.14 | | 1.7 | 0.045 | | 0.067 |
| C | 0.45 | | 0.6 | 0.018 | | 0.024 |
| C2 | 1.21 | | 1.36 | 0.048 | | 0.054 |
| D | 8.95 | | 9.35 | 0.352 | | 0.368 |
| D1 | | 8 | | | 0.315 | |
| E | 10 | | 10.4 | 0.394 | | 0.409 |
| E1 | 8.5 | | | | 0.334 | |
| G | 4.88 | | 5.28 | 0.192 | | 0.208 |
| L | 15 | | 15.85 | 0.591 | | 0.624 |
| L2 | 1.27 | | 1.4 | 0.050 | | 0.055 |
| L3 | 1.4 | | 1.75 | 0.055 | | 0.069 |
| M | 2.4 | | 3.2 | 0.094 | | 0.126 |
| R | | 0.4 | | | 0.016 | |
| V2 | 0° | | 8° | 0° | | 8° |



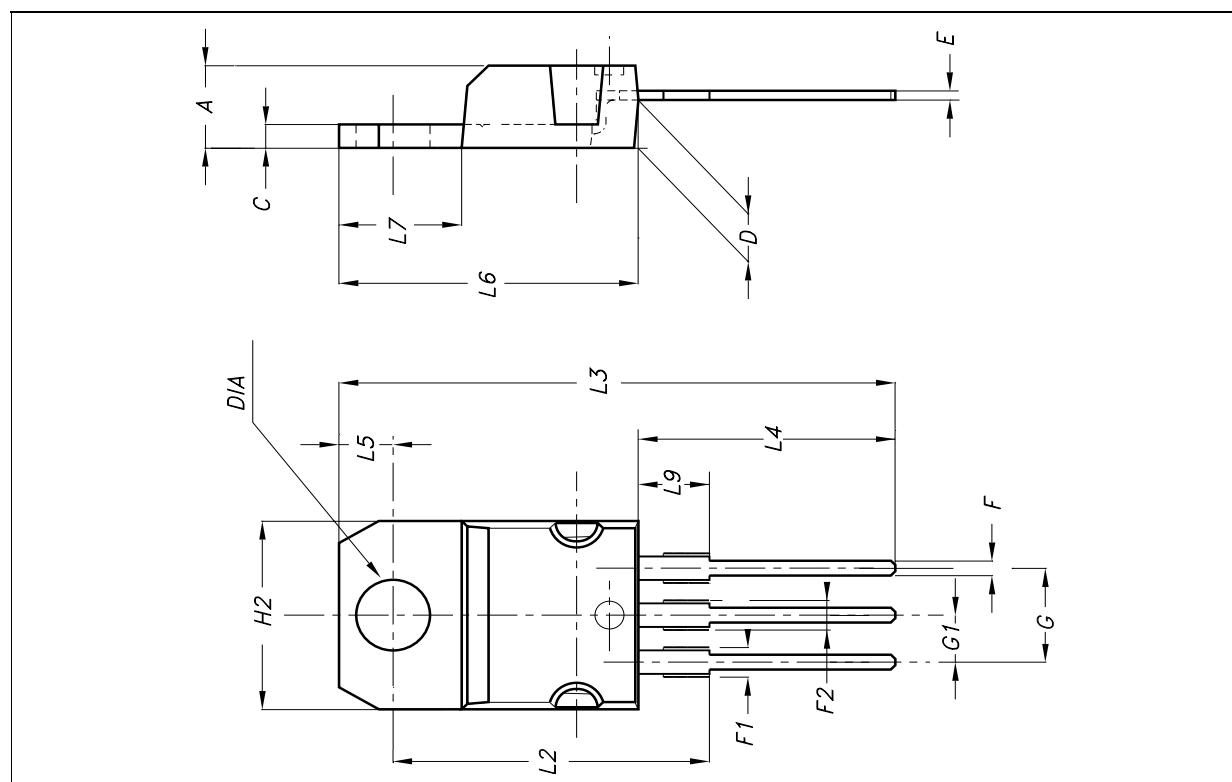
TO-262 (I²PAK) MECHANICAL DATA

| DIM. | mm | | | inch | | |
|------|------|------|------|-------|------|-------|
| | MIN. | TYP. | MAX. | MIN. | TYP. | MAX. |
| A | 4.4 | | 4.6 | 0.173 | | 0.181 |
| A1 | 2.49 | | 2.69 | 0.098 | | 0.106 |
| B | 0.7 | | 0.93 | 0.027 | | 0.036 |
| B2 | 1.14 | | 1.7 | 0.044 | | 0.067 |
| C | 0.45 | | 0.6 | 0.017 | | 0.023 |
| C2 | 1.23 | | 1.36 | 0.048 | | 0.053 |
| D | 8.95 | | 9.35 | 0.352 | | 0.368 |
| e | 2.4 | | 2.7 | 0.094 | | 0.106 |
| E | 10 | | 10.4 | 0.393 | | 0.409 |
| L | 13.1 | | 13.6 | 0.515 | | 0.531 |
| L1 | 3.48 | | 3.78 | 0.137 | | 0.149 |
| L2 | 1.27 | | 1.4 | 0.050 | | 0.055 |



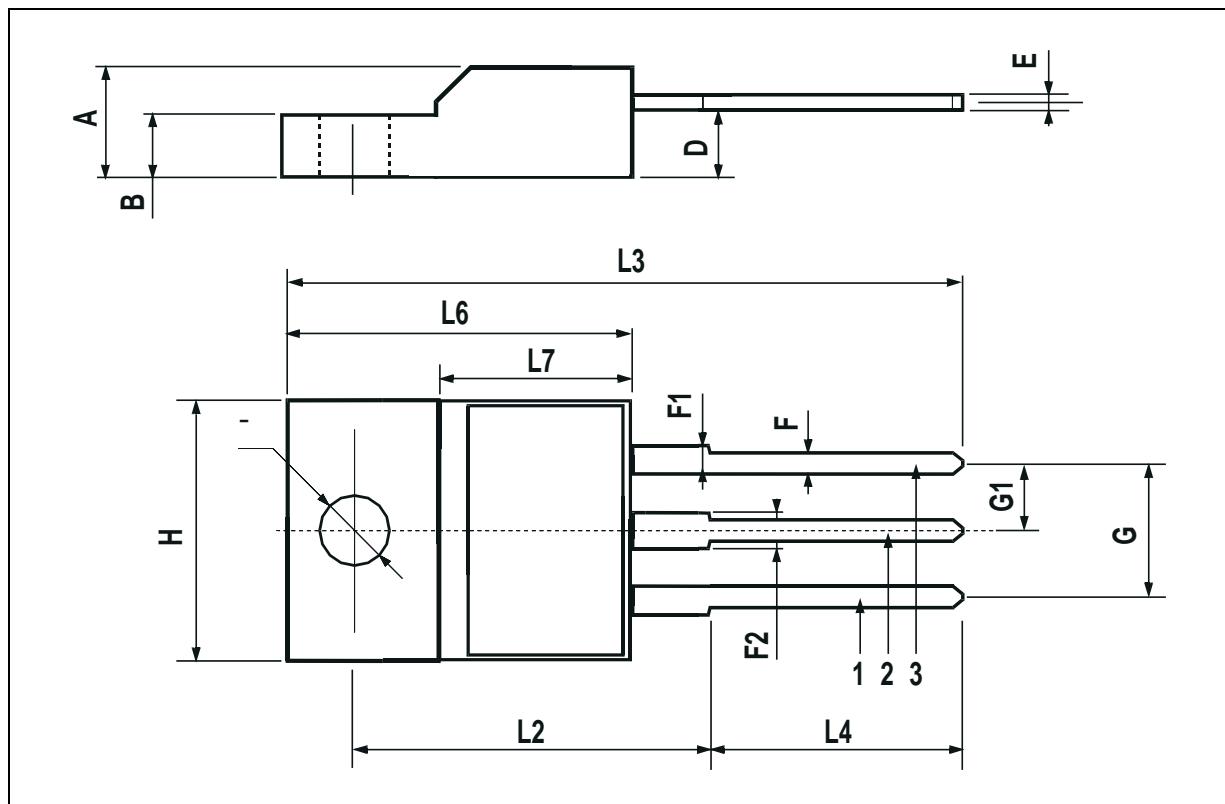
TO-220 MECHANICAL DATA

| DIM. | mm. | | | inch. | | |
|------------|-------|-------|-------|-------|-------|-------|
| | MIN. | TYP. | MAX. | MIN. | TYP. | TYP. |
| A | 4.4 | | 4.6 | 0.173 | | 0.181 |
| C | 1.23 | | 1.32 | 0.048 | | 0.051 |
| D | 2.40 | | 2.72 | 0.094 | | 0.107 |
| E | 0.49 | | 0.70 | 0.019 | | 0.027 |
| F | 0.61 | | 0.88 | 0.024 | | 0.034 |
| F1 | 1.14 | | 1.70 | 0.044 | | 0.067 |
| F2 | 1.14 | | 1.70 | 0.044 | | 0.067 |
| G | 4.95 | | 5.15 | 0.194 | | 0.203 |
| G1 | 2.40 | | 2.70 | 0.094 | | 0.106 |
| H2 | 10 | | 10.40 | 0.393 | | 0.409 |
| L2 | | 16.40 | | | 0.645 | |
| L3 | | 28.90 | | | 1.137 | |
| L4 | 13 | | 14 | 0.511 | | 0.551 |
| L5 | 2.65 | | 2.95 | 0.104 | | 0.116 |
| L6 | 15.25 | | 15.75 | 0.600 | | 0.620 |
| L7 | 6.20 | | 6.60 | 0.244 | | 0.260 |
| L9 | 3.50 | | 3.93 | 0.137 | | 0.154 |
| DIA | 3.75 | | 3.85 | 0.147 | | 0.151 |

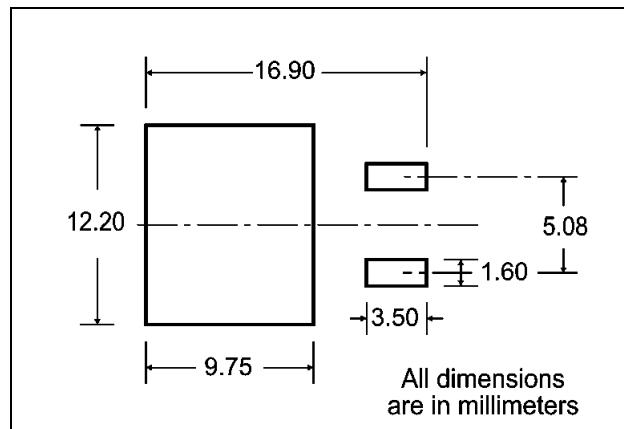


TO-220FP MECHANICAL DATA

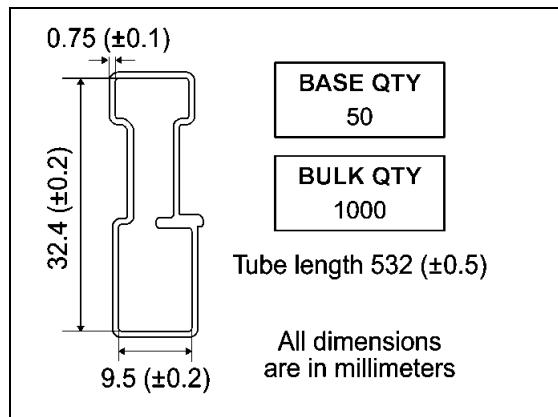
| DIM. | mm | | | inch | | |
|------|------|------|------|-------|-------|-------|
| | MIN. | TYP. | MAX. | MIN. | TYP. | MAX. |
| A | 4.4 | | 4.6 | 0.173 | | 0.181 |
| B | 2.5 | | 2.7 | 0.098 | | 0.106 |
| D | 2.5 | | 2.75 | 0.098 | | 0.108 |
| E | 0.45 | | 0.7 | 0.017 | | 0.027 |
| F | 0.75 | | 1 | 0.030 | | 0.039 |
| F1 | 1.15 | | 1.7 | 0.045 | | 0.067 |
| F2 | 1.15 | | 1.7 | 0.045 | | 0.067 |
| G | 4.95 | | 5.2 | 0.195 | | 0.204 |
| G1 | 2.4 | | 2.7 | 0.094 | | 0.106 |
| H | 10 | | 10.4 | 0.393 | | 0.409 |
| L2 | | 16 | | | 0.630 | |
| L3 | 28.6 | | 30.6 | 1.126 | | 1.204 |
| L4 | 9.8 | | 10.6 | 0.385 | | 0.417 |
| L6 | 15.9 | | 16.4 | 0.626 | | 0.645 |
| L7 | 9 | | 9.3 | 0.354 | | 0.366 |
| Ø | 3 | | 3.2 | 0.118 | | 0.126 |



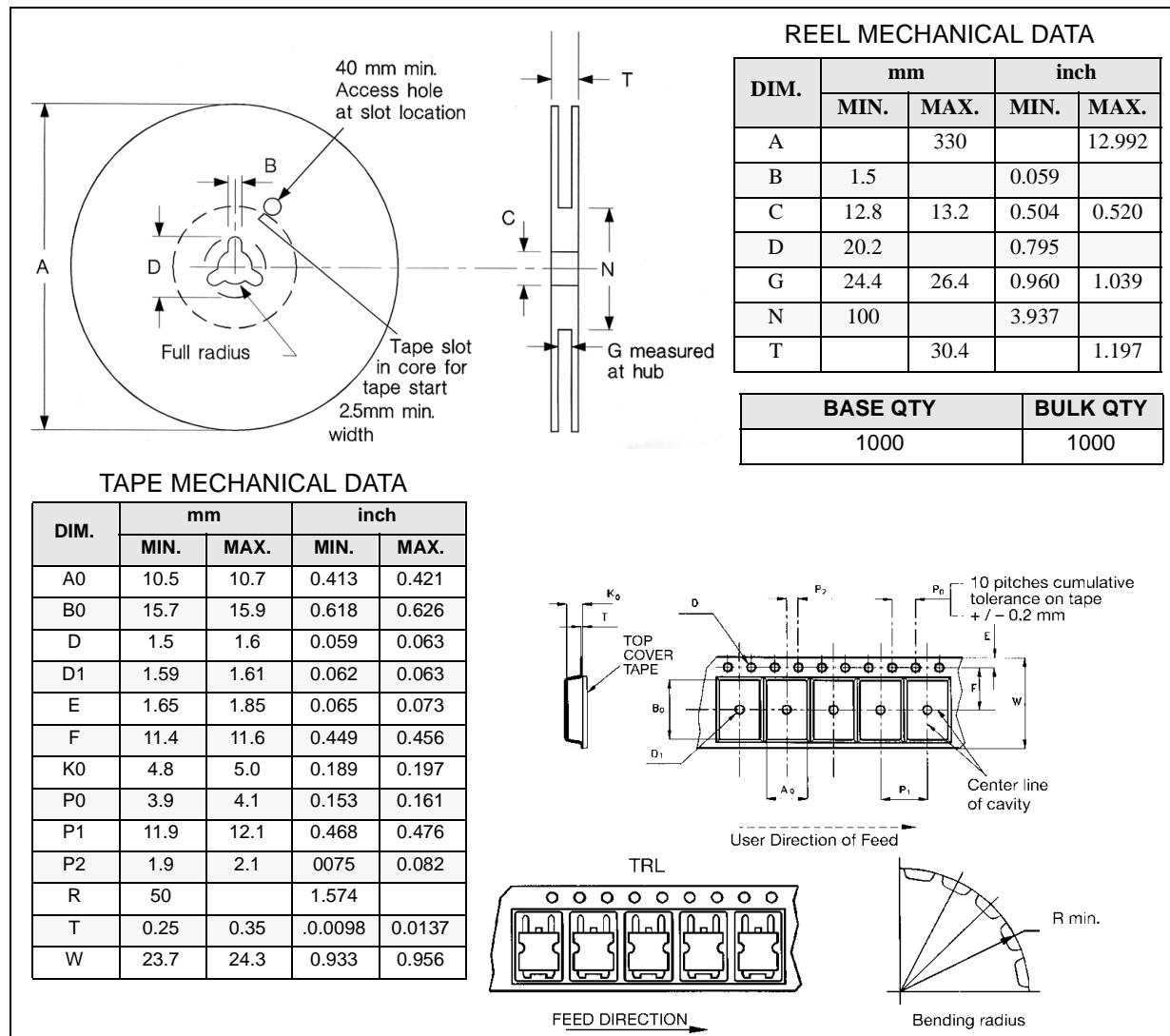
D²PAK FOOTPRINT



TUBE SHIPMENT (no suffix)*



TAPE AND REEL SHIPMENT (suffix "T4")*



* on sales type



STB80NF55-06/-1 STP80NF55-06 STP80NF55-06FP

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