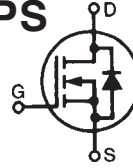


PolarHV™ Power MOSFET

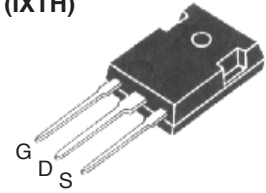
N-Channel Enhancement Mode
Fast Recovery Diode
Avalanche Rated

IXTH 26N60P
IXTQ 26N60P
IXTT 26N60P
IXTV 26N60P
IXTV 26N60PS

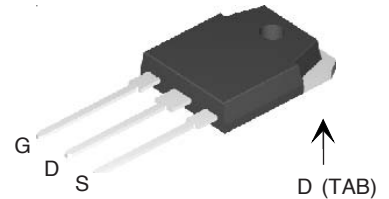
$V_{DSS} = 600 \text{ V}$
 $I_{D25} = 26 \text{ A}$
 $R_{DS(on)} \leq 270 \text{ m}\Omega$



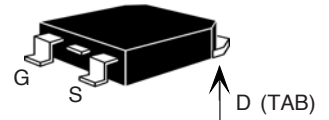
TO-247 (IXTH)



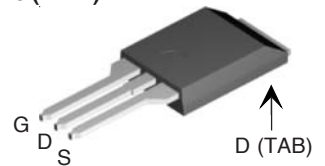
TO-3P (IXTQ)



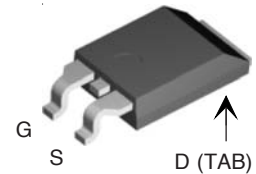
TO-268 (IXTT)



PLUS220 (IXTV)



PLUS220SMD (IXTV_S)



G = Gate D = Drain
S = Source TAB = Drain

Symbol	Test Conditions	Maximum Ratings	
V_{DSS}	$T_J = 25^\circ\text{C}$ to 150°C	600	V
V_{DGR}	$T_J = 25^\circ\text{C}$ to 150°C ; $R_{GS} = 1 \text{ M}\Omega$	600	V
V_{GSS}	Continuous	± 30	V
V_{GSM}	Transient	± 40	V
I_{D25}	$T_C = 25^\circ\text{C}$	26	A
I_{DM}	$T_C = 25^\circ\text{C}$, pulse width limited by T_{JM}	65	A
I_{AR}	$T_C = 25^\circ\text{C}$	26	A
E_{AR}	$T_C = 25^\circ\text{C}$	40	mJ
E_{AS}	$T_C = 25^\circ\text{C}$	1.2	J
dv/dt	$I_S \leq I_{DM}$, $di/dt \leq 100 \text{ A}/\mu\text{s}$, $V_{DD} \leq V_{DSS}$ $T_J \leq 150^\circ\text{C}$, $R_G = 5 \Omega$	10	V/ns
P_D	$T_C = 25^\circ\text{C}$	460	W
T_J		-55 ... +150	$^\circ\text{C}$
T_{JM}		150	$^\circ\text{C}$
T_{stg}		-55 ... +150	$^\circ\text{C}$
T_L	1.6 mm (0.062 in.) from case for 10 s Plastic body	300 250	$^\circ\text{C}$ $^\circ\text{C}$
M_d	Mounting torque (TO-3P&TO-247)	1.13/10	Nm/lb.in.
F_c	Mounting force (PLUS220)	11..65/2.5..15	N/lb
Weight	TO-3P	5.5	g
	TO-248	6.0	g
	TO-268	5.0	g
	PLUS220 & PLUS220SMD	4.0	g

Symbol	Test Conditions ($T_J = 25^\circ\text{C}$, unless otherwise specified)	Characteristic Values		
		Min.	Typ.	Max.
V_{DSS}	$V_{GS} = 0 \text{ V}$, $I_D = 250 \mu\text{A}$	600		V
$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 4 \text{ mA}$	2.5		V
I_{GSS}	$V_{GS} = \pm 30 \text{ V}$, $V_{DS} = 0$			$\pm 100 \text{ nA}$
I_{DSS}	$V_{DS} = V_{DSS}$ $V_{GS} = 0 \text{ V}$ $T_J = 125^\circ\text{C}$			25 μA
				250 μA
$R_{DS(on)}$	$V_{GS} = 10 \text{ V}$, $I_D = 0.5 I_{D25}$ Pulse test, $t \leq 300 \mu\text{s}$, duty cycle $d \leq 2\%$			270 $\text{m}\Omega$

Features

- Fast Recovery diode
- Unclamped Inductive Switching (UIS) rated
- International standard packages
- Low package inductance
- easy to drive and to protect

Advantages

- Easy to mount
- Space savings
- High power density

Symbol	Test Conditions	Characteristic Values ($T_J = 25^\circ\text{C}$, unless otherwise specified)		
		Min.	Typ.	Max.
g_{fs}	$V_{DS} = 20\text{ V}; I_D = 0.5 I_{D25}$, pulse test	16	26	S
C_{iss}	$V_{GS} = 0\text{ V}, V_{DS} = 25\text{ V}, f = 1\text{ MHz}$		4150	pF
C_{oss}			400	pF
C_{rss}			27	pF
$t_{d(on)}$	$V_{GS} = 10\text{ V}, V_{DS} = 0.5 I_{D25}$ $R_G = 5\ \Omega$ (External)		25	ns
t_r			27	ns
$t_{d(off)}$			75	ns
t_f			21	ns
$Q_{g(on)}$	$V_{GS} = 10\text{ V}, V_{DS} = 0.5 V_{DSS}, I_D = 0.5 I_{D25}$		72	nC
Q_{gs}			27	nC
Q_{gd}			24	nC
R_{thJC}	TO-3P, PLUS220 & TO-247			0.27 K/W
R_{thCK}			0.21	K/W

Source-Drain Diode		Characteristic Values ($T_J = 25^\circ\text{C}$, unless otherwise specified)		
Symbol	Test Conditions	Min.	Typ.	Max.
I_S	$V_{GS} = 0\text{ V}$			26 A
I_{SM}	Repetitive			78 A
V_{SD}	$I_F = I_S, V_{GS} = 0\text{ V}$, pulse test			1.5 V
t_{rr}	$I_F = 26\text{ A}, -di/dt = 100\text{ A}/\mu\text{s}$		400	ns
Q_{RM}	$V_R = 100\text{ V}; V_{GS} = 0\text{ V}$		7	μC

Characteristic Curves

Fig. 1. Output Characteristics @ 25°C

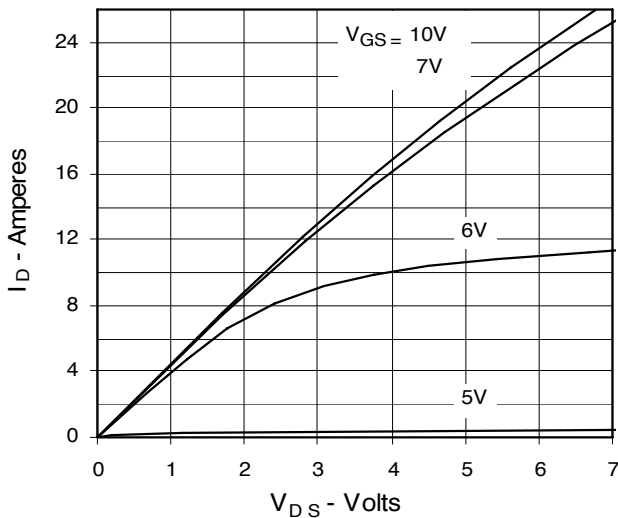
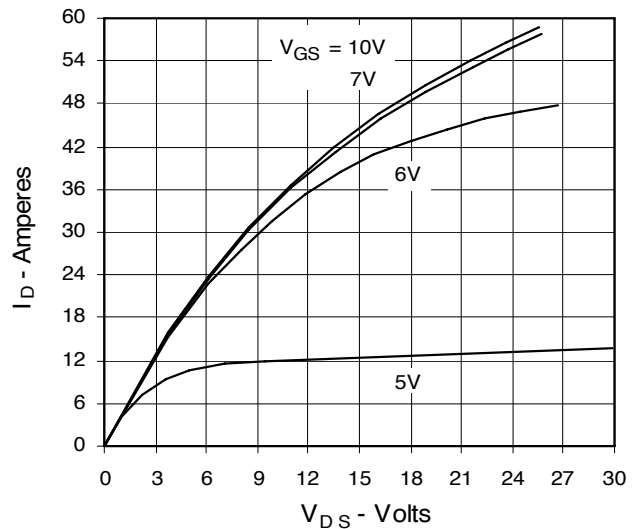


Fig. 2. Extended Output Characteristics @ 25°C

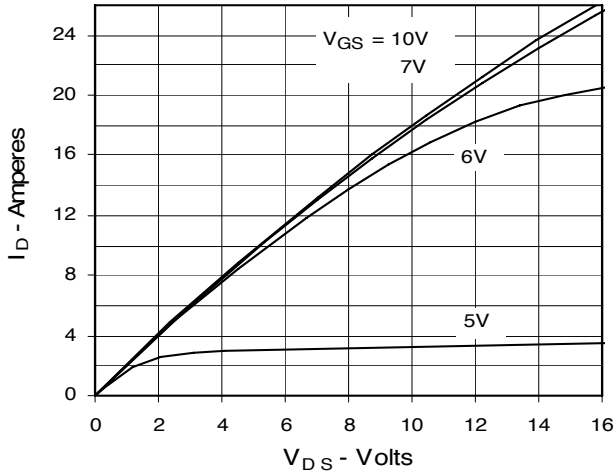


IXYS reserves the right to change limits, test conditions, and dimensions.

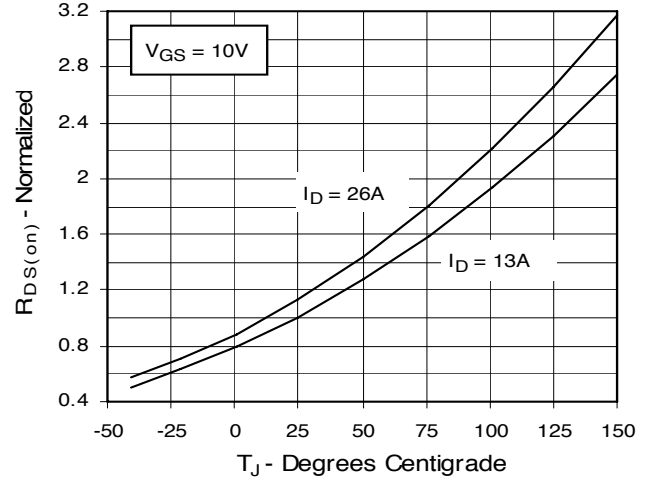
IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents:

4,835,592	4,931,844	5,049,961	5,237,481	6,162,665	6,404,065 B1	6,683,344	6,727,585
4,850,072	5,017,508	5,063,307	5,381,025	6,259,123 B1	6,534,343	6,710,405 B2	6,759,692
4,881,106	5,034,796	5,187,117	5,486,715	6,306,728 B1	6,583,505	6,710,463	

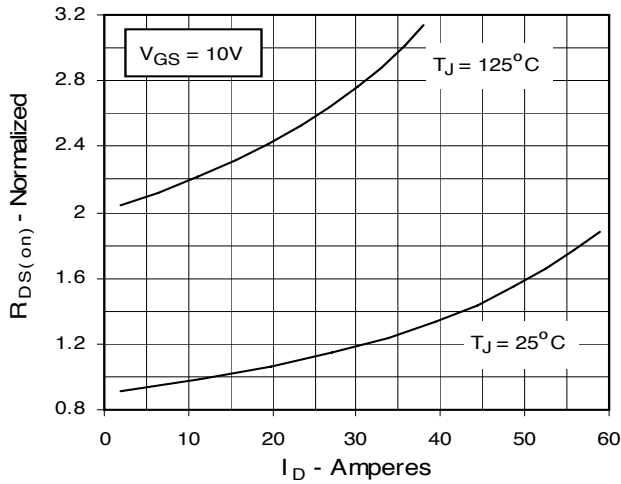
**Fig. 3. Output Characteristics
@ 125°C**



**Fig. 4. $R_{DS(on)}$ Normalized to 0.5 I_{D25}
Value vs. Junction Temperature**



**Fig. 5. $R_{DS(on)}$ Normalized to
0.5 I_{D25} Value vs. I_D**



**Fig. 6. Drain Current vs. Case
Temperature**

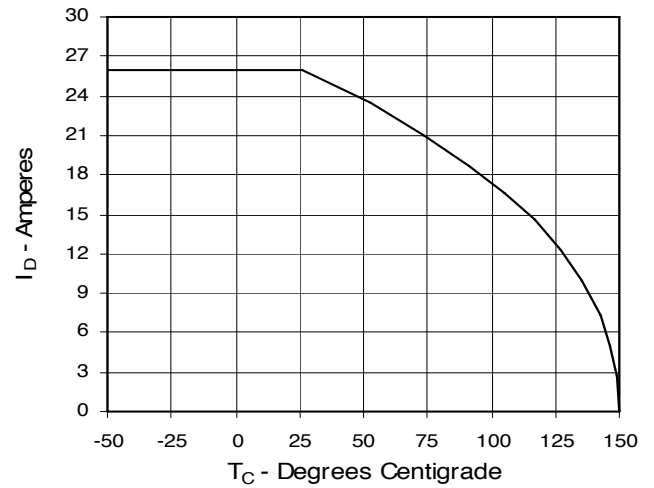


Fig. 7. Input Admittance

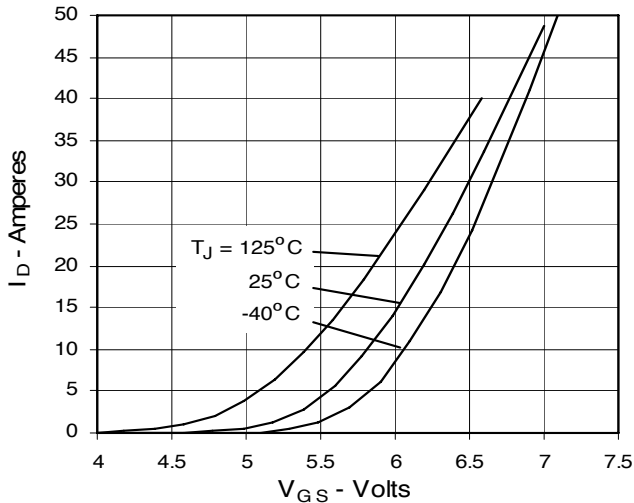


Fig. 8. Transconductance

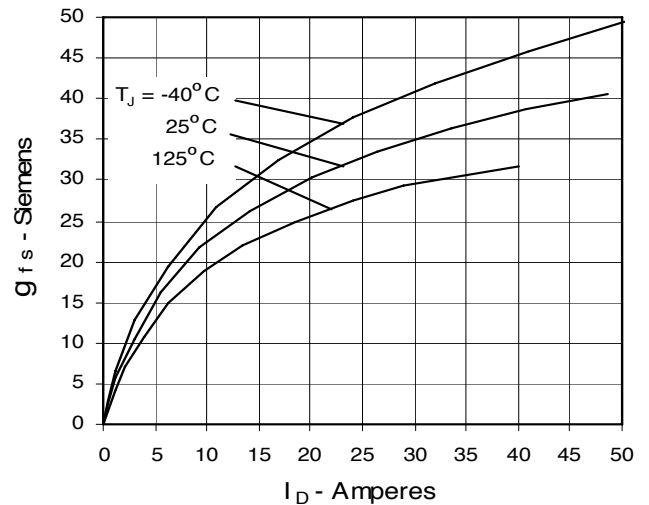


Fig. 9. Source Current vs. Source-To-Drain Voltage

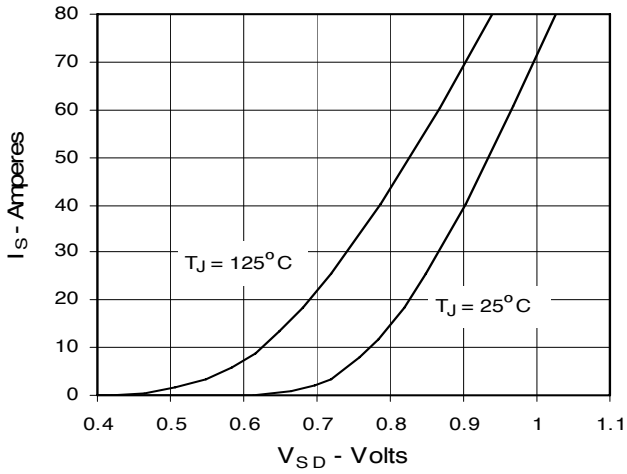


Fig. 10. Gate Charge

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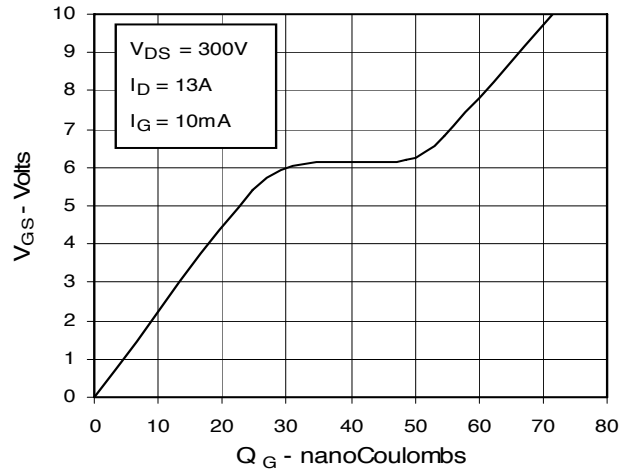


Fig. 11. Capacitance

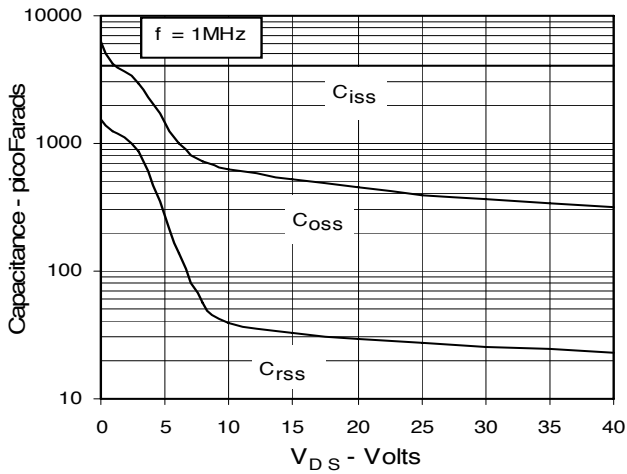


Fig. 12. Maximum Transient Thermal Resistance

