FAIRCHILD

SEMICONDUCTOR TM

KSC5039F

High Voltage Power Switch Switching Application



1.Base 2.Collector 3.Emitter

NPN Planar Silicon Transistor

Absolute Maximum Ratings $T_{C}=25^{\circ}C$ unless otherwise noted

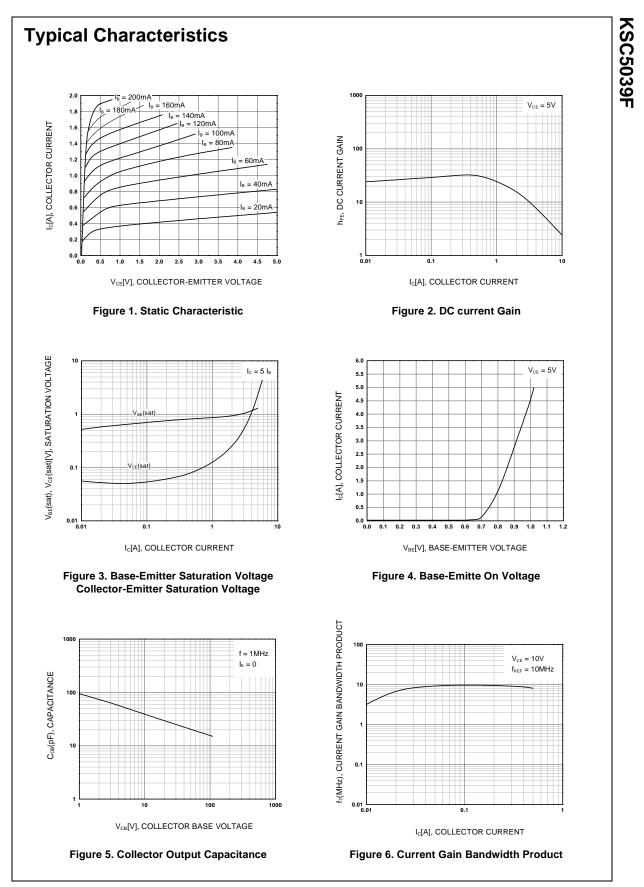
Symbol	Parameter	Value	Units	
V _{CBO}	Collector-Base Voltage	800	V	
V _{CEO}	Collector-Emitter Voltage	400	V	
V _{EBO}	Emitter-Base Voltage	7	V	
I _C	Collector Current (DC)	5	A	
I _{CP}	Collector Current (Pulse)	10	A	
I _B	Base Current	3	А	
P _C	Collector Dissipation (T _C =25°C)	30	W	
TJ	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	-65 ~ 150	°C	

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_{\rm C} = 1 {\rm mA}, I_{\rm E} = 0$	800			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_{\rm C} = 5 {\rm mA}, I_{\rm B} = 0$	400			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_{\rm C} = 1 {\rm mA}, I_{\rm C} = 0$	7			
I _{CBO}	Collector Cut-off Current	$V_{CB} = 500V, I_E = 0$			10	μΑ
I _{EBO}	Emitter Cut-off Current	V _{EB} = 7V, I _C = 0			10	μΑ
h _{FE}	*DC Current Gain	$V_{CE} = 5V, I_{C} = 0.3A$	10			
V _{CE} (sat)	*Collector-Emitter Saturation Voltage	$I_{\rm C} = 2.5 {\rm A}, I_{\rm B} = 0.5 {\rm A}$			1.5	V
V _{BE} (sat)	*Base-Emitter Saturation Voltage	$I_{\rm C} = 2.5 {\rm A}, I_{\rm B} = 0.5 {\rm A}$			2.0	V
f _T	Current Gain Bandwidth Product	$V_{CE} = 5V, I_{C} = 0.1A$		10		MHz
C _{ob}	Output Capacitance	V _{CB} = 10V , f = 1MHz		40		pF
t _{ON}	Turn ON Time	V _{CC} =150V , I _C = 2.5A,			1	μs
t _{STG}	Storage Time	$I_{B1} = -I_{B2} = 0.5A$			3	μs
t _F	Fall Time	$R_L = 60\Omega$			0.8	μs

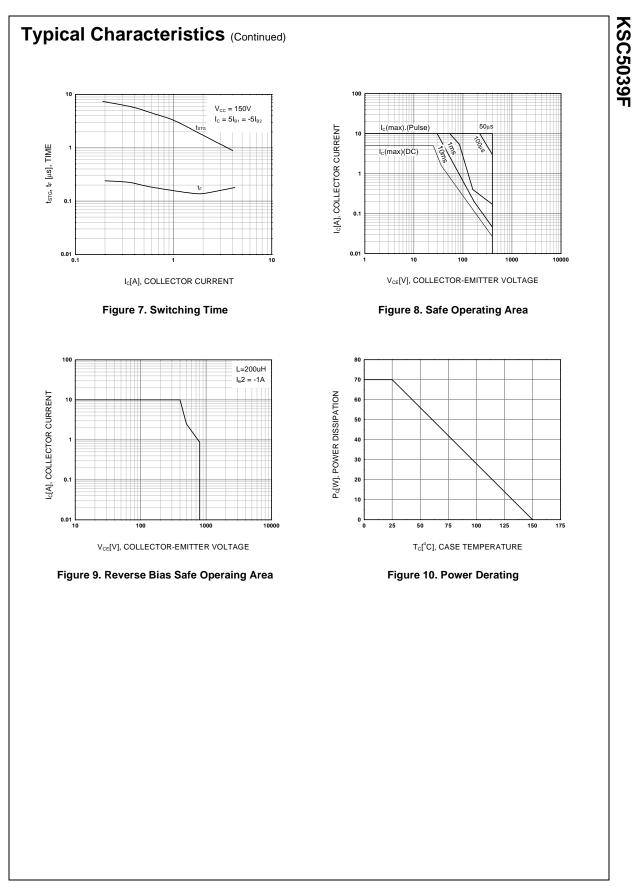
* Plus test: PW=300µs, Duty Cycle=2% Pulsed

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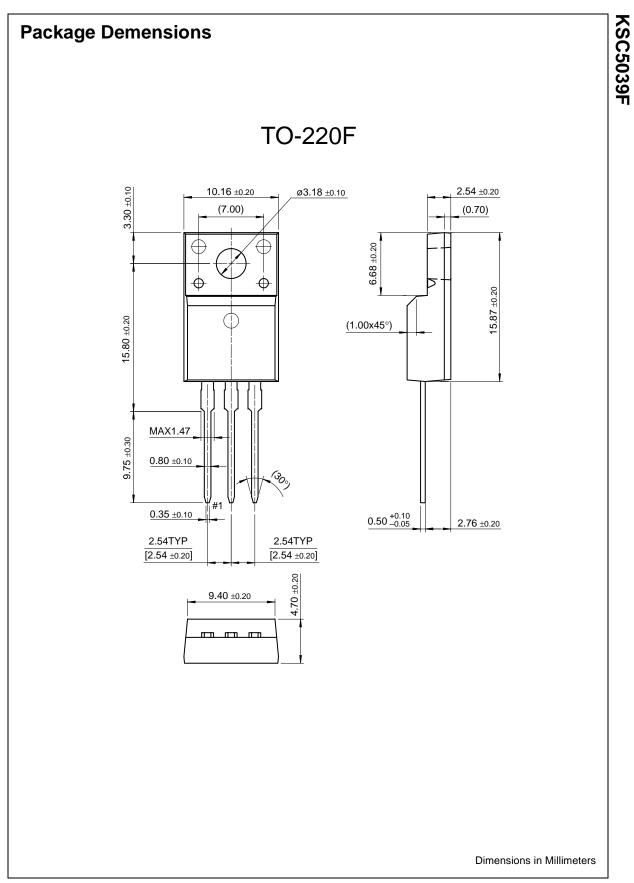
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