

## Thermally-Enhanced High Power RF LDMOS FET 200 W, 2110 – 2170 MHz

### Description

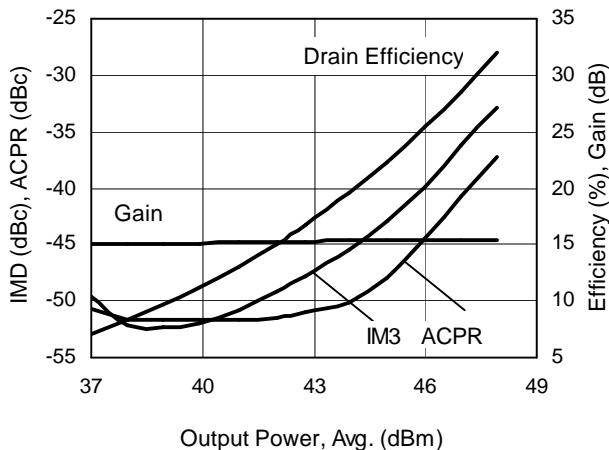
The PTFA212002E is a 200-watt, internally-matched, laterally double-diffused, **GOLDMOS** push-pull FET. It is characterized for single- and two-carrier WCDMA operation from 2110 to 2170 MHz. Thermally-enhanced packaging provides the coolest operation available. Full gold metallization ensures excellent device lifetime and reliability.

PTFA212002E  
Package 30275



#### Two-carrier WCDMA Drive-up

$V_{DD} = 28$  V,  $I_{DQ} = 1600$  mA,  $f_1 = 2140$  MHz,  $f_2 = 2150$  MHz,  
3GPP WCDMA signal, P/A R = 8.0 dB, 3.84 MHz BW



- Thermally-enhanced packaging
- Broadband internal matching
- Typical two-carrier WCDMA performance at 2140 MHz, 28 V
  - Average output power = 44 W
  - Gain = 15 dB
  - Efficiency = 27%
  - IM3 = -37 dBc
  - ACPR < -40 dBc
- Typical CW performance at 2140 MHz, 28 V
  - Output power at P-1dB = 220 W
  - Efficiency = 56%
- Integrated ESD protection: Human Body Model, Class 2 (minimum)
- Excellent thermal stability
- Low HCl drift
- Capable of handling 10:1 VSWR @ 28 V, 200 W (CW) output power

### RF Characteristics

#### WCDMA Measurements (tested in Infineon test fixture)

$V_{DD} = 28$  V,  $I_{DQ} = 2 \times 800$  mA,  $P_{OUT} = 44$  W average

$f_1 = 2135$  MHz,  $f_2 = 2145$  MHz, 3GPP signal, channel bandwidth = 3.84 MHz, peak/average = 8 dB @ 0.01% CCDF

Characteristic	Symbol	Min	Typ	Max	Unit
Gain	$G_{ps}$	14	15	—	dB
Drain Efficiency	$\eta_D$	25.5	27	—	%
Intermodulation Distortion	IMD	—	-37	-35	dBc

All published data at  $T_{CASE} = 25^\circ\text{C}$  unless otherwise indicated

**ESD:** Electrostatic discharge sensitive device—observe handling precautions!

## DC Characteristics (per side)

Characteristic	Conditions	Symbol	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}$ , $I_{DS} = 10 \text{ mA}$	$V_{(BR)DSS}$	65	—	—	V
Drain Leakage Current	$V_{DS} = 28 \text{ V}$ , $V_{GS} = 0 \text{ V}$	$I_{DSS}$	—	—	1.0	$\mu\text{A}$
Drain Leakage Current	$V_{DS} = 63 \text{ V}$ , $V_{GS} = 0 \text{ V}$	$I_{DSS}$	—	—	10	$\mu\text{A}$
On-State Resistance	$V_{GS} = 10 \text{ V}$ , $V_{DS} = 0.1 \text{ V}$	$R_{DS(on)}$	—	0.08	—	$\Omega$
Operating Gate Voltage	$V_{DS} = 28 \text{ V}$ , $I_{DQ} = 800 \text{ mA}$	$V_{GS}$	2.0	2.5	3.0	V
Gate Leakage Current	$V_{GS} = 10 \text{ V}$ , $V_{DS} = 0 \text{ V}$	$I_{GSS}$	—	—	1.0	$\mu\text{A}$

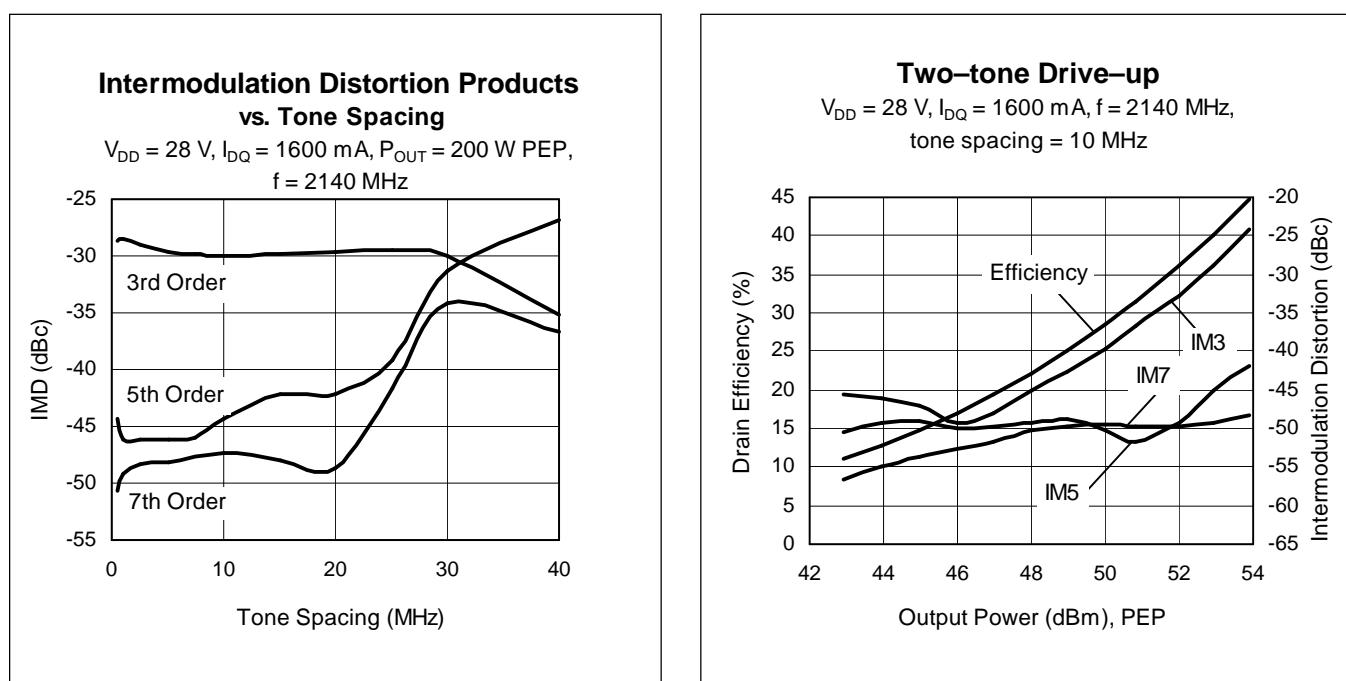
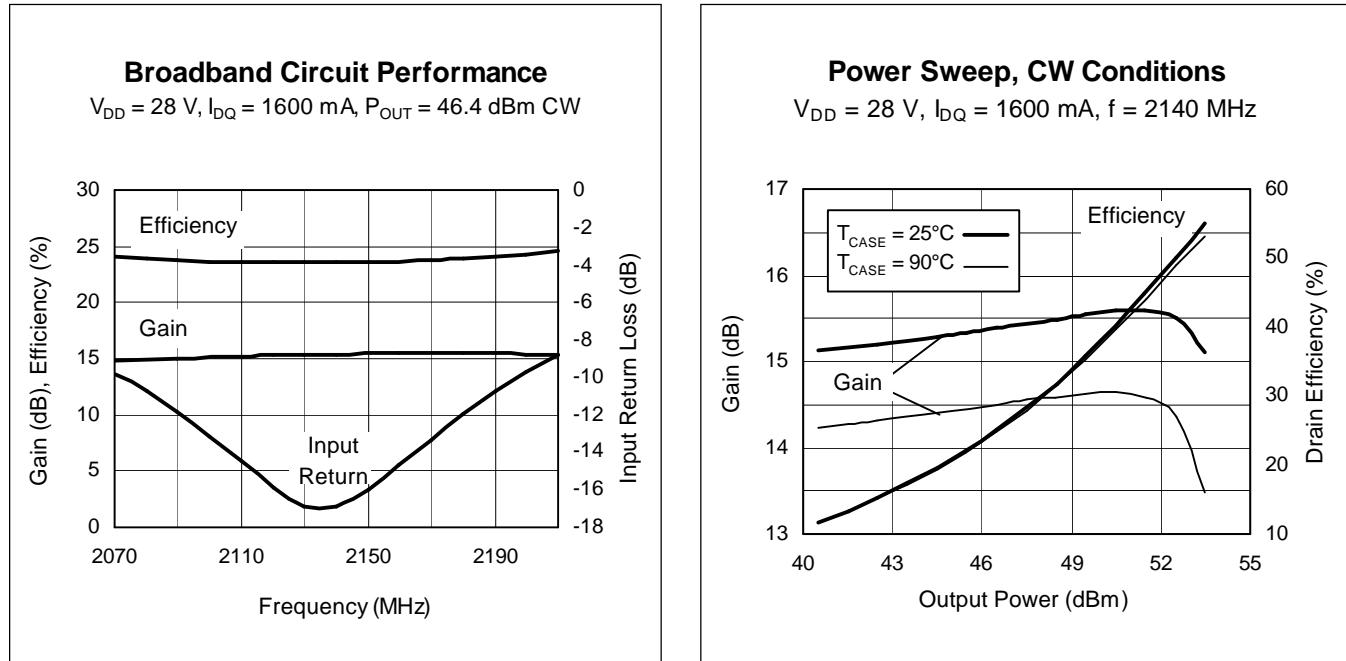
## Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	65	V
Gate-Source Voltage	$V_{GS}$	-0.5 to +12	V
Junction Temperature	$T_J$	200	$^{\circ}\text{C}$
Total Device Dissipation	$P_D$	795	W
Above 25°C derate by		4.55	W/ $^{\circ}\text{C}$
Storage Temperature Range	$T_{STG}$	-40 to +150	$^{\circ}\text{C}$
Thermal Resistance ( $T_{CASE} = 70^{\circ}\text{C}$ , 200 W CW)	$R_{\theta JC}$	0.22	$^{\circ}\text{C}/\text{W}$

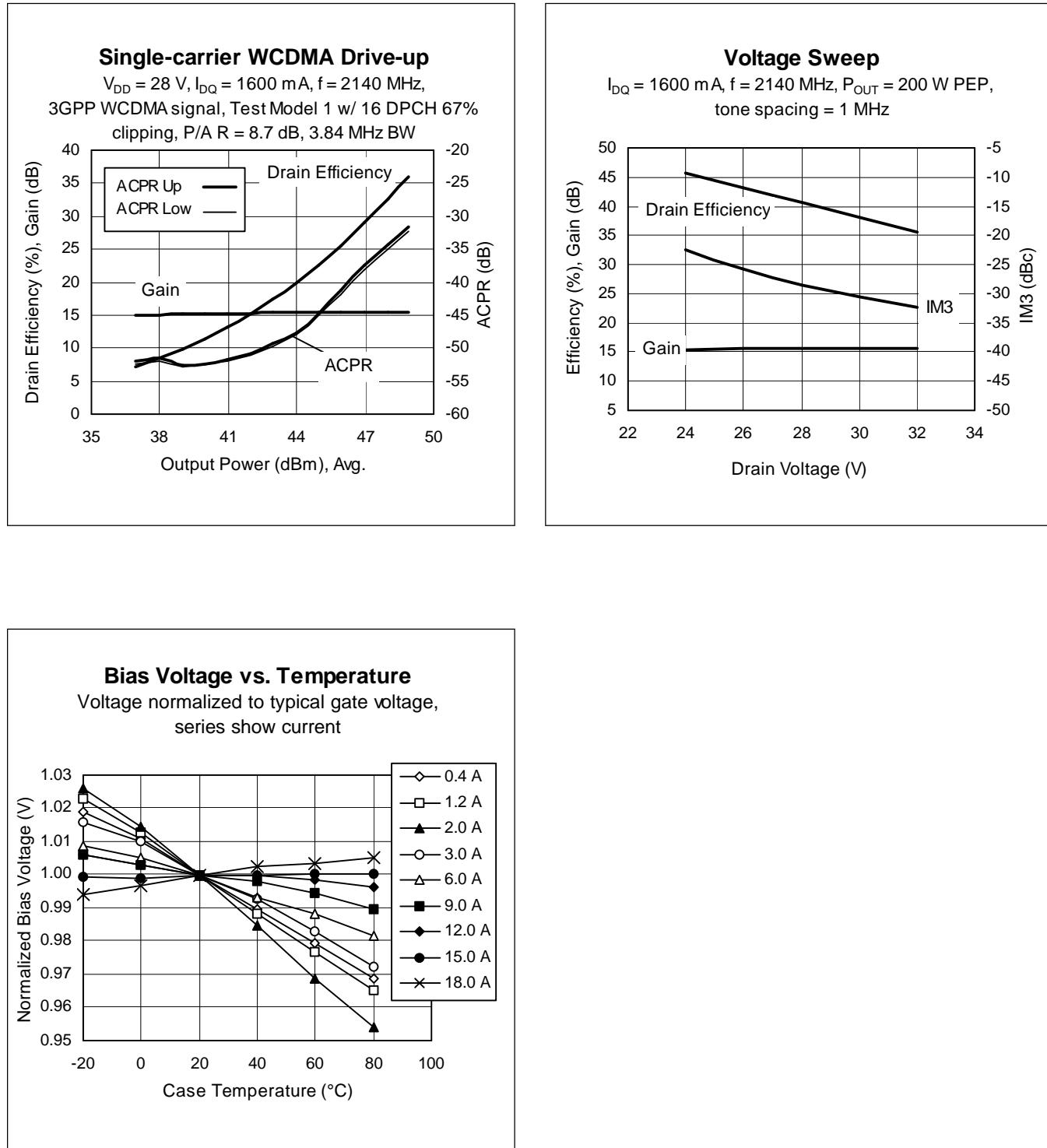
## Ordering Information

Type	Package Outline	Package Description	Marking
PTFA212002E	30275	Thermally-enhanced slotted flange, push-pull	PTFA212002E

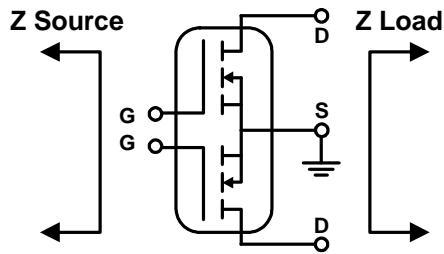
### Typical Performance (data taken in a production test fixture)



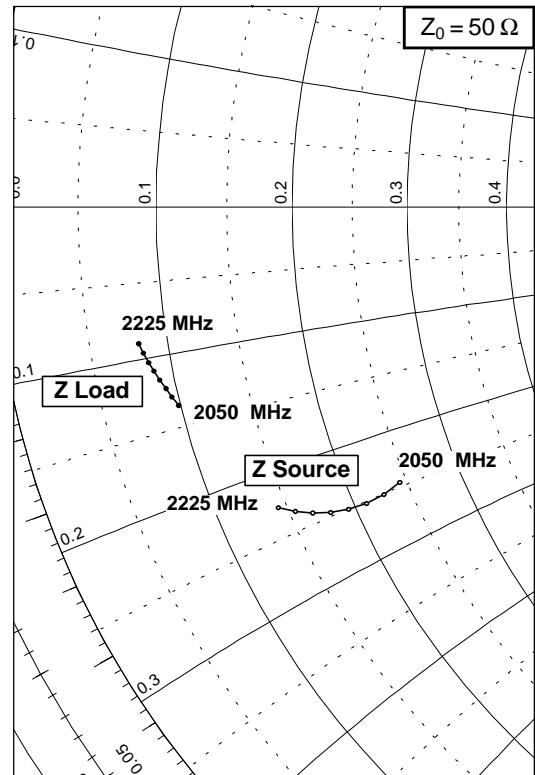
## Typical Performance (cont.)



## Broadband Circuit Impedance

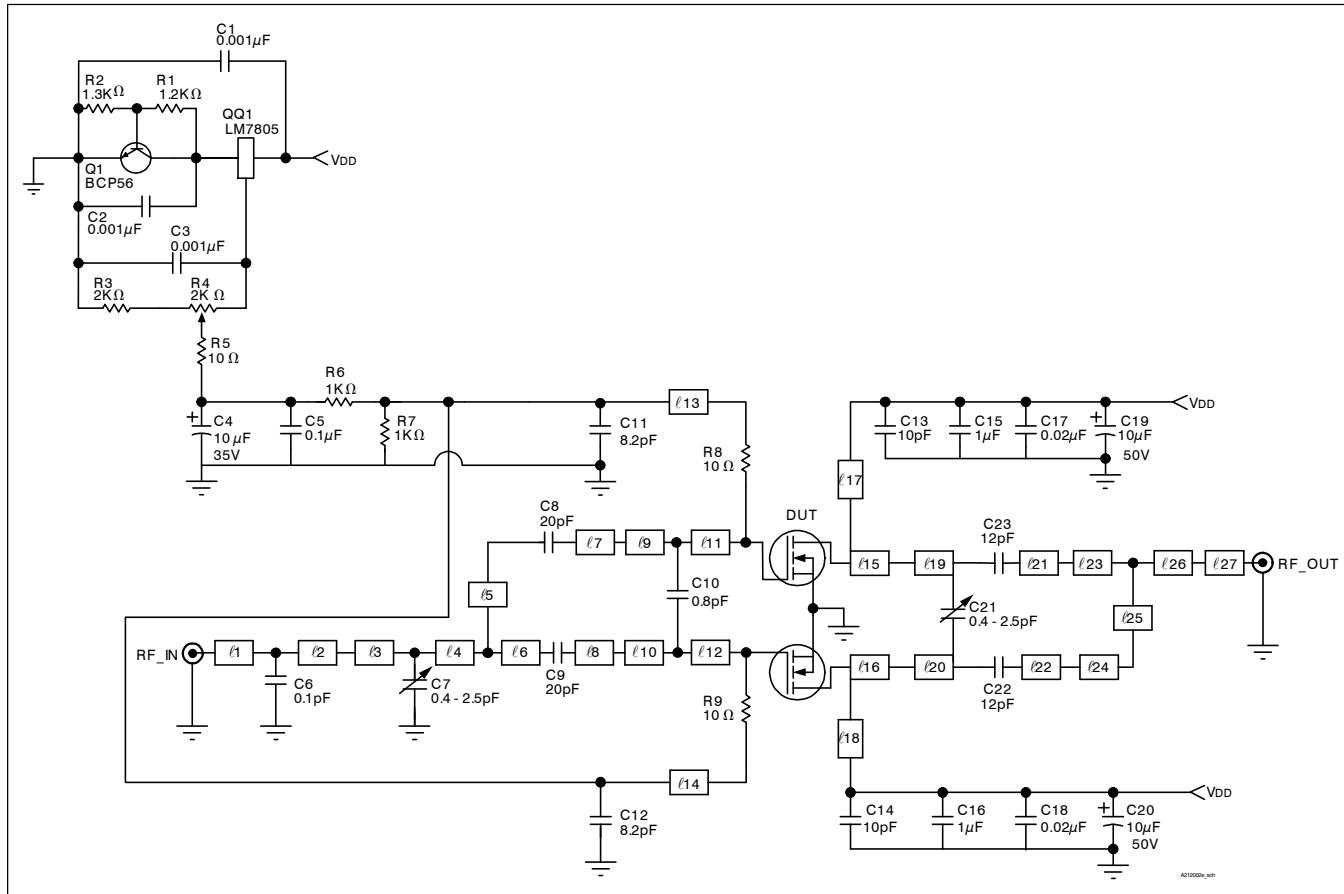


Frequency MHz	Z Source $\Omega$		Z Load $\Omega$	
	R	jX	R	jX
2050	12.21	-12.34	4.95	-6.77
2075	11.36	-12.55	4.80	-6.45
2100	10.52	-12.61	4.66	-6.12
2125	9.73	-12.53	4.52	-5.80
2150	9.00	-12.35	4.39	-5.47
2175	8.33	-12.08	4.27	-5.15
2200	7.73	-11.76	4.16	-4.82
2225	7.20	-11.39	4.06	-4.49



See next page for circuit information.

## Reference Circuit



Reference Circuit Schematic for  $f = 2140 \text{ MHz}$

### Circuit Assembly Information

DUT	PTFA212002E	LDMOS Transistor	
PCB	0.76 mm [.030"] thick, $\epsilon_r = 3.48$	Rogers 4350	1 oz. copper

Microstrip	Electrical Characteristics at 2140 MHz <sup>1</sup>	Dimensions: L x W (mm)	Dimensions: L x W (in.)
$\ell_1$	0.060 $\lambda$ , 50.0 $\Omega$	5.08 x 1.70	0.200 x 0.067
$\ell_2$	0.225 $\lambda$ , 50.0 $\Omega$	19.05 x 1.70	0.750 x 0.067
$\ell_3$	0.210 $\lambda$ , 36.0 $\Omega$	16.99 x 2.84	0.669 x 0.112
$\ell_4$	0.090 $\lambda$ , 36.0 $\Omega$	7.57 x 2.84	0.298 x 0.112
$\ell_5$	0.550 $\lambda$ , 50.0 $\Omega$	47.07 x 1.70	1.853 x 0.067
$\ell_6$	0.050 $\lambda$ , 50.0 $\Omega$	4.39 x 1.70	0.173 x 0.067
$\ell_7, \ell_8$	0.110 $\lambda$ , 32.0 $\Omega$	9.04 x 3.30	0.356 x 0.130
$\ell_9, \ell_{10}$	0.070 $\lambda$ , 22.4 $\Omega$	5.84 x 5.26	0.230 x 0.207
$\ell_{11}, \ell_{12}$	0.090 $\lambda$ , 9.1 $\Omega$	6.86 x 15.09	0.270 x 0.594
$\ell_{13}, \ell_{14}$	0.280 $\lambda$ , 50.0 $\Omega$	23.88 x 1.70	0.940 x 0.067
$\ell_{15}, \ell_{16}$	0.129 $\lambda$ , 8.4 $\Omega$	10.01 x 16.33	0.394 x 0.643

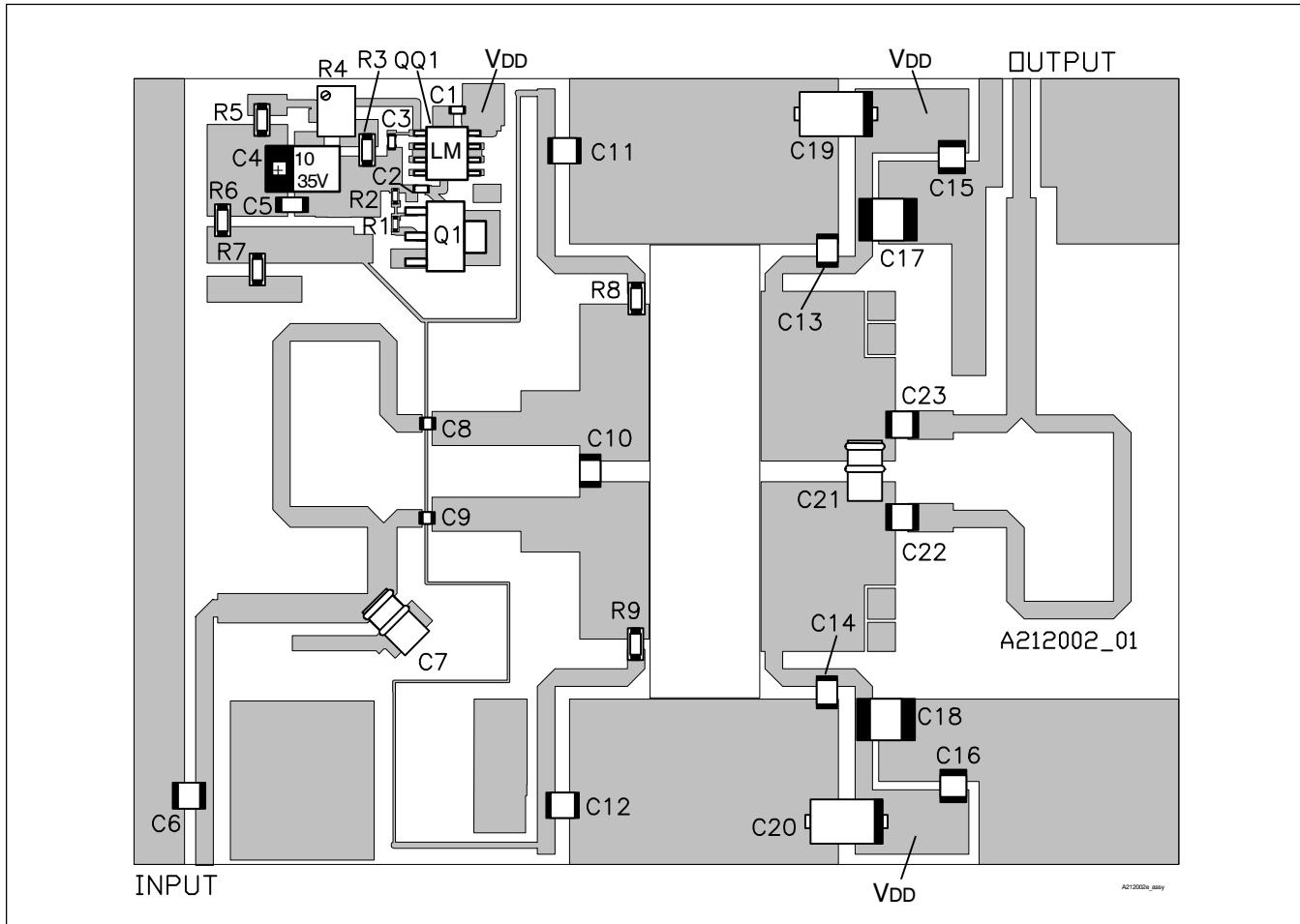
<sup>1</sup>Electrical characteristics are rounded.

(table cont. next page)

**Reference Circuit (cont.)**
**Circuit Assembly Information (cont.)**

<b>Microstrip</b>	<b>Electrical Characteristics at 2140 MHz*</b>	<b>Dimensions: L x W (mm)</b>	<b>Dimensions: L x W (in.)</b>
$\ell_{17}, \ell_{18}$	0.102 $\lambda$ , 50.0 $\Omega$	8.64 x 1.70	0.340 x 0.067
$\ell_{19}, \ell_{20}$	0.035 $\lambda$ , 13.1 $\Omega$	2.74 x 9.96	0.108 x 0.392
$\ell_{21}, \ell_{22}$	0.102 $\lambda$ , 20.5 $\Omega$	4.70 x 2.72	0.185 x 0.107
$\ell_{23}$	0.090 $\lambda$ , 50.0 $\Omega$	6.43 x 1.70	0.253 x 0.067
$\ell_{24}$	0.620 $\lambda$ , 50.0 $\Omega$	52.32 x 1.70	2.060 x 0.067
$\ell_{25}$	0.264 $\lambda$ , 36.0 $\Omega$	21.79 x 2.84	0.858 x 0.112
$\ell_{26}$	0.136 $\lambda$ , 50.0 $\Omega$	11.51 x 1.70	0.453 x 0.067

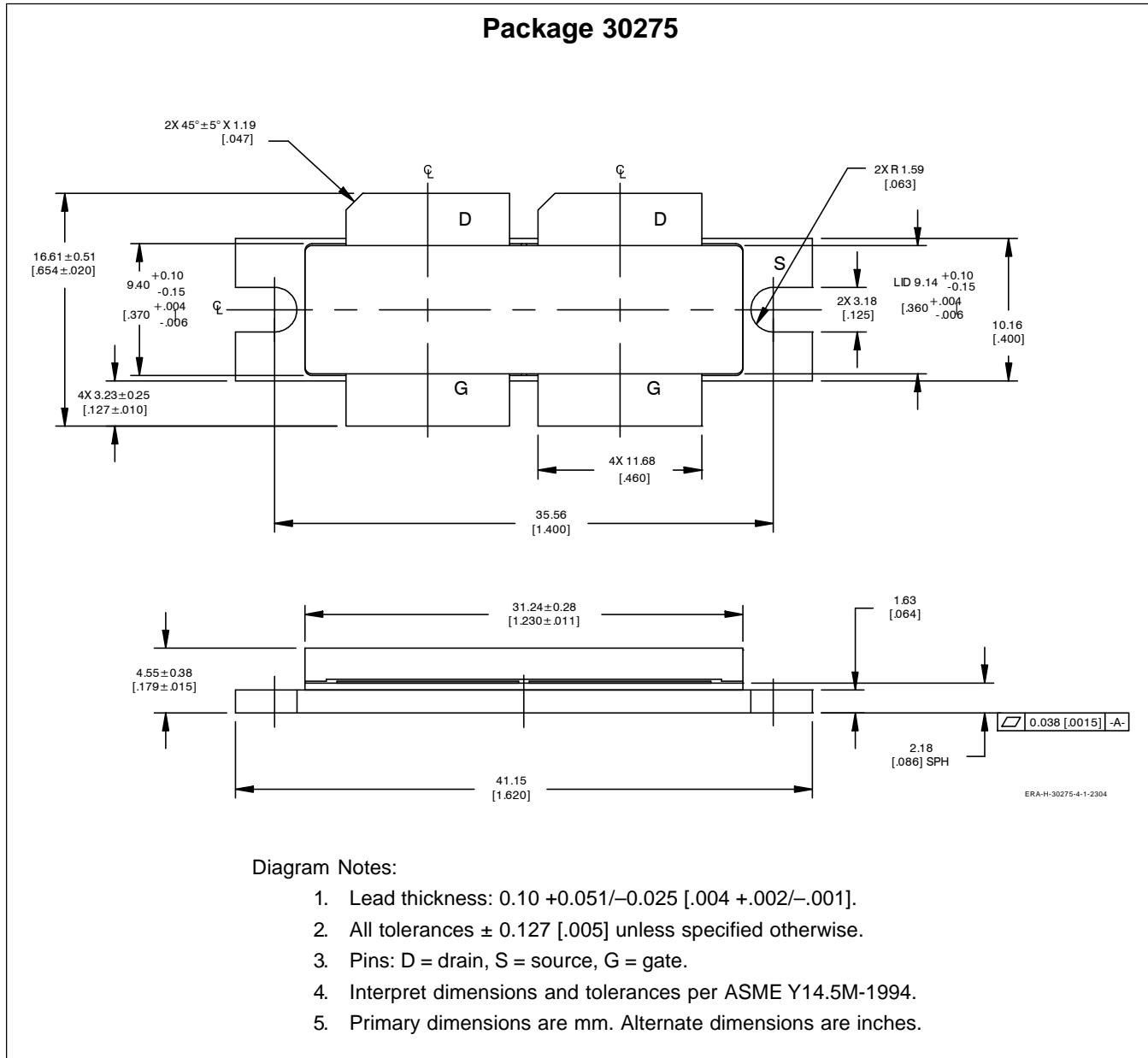
<b>Component</b>	<b>Description</b>	<b>Suggested Manufacturer</b>	<b>P/N or Comment</b>
C1, C2, C3	Capacitor, 0.001 $\mu$ F	Digi-Key	PCC1772CT-ND
C4	Capacitor, 10 $\mu$ F, 35 V, tant TE series	Digi-Key	PCS6106TR-ND, SMD
C5	Capacitor, 0.1 $\mu$ F	Digi-Key	P4525-ND
C6	Ceramic capacitor, 0.1 pF	ATC	100B 0R1
C7, C21	Variable capacitor, 0.4 – 2.5 pF	TEMEX USA	AT27280
C8, C9	Ceramic capacitor, 20 pF	ATC	100A 200
C10	Ceramic capacitor, 0.8 pF	ATC	100B 0R8
C11, C12	Ceramic capacitor, 8.2 pF	ATC	100B 8R2
C13, C14	Ceramic capacitor, 10 pF	ATC	100B 100
C15, C16	Ceramic capacitor, 1 $\mu$ F	ATC	920C105
C17, C18	Ceramic capacitor, 0.02 $\mu$ F	ATC	200B203
C19, C20	Tantalum capacitor, 10 $\mu$ F, 50 V	Digi-Key	P5182-ND
C22, C23	Ceramic capacitor, 12 pF	ATC	100B 120
Q1	Transistor	Infineon	BCP56
QQ1	Voltage regulator	National Semiconductor	LM7805
R1	Chip resistor, 1.2 k-ohms	Digi-Key	P1.2KGCT-ND
R2	Chip resistor, 1.3 k-ohms	Digi-Key	P1.3KGCT-ND
R3	Chip resistor, 2 k-ohms	Digi-Key	P2KECT-ND
R4	Potentiometer, 2 k-ohms	Digi-Key	3224W-202ETR-ND
R5, R8, R9	Chip resistor, 10 ohms	Digi-Key	P10ECT-ND
R6, R7	Chip resistor, 1 k-ohms	Digi-Key	P1KECT-ND

**Reference Circuit (cont.)**


Reference Circuit (not to scale)\*

\*Gerber Files for this circuit available on request

## Package Outline Specifications



Find the latest and most complete information about products and packaging at the Infineon Internet page  
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**PTFA212002E**

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**Revision History:**      **2005-05-16**

Preliminary Data Sheet

Previous Version:      2005-01-21, Preliminary Data Sheet

Page      Subjects (major changes since last revision)

3 – 8      Added performance and circuit information


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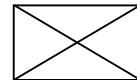
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