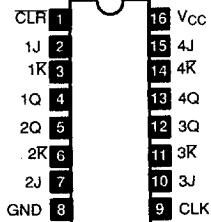


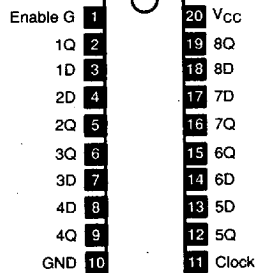
INTEGRATED CIRCUITS – TTL

(TRANSISTOR TRANSISTOR LOGIC)

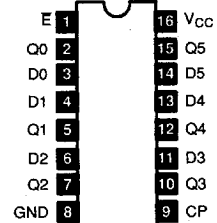
NTE74376 16-Lead DIP, See Diag. 249
Quadruple J-K Flip-Flop



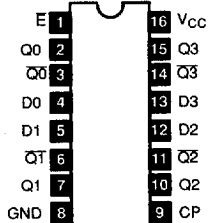
NTE74HC377, NTE74LS377 20-Lead DIP, See Diag. 294
Octal D-Type Flip-Flop w/Common Enable
w/Clock & Single Rail Outputs



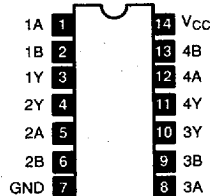
NTE74LS378 16-Lead DIP, See Diag. 249
Hex D-Type Flip-Flop
w/Clock Enable



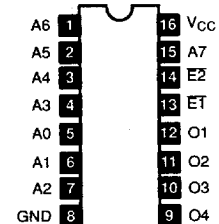
NTE74LS379 16-Lead DIP, See Diag. 249
Quad D-Type Flip-Flop



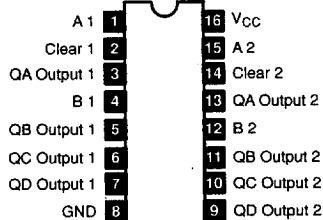
NTE74LS386 14-Lead DIP, See Diag. 247
Quad 2-Input Exclusive OR Gate



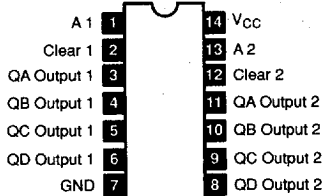
NTE74S387 16-Lead DIP, See Diag. 249
1024-Bit (256 x 4) Open Collector PROM,
50ns Max Address Access



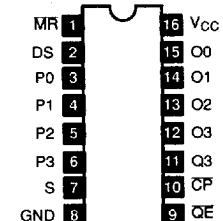
NTE74390, NTE74HC390, NTE74LS390 16-Lead DIP, See Diag. 249
Dual 4-Bit Decade Ripple Counter



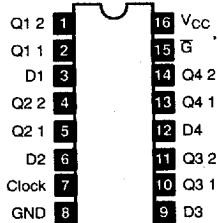
NTE74393, NTE74HC393, NTE74LS393 14-Lead DIP, See Diag. 247
Dual 4-Bit Binary Ripple Counter



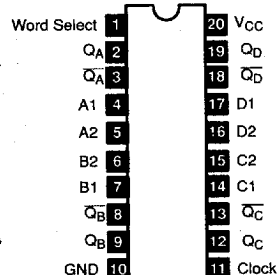
NTE74LS395A 16-Lead DIP, See Diag. 249
4-Bit Cascadable Shift Register
w/3-State Outputs



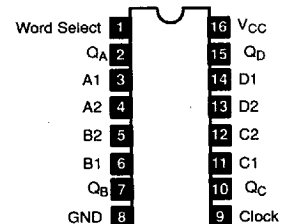
NTE74LS396 16-Lead DIP, See Diag. 249
Octal Storage Register w/Parallel Access



NTE74LS398 20-Lead DIP, See Diag. 294
Quad 2-Port Register



NTE74LS399 16-Lead DIP, See Diag. 249
Quad 2-Input Multiplexer w/Storage



See Diagrams, beginning on Page 1-293