

| REVISIONS  |  |                                     |                |   |   |                           |                   |    |   |   |   |    |    |    |    |    |
|--|--|-------------------------------------|----------------|---|---|---------------------------|-------------------|----|---|---|---|----|----|----|----|----|
| LTR  | DESCRIPTION  | DATE (YR-MO-DA)                     | APPROVED       |   |   |                           |                   |    |   |   |   |    |    |    |    |    |
| C  | Table I; for VT <sub>LOAD</sub> changed the min and max limits from -300 and +300 mV pk to -400 and +400 mV pk. For VT <sub>LINE</sub> changed the min and max limits from -300 and +300 mV pk to -500 and +500 mV pk. For V <sub>tonOS</sub> chagned the max limit from 50 to 120 mV pk. -sld | 98-10-13                            | K.A. Cottongim |   |   |                           |                   |    |   |   |   |    |    |    |    |    |
|  |  |                                     |                |   |   |                           |                   |    |   |   |   |    |    |    |    |    |
| REV  |  |                                     |                |   |   |                           |                   |    |   |   |   |    |    |    |    |    |
| SHEET  |  |                                     |                |   |   |                           |                   |    |   |   |   |    |    |    |    |    |
| REV  |  |                                     |                |   |   |                           |                   |    |   |   |   |    |    |    |    |    |
| SHEET  |  |                                     |                |   |   |                           |                   |    |   |   |   |    |    |    |    |    |
| REV STATUS OF SHEETS   |  | REV                                 | C              | C | C   | C                         | C                 | C  | C | C | C | C  | C  | C  | C  | C  |
|  |  | SHEET                               | 1              | 2 | 3   | 4                         | 5                 | 6  | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 |
| PMIC N/A   |  | PREPARED BY<br>Steve L. Duncan      |                |   | <b>DEFENSE SUPPLY CENTER COLUMBUS<br/>COLUMBUS, OHIO 43216-5000</b>       |                           |                   |    |   |   |   |    |    |    |    |    |
| <b>STANDARD<br/>MICROCIRCUIT<br/>DRAWING</b><br><br>THIS DRAWING IS AVAILABLE<br>FOR USE BY ALL<br>DEPARTMENTS<br>AND AGENCIES OF THE<br>DEPARTMENT OF DEFENSE<br><br>AMSC N/A |  | CHECKED BY<br>Michael Jones         |                |   | MICROCIRCUIT, HYBRID, LINEAR, 12-VOLT, SINGLE<br>CHANNEL, DC/DC CONVERTER |                           |                   |    |   |   |   |    |    |    |    |    |
|  |  | APPROVED BY<br>Kendall A. Cottongim |                |   |   |                           |                   |    |   |   |   |    |    |    |    |    |
|  |  | DRAWING APPROVAL DATE<br>94-05-13   |                |   | SIZE<br><b>A</b>  | CAGE CODE<br><b>67268</b> | <b>5962-93069</b> |    |   |   |   |    |    |    |    |    |
|  |  | REVISION LEVEL<br><br>C             |                |   | SHEET   | 1                         | OF                | 14 |   |   |   |    |    |    |    |    |

DSCC FORM 2233  
APR 97

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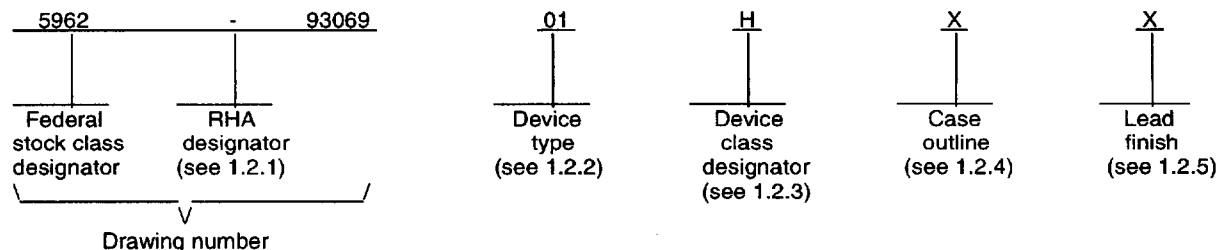
DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

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1. SCOPE

1.1 Scope. This drawing documents five product assurance classes, class D (lowest reliability), class E, (exceptions), class G (lowest high reliability), class H (high reliability), and class K, (highest reliability) and a choice of case outlines and lead finishes are available and are reflected in the Part or Identifying Number (PIN). When available, a choice of radiation hardness assurance levels are reflected in the PIN.

1.2 PIN. The PIN shall be as shown in the following example:



1.2.1 Radiation hardness assurance (RHA) designator. Device classes H and K RHA marked devices shall meet the MIL-PRF-38534 specified RHA levels and shall be marked with the appropriate RHA designator. A dash (-) indicates a non-RHA device.

1.2.2 Device type(s). The device type(s) shall identify the circuit function as follows:

| <u>Device type</u> | <u>Generic number</u>       | <u>Circuit function</u>            |
|--------------------|-----------------------------|------------------------------------|
| 01                 | MTR2812S/883, MTR2812SF/883 | DC/DC converter, 30 W, 12 V output |

1.2.3 Device class designator. This device class designator shall be a single letter identifying the product assurance level as follows:

| <u>Device class</u> | <u>Device performance documentation</u>          |
|---------------------|--|
| D, E, G, H, or K    | Certification and qualification to MIL-PRF-38534 |

1.2.4 Case outline(s). The case outline(s) shall be as designated in MIL-STD-1835 and as follows:

| <u>Outline letter</u> | <u>Descriptive designator</u> | <u>Terminals</u> | <u>Package style</u> |
|-----------------------|-------------------------------|------------------|----------------------|
| X                     | See figure 1                  | 10               | Dual-in-line         |
| Z                     | See figure 1                  | 10               | Flange mount         |

1.2.5 Lead finish. The lead finish shall be as specified in MIL-PRF-38534.

|   |                   |                             |                    |
|---|-------------------|-----------------------------|--------------------|
| <b>STANDARD<br/>MICROCIRCUIT DRAWING<br/>DEFENSE SUPPLY CENTER COLUMBUS<br/>COLUMBUS, OHIO 43216-5000</b> | <b>SIZE<br/>A</b> |                             | <b>5962-93069</b>  |
|   |                   | <b>REVISION LEVEL<br/>C</b> | <b>SHEET<br/>2</b> |

DSCC FORM 2234  
APR 97

9004708 0039201 12T

1.3 Absolute maximum ratings. 1/

Input voltage range . . . . . -0.5 V dc to +50 V dc  
 Power dissipation (P<sub>D</sub>) . . . . . 12 W  
 Output power . . . . . 30.9 W  
 Lead soldering temperature (10 seconds) . . . . . +300°C  
 Storage temperature range . . . . . -65°C to +150°C

1.4 Recommended operating conditions.

Input voltage range . . . . . +16 V dc to +40 V dc  
 Case operating temperature range (T<sub>C</sub>) . . . . . -55°C to +125°C

2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbook. The following specification, standards, and handbook form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those listed in the issue of the Department of Defense Index of Specifications and Standards (DoDISS) and supplement thereto, cited in the solicitation.

SPECIFICATION

DEPARTMENT OF DEFENSE

MIL-PRF-38534 - Hybrid Microcircuits, General Specification for.

STANDARDS

DEPARTMENT OF DEFENSE

MIL-STD-883 - Test Methods and Procedures for Microelectronics.  
 MIL-STD-973 - Configuration Management.  
 MIL-STD-1835 - Microcircuit Case Outlines.

HANDBOOK

DEPARTMENT OF DEFENSE

MIL-HDBK-780 - Standard Microcircuit Drawings.

(Unless otherwise indicated, copies of the specification, standards, and handbook are available from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

1/ Stresses above the absolute maximum rating may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.

|   |                  |                     |                   |
|---|------------------|---------------------|-------------------|
| STANDARD<br>MICROCIRCUIT DRAWING<br>DEFENSE SUPPLY CENTER COLUMBUS<br>COLUMBUS, OHIO 43216-5000 | SIZE<br><b>A</b> |                     | 5962-93069        |
|   |                  | REVISION LEVEL<br>C | SHEET<br><b>3</b> |

DSCC FORM 2234  
 APR 97

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**3. REQUIREMENTS**

**3.1 Item requirements.** The individual item performance requirements for device classes D, E, G, H, and K shall be in accordance with MIL-PRF-38534. Compliance with MIL-PRF-38534 may include the performance of all tests herein or as designated in the device manufacturer's Quality Management (QM) plan or as designated for the applicable device class. Therefore, the tests and inspections herein may not be performed for the applicable device class (see MIL-PRF-38534). Furthermore, the manufacturers may take exceptions or use alternate methods to the tests and inspections herein and not perform them. However, the performance requirements as defined in MIL-PRF-38534 shall be met for the applicable device class.

**3.2 Design, construction, and physical dimensions.** The design, construction, and physical dimensions shall be as specified in MIL-PRF-38534 and herein.

**3.2.1 Case outline(s).** The case outline(s) shall be in accordance with 1.2.4 herein and figure 1.

**3.2.2 Terminal connections.** The terminal connections shall be as specified on figure 2.

**3.3 Electrical performance characteristics.** Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full specified operating temperature range.

**3.4 Electrical test requirements.** The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are defined in table I.

**3.5 Marking of Device(s).** Marking of device(s) shall be in accordance with MIL-PRF-38534. The device shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's vendor similar PIN may also be marked as listed in QML-38534.

**3.6 Data.** In addition to the general performance requirements of MIL-PRF-38534, the manufacturer of the device described herein shall maintain the electrical test data (variables format) from the initial quality conformance inspection group A lot sample, for each device type listed herein. Also, the data should include a summary of all parameters manually tested, and for those which, if any, are guaranteed. This data shall be maintained under document revision level control by the manufacturer and be made available to the preparing activity (DSCC-VA) upon request.

**3.7 Certificate of compliance.** A certificate of compliance shall be required from a manufacturer in order to supply to this drawing. The certificate of compliance (original copy) submitted to DSCC-VA shall affirm that the manufacturer's product meets the performance requirements of MIL-PRF-38534 and herein.

**3.8 Certificate of conformance.** A certificate of conformance as required in MIL-PRF-38534 shall be provided with each lot of microcircuits delivered to this drawing.

**4. QUALITY ASSURANCE PROVISIONS**

**4.1 Sampling and inspection.** Sampling and inspection procedures shall be in accordance with MIL-PRF-38534 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein.

|   |                   |                             |                    |
|---|-------------------|-----------------------------|--------------------|
| <b>STANDARD<br/>MICROCIRCUIT DRAWING<br/>DEFENSE SUPPLY CENTER COLUMBUS<br/>COLUMBUS, OHIO 43216-5000</b> | <b>SIZE<br/>A</b> |                             | <b>5962-93069</b>  |
|   |                   | <b>REVISION LEVEL<br/>C</b> | <b>SHEET<br/>4</b> |

DSCC FORM 2234  
APR 97

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TABLE I. Electrical performance characteristics.

| Test                  | Symbol             | Conditions<br>-55°C ≤ T <sub>C</sub> ≤ +125°C<br>V <sub>IN</sub> = 28 V dc ± 0.5 V dc<br>no external sync, C <sub>L</sub> = 0<br>unless otherwise specified | Group A<br>subgroups | Device<br>type | Limits |       | Unit   |
|-----------------------|--------------------|---|----------------------|----------------|--------|-------|--------|
|                       |                    |   |                      |                | Min    | Max   |        |
| Output voltage        | V <sub>OUT</sub>   | I <sub>OUT</sub> = 2.5 A dc   | 1                    | 01             | 11.88  | 12.12 | V dc   |
|                       |                    |   | 2, 3                 |                | 11.64  | 12.36 |        |
| Output current        | I <sub>OUT</sub>   | V <sub>IN</sub> = 16 V dc, 28 V dc,<br>and 40 V dc  | 1, 2, 3              |                | 0.0    | 2500  | mA     |
| Output ripple voltage | V <sub>RIP</sub>   | I <sub>OUT</sub> = 2.5 A,<br>B.W. = 10 kHz to 2 MHz   | 1                    |                |        | 40    | mV p-p |
|                       |                    |   | 2, 3                 |                |        | 90    |        |
| Line regulation       | V <sub>RLINE</sub> | V <sub>IN</sub> = 16 V dc to 40 V dc,<br>I <sub>OUT</sub> = 2.5 A   | 1, 2, 3              |                |        | 50    | mV     |
| Load regulation       | V <sub>RLOAD</sub> | I <sub>OUT</sub> = 0 to 2.5 A   | 1, 2, 3              |                |        | 50    |        |
| Input current         | I <sub>IN</sub>    | I <sub>OUT</sub> = 0, inhibit<br>(pin 2) = 0  | 1, 2, 3              |                |        | 8     | mA     |
|                       |                    | I <sub>OUT</sub> = 0, inhibit (pin 2)<br>open   |                      |                |        | 75    |        |
| Input ripple current  | I <sub>RIP</sub>   | I <sub>OUT</sub> = 2.5 A,<br>B.W. = 10 kHz to 10 MHz  | 1, 2, 3              |                |        | 50    | mA p-p |
| Efficiency            | Eff                | I <sub>OUT</sub> = 2.5 A  | 1                    |                | 80     |       | %      |
|                       |                    |   | 2, 3                 |                | 77     |       |        |
| Isolation             | ISO                | Input to output or any pin<br>to case (except pins 7 and<br>8) at 500 V dc  | 1                    |                | 100    |       | MΩ     |
| Capacitive load 1/ 2/ | C <sub>L</sub>     | No effect on dc performance   | 4                    |                |        | 3000  | μF     |

See footnotes at end of table.

STANDARD  
MICROCIRCUIT DRAWING  
DEFENSE SUPPLY CENTER COLUMBUS  
COLUMBUS, OHIO 43216-5000

SIZE  
A

5962-93069

REVISION LEVEL  
C

SHEET  
5

TABLE I. Electrical performance characteristics - Continued.

| Test  | Symbol             | Conditions<br>-55°C ≤ T <sub>C</sub> ≤ +125°C<br>V <sub>IN</sub> = 28 V dc ±.5 V dc<br>no external sync, C <sub>L</sub> = 0<br>unless otherwise specified | Group A<br>subgroups | Device<br>type | Limits |      | Unit  |
|---|--------------------|---|----------------------|----------------|--------|------|-------|
|   |                    |   |                      |                | Min    | Max  |       |
| Power dissipation,<br>load fault                                      | P <sub>D</sub>     | Short circuit   | 1                    | 01             |        | 10   | W     |
|   |                    |   | 2, 3                 |                |        | 12   |       |
| Switching frequency   | F <sub>S</sub>     | I <sub>OUT</sub> = 2.5 A  | 4, 5, 6              |                | 550    | 650  | kHz   |
| External sync range <sup>3/</sup>                                     | F <sub>SYNC</sub>  | I <sub>OUT</sub> = 2.5 A, TTL level to<br>pin 9   | 4, 5, 6              |                | 500    | 675  | kHz   |
| Output response to step<br>transient load<br>changes <sup>4/</sup>    | V <sub>TLOAD</sub> | 50 percent load to/from<br>100 percent load   |                      |                | -400   | +400 | mV pk |
| Recovery time, step<br>transient load<br>changes <sup>2/ 4/ 5/</sup>  | T <sub>TLOAD</sub> | 50 percent load to/from<br>100 percent load   |                      |                |        | 200  | μs    |
| Output response to<br>transient step line<br>changes <sup>2/ 6/</sup> | V <sub>TLINE</sub> | Input step from 16 V dc<br>to 40 V dc, I <sub>OUT</sub> = 2.5 A   |                      |                | -500   | +500 | mV pk |
|   |                    | Input step from 40 V dc to<br>16 V dc, I <sub>OUT</sub> = 2.5 A   |                      |                | -500   | +500 |       |
| Recovery time,<br>transient step line<br>changes <sup>2/ 5/</sup>     | T <sub>TLINE</sub> | Input step from 16 V dc to<br>40 V dc, I <sub>OUT</sub> = 2.5 A   |                      |                |        | 300  | μs    |
|   |                    | Input step from 40 V dc to<br>16 V dc, I <sub>OUT</sub> = 2.5 A   |                      |                |        | 300  |       |
| Turn-on overshoot <sup>2/</sup>                                       | V <sub>tonOS</sub> | I <sub>OUT</sub> = 2.5 A  |                      |                |        | 120  | mV pk |
| Turn-on delay <sup>7/</sup>   | T <sub>onD</sub>   | I <sub>OUT</sub> = 2.5 A  |                      |                |        | 5    | ms    |
| Load fault recovery<br><sup>2/</sup>                                  | T <sub>rLF</sub>   | I <sub>OUT</sub> = 2.5 A  |                      |                |        | 5    |       |

See footnotes at top of next page.

|   |                   |                     |                   |
|---|-------------------|---------------------|-------------------|
| <b>STANDARD<br/>MICROCIRCUIT DRAWING<br/>DEFENSE SUPPLY CENTER COLUMBUS<br/>COLUMBUS, OHIO 43216-5000</b> | <b>SIZE<br/>A</b> |                     | <b>5962-93069</b> |
|   |                   | REVISION LEVEL<br>C | SHEET<br>6        |

DSCC FORM 2234  
APR 97

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TABLE I. Electrical performance characteristics - Continued.

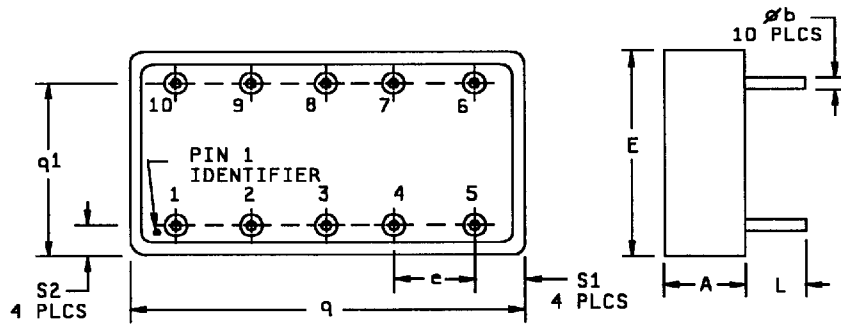
- 1/ Capacitive load may be any value from 0 to the maximum limit without compromising dc performance.
- 2/ Parameter shall be tested as part of design characterization and after design or process changes; therefore, the parameter shall be guaranteed to the limits specified in table I.
- 3/ A TTL level waveform ( $V_{IH} = 4.5$  V minimum,  $V_{IL} = .8$  V maximum) with a 50 percent  $\pm 10$  percent duty cycle applied to sync input pin (pin 9) within the sync range frequency shall cause the converter's switching frequency to become synchronous with the frequency applied to the sync input pin (pin 9).
- 4/ Load step transition time 10 microseconds maximum.
- 5/ Recovery time is measured from the initiation of the transient to where  $V_{OUT}$  has returned to within  $\pm 1$  percent of  $V_{OUT}$  final value.
- 6/ Input step transition time 10 and 50 microseconds.
- 7/ Turn-on delay time measurement is either for a step application of power at the input or the removal of a ground signal from the inhibit pin (pin 2) while power is applied to the input.

|   |                   |                             |                    |
|---|-------------------|-----------------------------|--------------------|
| <b>STANDARD<br/>MICROCIRCUIT DRAWING<br/>DEFENSE SUPPLY CENTER COLUMBUS<br/>COLUMBUS, OHIO 43216-5000</b> | <b>SIZE<br/>A</b> |                             | <b>5962-93069</b>  |
|   |                   | <b>REVISION LEVEL<br/>C</b> | <b>SHEET<br/>7</b> |

DSCC FORM 2234  
APR 97

■ 9004708 0039206 701 ■

Case X



| Symbol | Millimeters |       | Inches   |       |
|--------|-------------|-------|----------|-------|
|        | Min         | Max   | Min      | Max   |
| A      |             | 10.16 |          | .400  |
| φb     | 0.89        | 1.14  | .035     | .045  |
| e      | 10.16 BSC   |       | .400 BSC |       |
| E      | 28.07       | 28.32 | 1.105    | 1.115 |
| L      | 6.09        | 6.60  | .240     | .260  |
| q      |             | 53.21 |          | 2.095 |
| q1     | 24.26 BSC   |       | .955 BSC |       |
| S1     | 6.22 BSC    |       | .245 BSC |       |
| S2     | 3.94 BSC    |       | .155 BSC |       |

NOTES:

1. The case outline X was originally designed using the inch-pound units of measurement, in the event of conflict between the metric and inch-pound units, the inch-pound units shall take precedence.
2. Device weight: 52 grams.

FIGURE 1. Case outline(s).

|   |                   |                     |                   |
|---|-------------------|---------------------|-------------------|
| <b>STANDARD<br/>MICROCIRCUIT DRAWING<br/>DEFENSE SUPPLY CENTER COLUMBUS<br/>COLUMBUS, OHIO 43216-5000</b> | <b>SIZE<br/>A</b> |                     | <b>5962-93069</b> |
|   |                   | REVISION LEVEL<br>C | SHEET<br>8        |

DSCC FORM 2234  
APR 97

9004708 0039207 648



Case Z

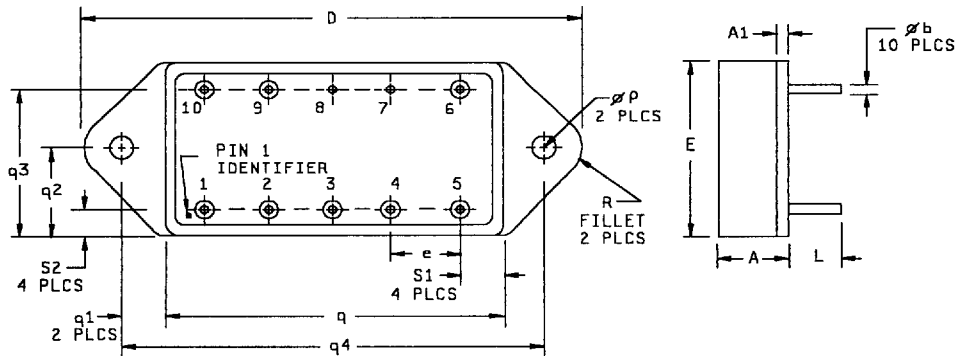


FIGURE 1. Case outline(s) - Continued.

STANDARD  
MICROCIRCUIT DRAWING  
DEFENSE SUPPLY CENTER COLUMBUS  
COLUMBUS, OHIO 43216-5000

SIZE  
A

5962-93069

REVISION LEVEL  
C

SHEET  
9

DSCC FORM 2234  
APR 97

■ 9004708 0039208 584 ■

Case Z - Continued

| Symbol   | Millimeters |       | Inches    |       |
|----------|-------------|-------|-----------|-------|
|          | Min         | Max   | Min       | Max   |
| A        |             | 10.16 |           | .400  |
| A1       | 1.27        | 1.78  | .05       | .07   |
| $\phi b$ | 0.89        | 1.14  | .035      | .045  |
| D        |             | 73.66 |           | 2.900 |
| e        | 10.16 BSC   |       | .400 BSC  |       |
| E        | 28.07       | 28.32 | 1.105     | 1.115 |
| L        | 6.09        | 6.60  | .240      | .260  |
| $\phi p$ | 3.99        | 4.19  | .157      | .165  |
| q        | 53.08 BSC   |       | 2.090 BSC |       |
| q1       | 5.84 BSC    |       | .230 BSC  |       |
| q2       | 14.10 BSC   |       | .555 BSC  |       |
| q3       | 24.26 BSC   |       | .955 BSC  |       |
| q4       | 64.52       | 65.02 | 2.54      | 2.56  |
| R        | 4.19        | 4.44  | .165      | .175  |
| S1       | 6.22 BSC    |       | .245 BSC  |       |
| S2       | 3.94 BSC    |       | .155 BSC  |       |

NOTES:

1. The case outline Z was originally designed using the inch-pound units of measurement, in the event of conflict between the metric and inch-pound units, the inch-pound units shall take precedence.
2. Unless otherwise specified, the tolerance is  $\pm 0.01$  for two place decimals and  $\pm 0.005$  for three place decimals.
3. Device weight: 55 grams.

FIGURE 1. Case outline(s) - Continued.

|   |                  |                            |                    |
|---|------------------|----------------------------|--------------------|
| <b>STANDARD<br/>MICROCIRCUIT DRAWING<br/>DEFENSE SUPPLY CENTER COLUMBUS<br/>COLUMBUS, OHIO 43216-5000</b> | SIZE<br><b>A</b> |                            | <b>5962-93069</b>  |
|   |                  | REVISION LEVEL<br><b>C</b> | SHEET<br><b>10</b> |

DSCC FORM 2234  
APR 97

■ 9004708 0039209 410 ■

|                 |                 |
|-----------------|-----------------|
| Device type     | 01              |
| Case outlines   | X and Z         |
| Terminal number | Terminal symbol |
| 1               | Positive input  |
| 2               | Inhibit         |
| 3               | Sense return    |
| 4               | Output common   |
| 5               | Positive output |
| 6               | Positive sense  |
| 7               | Case ground     |
| 8               | Case ground     |
| 9               | Sync input      |
| 10              | Input common    |

FIGURE 2. Terminal connections.

|   |                   |                             |                     |
|---|-------------------|-----------------------------|---------------------|
| <b>STANDARD<br/>MICROCIRCUIT DRAWING<br/>DEFENSE SUPPLY CENTER COLUMBUS<br/>COLUMBUS, OHIO 43216-5000</b> | <b>SIZE<br/>A</b> |                             | <b>5962-93069</b>   |
|   |                   | <b>REVISION LEVEL<br/>C</b> | <b>SHEET<br/>11</b> |

DSCC FORM 2234  
APR 97

■ 9004708 0039210 132 ■

TABLE II. Electrical test requirements.

|  |   |
|--|---|
| MIL-PRF-38534 test requirements  | Subgroups<br>(in accordance with<br>MIL-PRF-38534, group<br>A test table) |
| Interim electrical parameters  |   |
| Final electrical parameters  | 1*, 2, 3, 4, 5, 6   |
| Group A test requirements  | 1, 2, 3, 4, 5, 6  |
| Group C end-point electrical<br>parameters                                 | 1   |
| MIL-STD-883, group E end-point<br>electrical parameters for RHA<br>devices | Subgroups**<br>(in accordance with<br>method 5005, group A<br>test table) |

\* PDA applies to subgroup 1.

\*\* When applicable to this standard microcircuit drawing,  
the subgroups shall be defined.

4.2 Screening. Screening shall be in accordance with MIL-PRF-38534. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DSCC-VA or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.

(2)  $T_A$  as specified in accordance with table I of method 1015 of MIL-STD-883.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Conformance and periodic inspections. Conformance inspection (CI) and periodic inspection (PI) shall be in accordance with MIL-PRF-38534 and as specified herein.

4.3.1 Group A inspection (CI). Group A inspection shall be in accordance with MIL-PRF-38534 and as follows:

a. Tests shall be as specified in table II herein.

b. Subgroups 7, 8, 9, 10, and 11 shall be omitted.

4.3.2 Group B inspection (PI). Group B inspection shall be in accordance with MIL-PRF-38534.

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|---|-------------------|-----------------------------|---------------------|
| <b>STANDARD<br/>MICROCIRCUIT DRAWING<br/>DEFENSE SUPPLY CENTER COLUMBUS<br/>COLUMBUS, OHIO 43216-5000</b> | <b>SIZE<br/>A</b> |                             | <b>5962-93069</b>   |
|   |                   | <b>REVISION LEVEL<br/>C</b> | <b>SHEET<br/>12</b> |

DSCC FORM 2234  
APR 97

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4.3.3 Group C inspection (PI). Group C inspection shall be in accordance with MIL-PRF-38534 and as follows:

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test, method 1005 of MIL-STD-883.
  - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DSCC-VA or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.
  - (2)  $T_A$  as specified in accordance with table I of method 1005 of MIL-STD-883.
  - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

4.3.4 Group D inspection (PI). Group D inspection shall be in accordance with MIL-PRF-38534.

4.3.5 Group E inspection. Group E inspection is required only for parts intended to be marked as radiation hardness assured (see 3.5 herein). RHA levels shall be M, D, R, and H. RHA quality conformance inspection sample tests shall be performed at the RHA level specified in the acquisition document.

- a. RHA tests for levels M, D, R, and H shall be performed through each level to determine at what levels the devices meet the RHA requirements. These RHA tests shall be performed for initial qualification and after design or process changes which may affect the RHA performance of the device.
- b. End-point electrical parameters shall be as specified in table II herein.
- c. Prior to total dose irradiation, each selected sample shall be assembled in its qualified package. It shall pass the specified group A electrical parameters in table I for subgroups specified in table II herein.
- d. The devices shall be subjected to radiation hardness assured tests as specified in MIL-PRF-38534 for RHA level being tested, and meet the postirradiation end-point electrical parameter limits as defined in table I at  $T_A = +25^\circ\text{C} \pm 5$  percent, after exposure.
- e. Prior to and during total dose irradiation testing, the devices shall be biased to establish a worst case condition as specified in the radiation exposure circuit.
- f. For device classes H and K, subgroups 1 and 2 in table V, method 5005 of MIL-STD-883 shall be tested as appropriate for device construction.
- g. When specified in the purchase order or contract, a copy of the RHA delta limits shall be supplied.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38534.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-973 using DD Form 1692, Engineering Change Proposal.

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|---|-------------------|-----------------------------|---------------------|
| <b>STANDARD<br/>MICROCIRCUIT DRAWING<br/>DEFENSE SUPPLY CENTER COLUMBUS<br/>COLUMBUS, OHIO 43216-5000</b> | <b>SIZE<br/>A</b> |                             | <b>5962-93069</b>   |
|   |                   | <b>REVISION LEVEL<br/>C</b> | <b>SHEET<br/>13</b> |

DSCC FORM 2234  
APR 97

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6.4 Record of users. Military and industrial users shall inform Defense Supply Center Columbus when a system application requires configuration control and the applicable SMD. DSCC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DSCC-VA, telephone (614) 692-0526.

6.5 Comments. Comments on this drawing should be directed to DSCC-VA, Columbus, Ohio 43216-5000, or telephone (614) 692-0512.

6.6 Sources of supply. Sources of supply are listed in QML-38534. The vendors listed in QML-38534 have submitted a certificate of compliance (see 3.7 herein) to DSCC-VA and have agreed to this drawing.

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| <b>STANDARD<br/>MICROCIRCUIT DRAWING<br/>DEFENSE SUPPLY CENTER COLUMBUS<br/>COLUMBUS, OHIO 43216-5000</b> | <b>SIZE<br/>A</b> |                             | <b>5962-93069</b>   |
|   |                   | <b>REVISION LEVEL<br/>C</b> | <b>SHEET<br/>14</b> |

DSCC FORM 2234  
APR 97

■ 9004708 0039213 941 ■

STANDARD MICROCIRCUIT DRAWING SOURCE APPROVAL BULLETIN

DATE: 98-10-13

Approved sources of supply for SMD 5962-93069 are listed below for immediate acquisition only and shall be added to QML-38534 during the next revision. QML-38534 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DSCC-VA. This bulletin is superseded by the next dated revision of QML-38534.

| Standard microcircuit drawing<br>PIN <u>1/</u> | Vendor CAGE number | Vendor similar PIN <u>2/</u>   |
|--|--------------------|--------------------------------|
| 5962-9306901HXA<br>5962-9306901HXC             | 50821<br>50821     | MTR2812S/883<br>MTR2812S/883   |
| 5962-9306901HZA<br>5962-9306901HXC             | 50821<br>50821     | MTR2812SF/883<br>MTR2812SF/883 |

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the vendor to determine its availability.
- 2/ **Caution.** Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE  
number

50821

Vendor name  
and address

Interpoint Corporation  
10301 Willows Road  
Redmond, WA 98073-9705

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in this information bulletin.

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