

# **Powerline N-Channel IGBT**

## Advance Information

DS5054-1.2 January 1999

The ITZ25F12 is a very robust non punch through nchannel, enhancement mode insulated gate bipolar transistor (IGBT) designed for low power dissipation in a wide range of high voltage applications such as power supplies and motor drives. The high impedance gate simplifies gate drive considerations, allowing operation directly from low power control circuitry.

The device is optimised for high frequency operation with fast rise and fall times. The product is suitable for modern systems employing switching frequencies up to and beyond the audible spectrum.

Low saturation voltages minimise power dissipation, thereby reducing the cost of the overall system in which they are used.

The ITZ is fully short circuit rated making it especially suited for motor control and other applications requiring short circuit withstand capability. Each device in the Powerline range is available with or without an integral anti-parallel ultrafast soft recovery diode, see separate datasheet for co-pack device.

Typical applications include high frequency inverters for motor control, pwm, welding and heating apparatus. The Powerline range of IGBTs is also applicable to switched mode (SMPS) and uninterruptible power supplies (UPS).

#### **FEATURES**

- Enhancement Mode n-Channel Device
- Non Punch Through Structure
- High Switching Speed
- Low On-state Saturation Voltage
- High Input Impedance Simplifies Gate Drive
- Fully Short Circuit Rated To 10µs
- Square RBSOA

#### **APPLICATIONS**

- High Frequency Inverters
- Motor Control
- Switched Mode Power Supplies
- High Frequency Welding
- UPS Systems
- PWM Drives

#### **KEY PARAMETERS**

V <sub>CES</sub>	(max)	1200V
V CES V CE(sat)	(typ)	2.8V
I <sub>C25</sub>	(max)	40A
I <sub>C85</sub>	(max)	25A
I <sub>CM</sub>	(max)	75A
t	(max)	<b>10</b> μ <b>s</b>

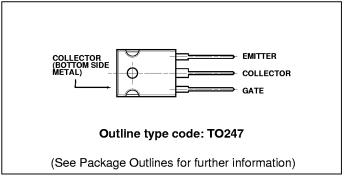


Fig.1 Pin connections - top view (not to scale)

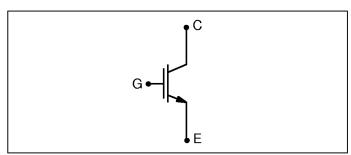


Fig.2 ITZ25F12 circuit

### ORDERING INFORMATION

ITZ25F12P TO247

Note: When ordering, use the complete part number.

Caution: These devices are sensitive to electrostatic discharge. Users should observe proper ESD handling precautions.

## **ITZ25F12**

### **ABSOLUTE MAXIMUM RATINGS**

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device.

T<sub>case</sub> = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Max.	Units
V <sub>CES</sub>	Collector-emitter voltage	$V_{GE} = 0V$	1200	V
V <sub>GES</sub>	Gate-emitter voltage	-	±20	V
I <sub>C25</sub>	Continuous collector current	T <sub>case</sub> = 25°C	40	Α
I <sub>C85</sub>	Continuous collector current	$T_{case} = 85^{\circ}C$	25	Α
I <sub>CM</sub>	Pulsed collector current	1ms, T <sub>case</sub> = 85°C	75	Α
P <sub>tot</sub>	Power dissipation	$T_{case} = 85^{\circ}C$	100	w

### THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Conditions	Min.	Max.	Units
R <sub>th(j-c)</sub>	Thermal resistance	DC junction to case	-	0.65	°C/W
T <sub>OP</sub>	Operating junction temperature range	-	-40	150	°C
T <sub>stg</sub>	Storage temperature range	-	-40	150	°C
-	Mounting torque	M3 screw	-	1.1	Nm

### DC ELECTRICAL CHARACTERISTICS

T<sub>case</sub> = 25°C unless stated otherwise.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
I <sub>CES</sub>	Collector cut-off current	$V_{GE} = 0V, V_{CE} = 1200V$	ı	-	1	mA
I <sub>GES</sub>	Gate leakage current	$V_{GE} = 20V, V_{CE} = 0V$	-	-	±500	nA
V <sub>GE(TH)</sub>	Gate threshold voltage	$I_{\rm C}$ = 1mA, $V_{\rm CE}$ = $V_{\rm GE}$	4.5	6	7.5	V
V <sub>CE(SAT)</sub>	Collector-emitter saturation voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 25A	-	2.8	3.3	V
		V <sub>GE</sub> = 15V, I <sub>C</sub> = 25A, T <sub>j</sub> = 125°C	-	3.4	-	V

### **AC ELECTRICAL CHARACTERISTICS**

 $T_{case} = 25$ °C unless stated otherwise.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 15V, f = 1MHz	-	4385	-	рF
C <sub>oes</sub>	Output capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 15V, f = 1MHz	-	325	-	рF
C <sub>res</sub>	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 15V, f = 1MHz$	ı	65	-	рF

## **INDUCTIVE SWITCHING CHARACTERISTICS - see figures 3 to 5**

 $T_{case} = 25$ °C unless stated otherwise.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
t <sub>d(ON)</sub>	Turn-on delay time		-	90	-	ns
t <sub>r</sub>	Rise time	J 05A	-	35	-	ns
E <sub>on</sub>	Turn-on energy loss - per cycle	$I_{c} = 25A,$ $V_{GE} = \pm 15V,$	-	2.2	-	mJ
t <sub>d(OFF)</sub>	Turn-off delay time	$V_{CE} = 50\%V_{ces}$	-	190	-	ns
t <sub>f</sub>	Fall time	$R_{G(ON)} = 20\Omega,$ $R_{G(OFF)} = 5\Omega$	-	60	-	ns
E <sub>OFF</sub>	Turn-off energy loss - per cycle	J	-	2.5	-	mJ
T <sub>case</sub> = 125	°C unless stated otherwise.		•	•	•	
t <sub>d(ON)</sub>	Turn-on delay time		-	90	-	ns
t <sub>r</sub>	Rise time	J 25A	-	40	-	ns
E <sub>on</sub>	Turn-on energy loss - per cycle	$I_{C} = 25A,$ $V_{GE} = \pm 15V,$ $V_{CE} = 50\%V_{ces}$ $R_{G(ON)} = 20\Omega,$ $R_{G(OFF)} = 5\Omega$	-	3.5	-	mJ
t <sub>d(OFF)</sub>	Turn-off delay time		-	210	-	ns
t <sub>f</sub>	Fall time		-	80	-	ns
E <sub>OFF</sub>	Turn-off energy loss - per cycle		-	3.1	-	mJ

For additional switching information please refer to figures 8 to 13.

## **SHORT CIRCUIT RATING**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
t <sub>sc</sub>	Short circuit withstand time	$T_c = 125^{\circ}C, V_{GE} = 15V, V_{CE} = 80\% V_{CES}$	-	-	10	μs

### **ITZ25F12**

### **BASIC TEST CIRCUIT AND SWITCHING DEFINITIONS**

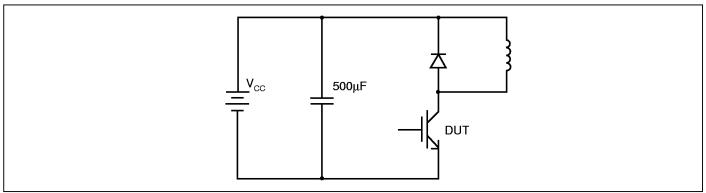


Fig.3 Basic d.c. chopper circuit

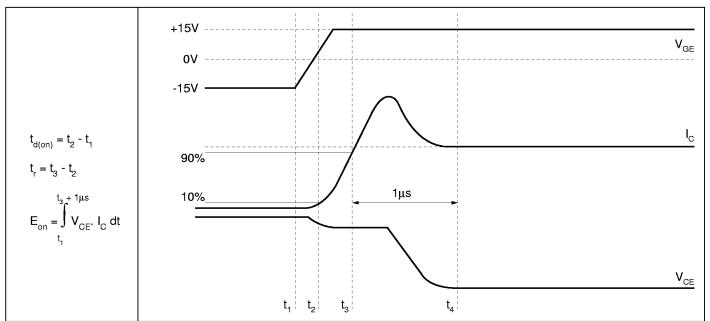


Fig.4 Turn-on characteristics

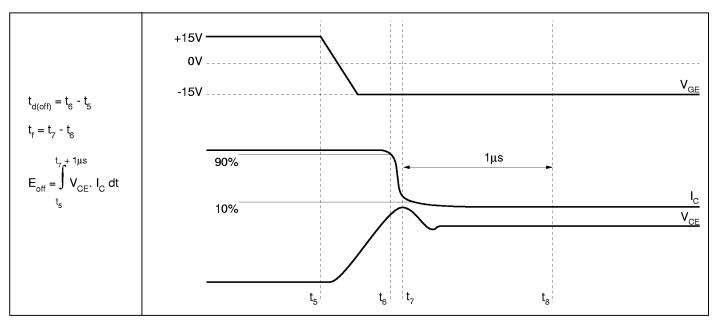
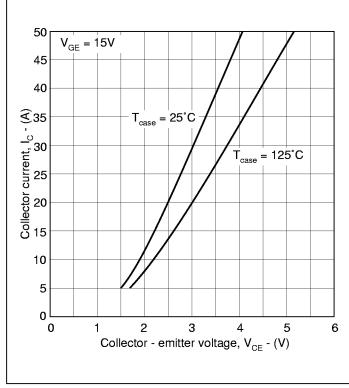


Fig.5 Turn-off characteristics

### **CURVES**



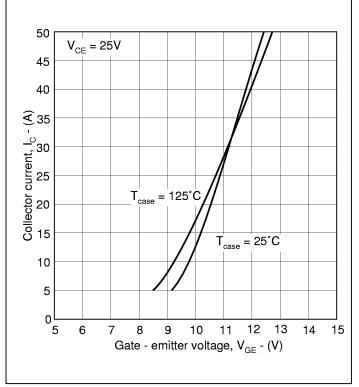
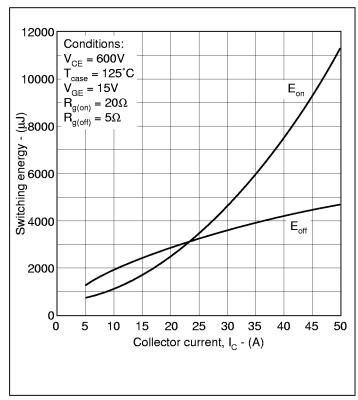
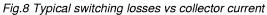


Fig.6 Typical output characteristics







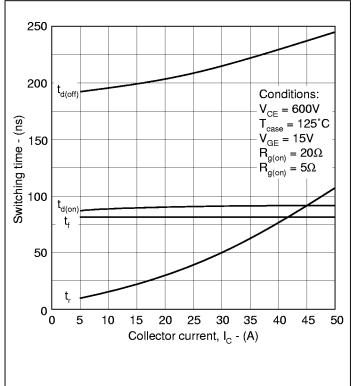
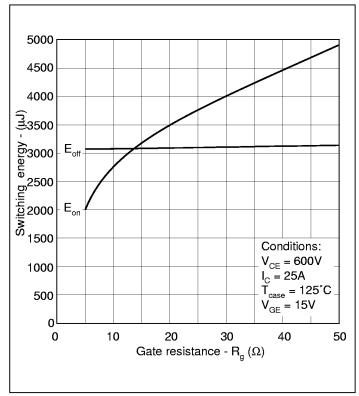


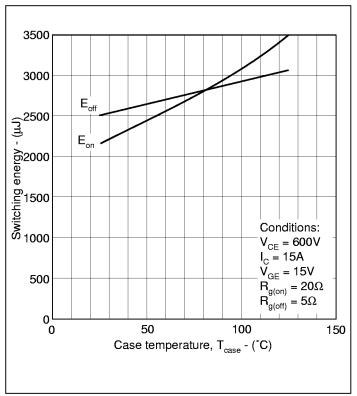
Fig.9 Typical switching times vs collector current

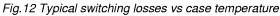


600 Conditions: V<sub>CE</sub> = 600V  $I_C = 25A$   $T_{case} = 125^{\circ}C$ 500  $V_{GE}^{GE} = 15V$ Switching time - (ns) 300 200  $\mathsf{t}_{\mathsf{d}(\mathsf{off})}$ 100 t<sub>d(on)</sub> 0 0 10 20 30 40 50 Gate resistance -  $R_{q}$  ( $\Omega$ )

Fig.10 Typical switching losses vs gate resistance

Fig.11 Typical switching times vs gate resistance





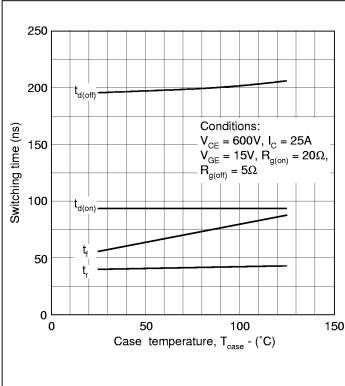
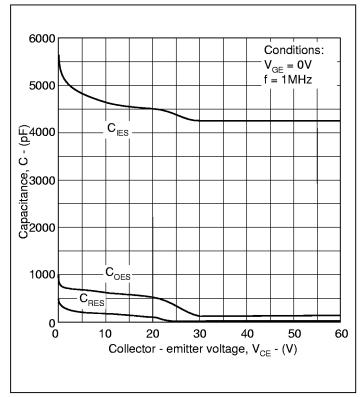


Fig.13 Typical switching time vs case temperature



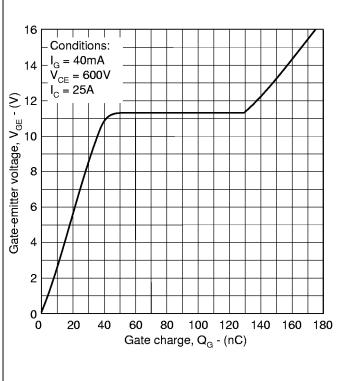


Fig.14 Typical capacitance



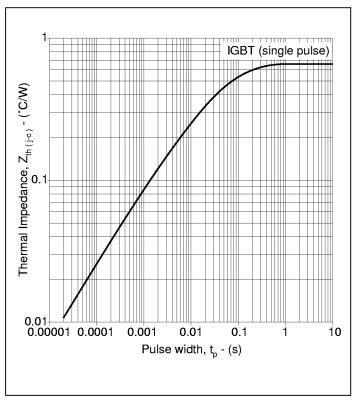


Fig.16 Transient thermal impedance - junction to case

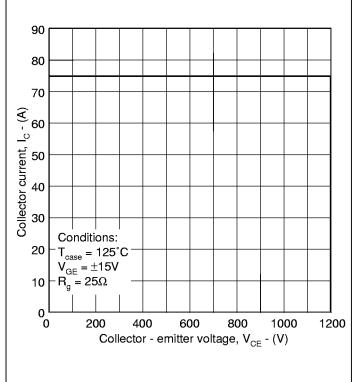
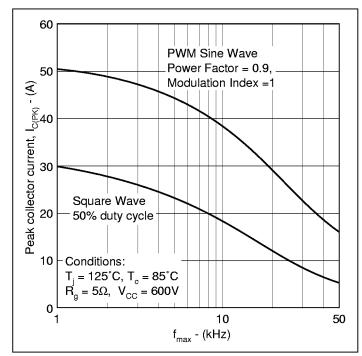


Fig.17 Reverse bias safe operating area

## ITZ25F12



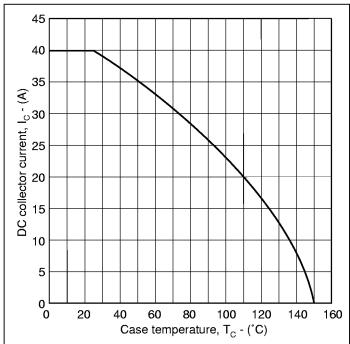
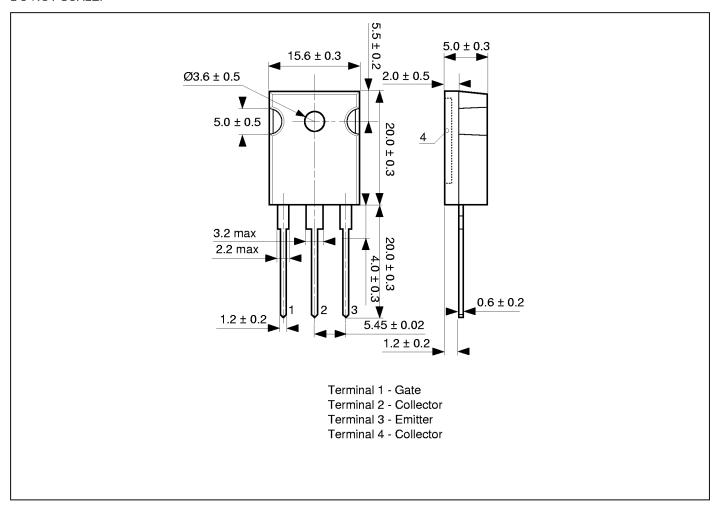


Fig. 18 Three Phase PWM inverter operating frequency

Fig.19 DC current rating vs case temperature

#### **PACKAGE OUTLINE - TO247**

For further package information, please contact your local Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.





**SEMICONDUCTOR** 

## HEADQUARTERS POWER OPERATIONS

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**ADVANCE INFORMATION** - The product design is complete and final characterisation for volume production is well in hand. These are supported by agents and distributors in major countries world-wide.

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