

## Silicon PNP Power Transistors

2SB506

## DESCRIPTION

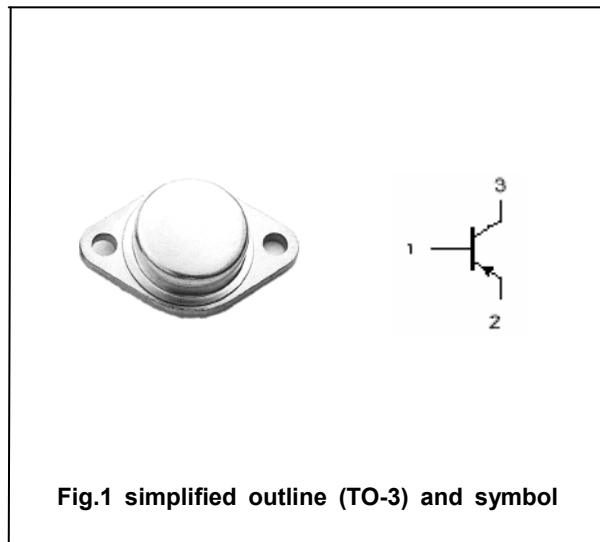
- With TO-3 package
- Wide area of safe operation

## APPLICATIONS

- Low frequency power amplification
- Power switching application

## PINNING(see Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

Absolute maximum ratings( $T_a = \square$ )

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$V_{CBO}$	Collector-base voltage	Open emitter	-150	V
$V_{CEO}$	Collector-emitter voltage	Open base	-100	V
$V_{EBO}$	Emitter-base voltage	Open collector	-7	V
$I_C$	Collector current		-5	A
$P_C$	Collector power dissipation	$T_C = 25 \square$	60	W
$T_j$	Junction temperature		100	$\square$
$T_{stg}$	Storage temperature		-55~100	$\square$

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## CHARACTERISTICS

T<sub>j</sub>=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> =-10mA; I <sub>B</sub> =0	-100			V
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage	I <sub>E</sub> =-1mA; I <sub>C</sub> =0	-7			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =-5A; I <sub>B</sub> =-0.5A			-1.0	V
V <sub>BEsat</sub>	Base-emitter saturation voltage	I <sub>C</sub> =-5A; I <sub>B</sub> =-0.5A			-1.5	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =-100V; I <sub>E</sub> =0			-0.1	mA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =-7V; I <sub>C</sub> =0			-0.1	mA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> =-1A ; V <sub>CE</sub> =-5V	35		200	
f <sub>T</sub>	Transition frequency	I <sub>C</sub> =-0.3A ; V <sub>CE</sub> =-10V		20		MHz

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PACKAGE OUTLINE

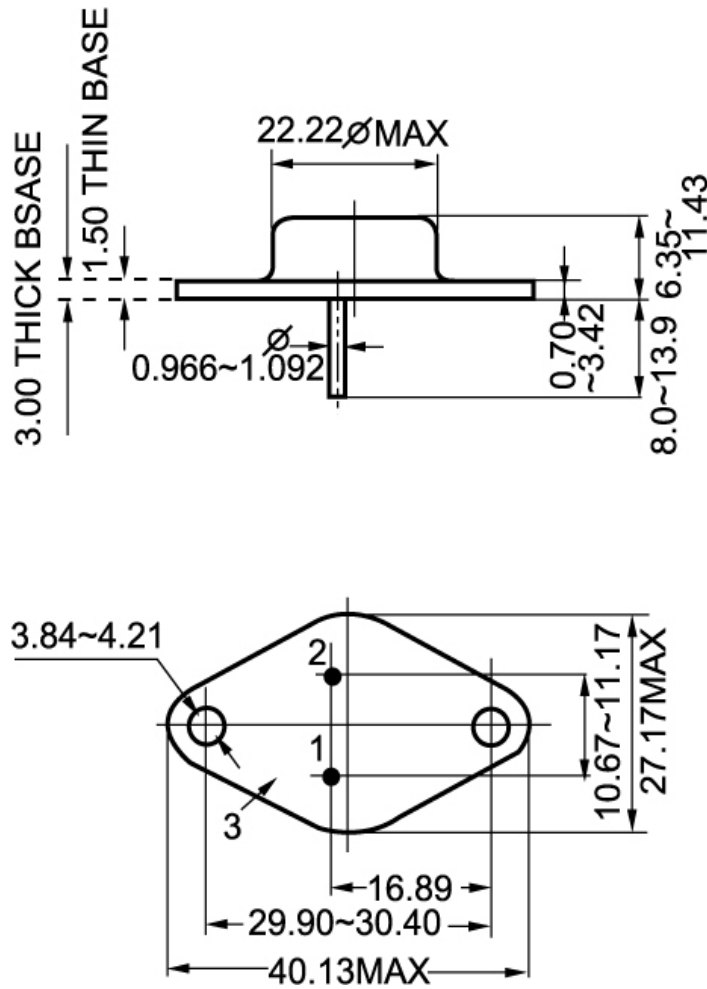


Fig.2 outline dimensions (unindicated tolerance:±0.1mm)