Unit: mm

TOSHIBA Diode Silicon Epitaxial Schottky Barrier Type

# **1SS406**

## **High Speed Switching Application**

Low forward voltage : V<sub>F (3)</sub> = 0.50V (typ.)
 Low reverse current : I<sub>R</sub>= 0.5µA (max)
 Small total capacitance : C<sub>T</sub> = 3.9pF (typ.)

# Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Maximum (peak) reverse Voltage	$V_{RM}$	25	V
Reverse voltage	V <sub>R</sub>	20	V
Maximum (peak) forward current	I <sub>FM</sub>	100	mA
Average forward current	Io	50	mA
Surge current (10ms)	I <sub>FSM</sub>	1	Α
Power dissipation	P*	200	mW
Junction temperature	Tj	125	°C
Storage temperature range	T <sub>stg</sub>	-55~125	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in

USC

JEDEC —

JEITA —

TOSHIBA 1-1E1A

Weight: 0.004g(Typ.)

temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

\*: Mounted on a glass epoxy circuit board of 20 × 20 mm, pad dimension of 4 × 4 mm.

### **Electrical Characteristics (Ta = 25°C)**

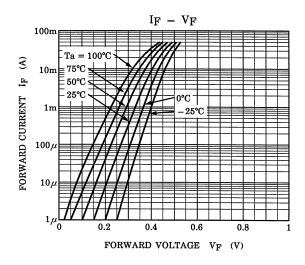
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit	
Forward voltage	V <sub>F (1)</sub>	_	I <sub>F</sub> = 1mA	_	0.33	_		
	V <sub>F (2)</sub>	_	I <sub>F</sub> = 5mA	-	0.38	_	V	
	V <sub>F (3)</sub>	_	I <sub>F</sub> = 50mA	_	0.50	0.55		
Reverse current	I <sub>R</sub>	_	V <sub>R</sub> = 20V	_	_	0.5	μΑ	
Total capacitance	C <sub>T</sub>	_	V <sub>R</sub> = 0, f = 1MH <sub>z</sub>	_	3.9	_	pF	

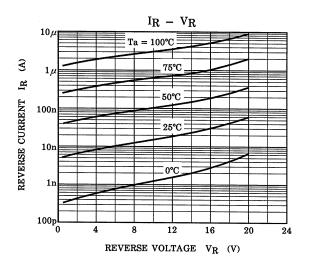
### **Equivalent Circuit (Top View)**

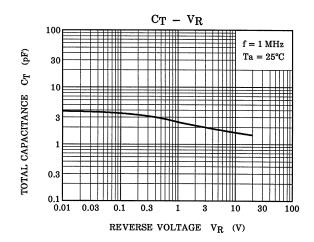


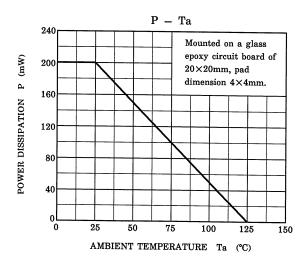
### Marking











2

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20070701-EN GENERAL

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