National Semiconductor

# 100422 256 x 4-Bit Static RAM 10 ns, 7 ns, 5 ns

## General Description

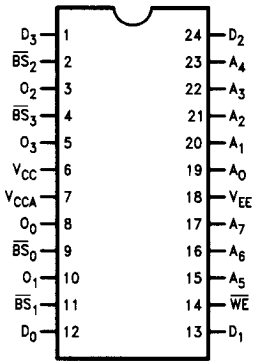
The 100422 is a 1024-bit read/write Random Access Memory (RAM), organized 256 words by four bits per word. It is designed for high-speed scratchpad, control and buffer storage applications. The device features full on-chip address decoding, separate Data Input and non-inverting Data Output lines, as well as four active-LOW Bit Select lines.

## Features

- Address access time—5 ns/7 ns/10 ns max
- Bit select access time—4 ns/5 ns/5 ns max
- Four bits can be independently selected
- Open-emitter outputs for easy memory expansion
- Polyimide die coat for alpha immunity

## Connection Diagrams

**24-Pin Ceramic Dual-In-Line Package**



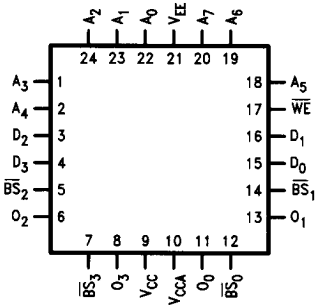

TL/D/9643–2

**Top View**

**Order Number 100422DC5, 100422DC7 or 100422DC10**
**See NS Package Number J24E***

*For most current package information, contact product marketing.

**24-Pin Ceramic Flatpak**

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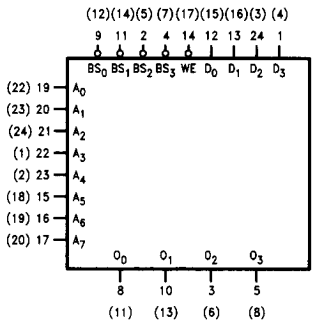
**Top View**

**Order Number 100422FC5, 100422FC7 or 100422FC10**
**See NS Package Number W24B***

*For most current package information, contact product marketing.

**Optional Processing, QR = Burn-In**

## Logic Symbol

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$V_{CC}$ = Pin 6 (9)
$V_{CCA}$ = Pin 7 (10)
$V_{EE}$ = Pin 18 (21)
( ) = Flatpak

**Pin Names**

| | |
|---|---|
| $\overline{WE}$ | Write Enable Input (Active LOW) |
| $\overline{BS}_0$–$\overline{BS}_3$ | Bit Select Inputs (Active LOW) |
| $A_0$–$A_7$ | Address Inputs |
| $D_0$–$D_3$ | Data Inputs |
| $O_0$–$O_3$ | Data Outputs |