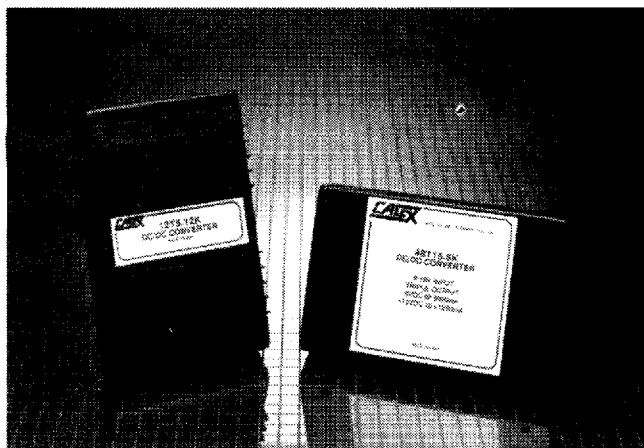


NEW* 55 Watt Triple Output DC/DC Converters

3355 Vincent Road, Pleasant Hill, CA 94523-4389 800-542-3355 Telephone (415)932-3911 FAX: (415)932-6017



FEATURES

T-57-11

- PCB Mounting With Optional Heat Sink Or Chassis Mount Versions
- 2:1 Wide Input Voltage Range (9-18, 18-36, 36-72)
- Efficiencies to 87%
- Common and Differential Mode Input Filtering
- Remote Sense On +5 Volt Output
- Single and Dual Outputs Are Isolated From Each Other
- No Derating To 80°C Case Temperature
- 5 Year Warranty

SELECTION CHART

MODEL	INPUT RANGE VDC		OUTPUTS VDC	OUTPUT CURRENT mA		CASE
	MIN	MAX		MIN	MAX	
12T5.12K	9.0	18.0	5, ±12	5000	±1250	K
12T5.15K	9.0	18.0	5, ±15	5000	±1000	K
24T5.12K	18.0	36.0	5, ±12	5000	±1250	K
24T5.15K	18.0	36.0	5, ±15	5000	±1000	K
48T5.12K	36.0	72.0	5, ±12	5000	±1250	K
48T5.15K	36.0	72.0	5, ±15	5000	±1000	K

*New product specifications are subject to change without notice.

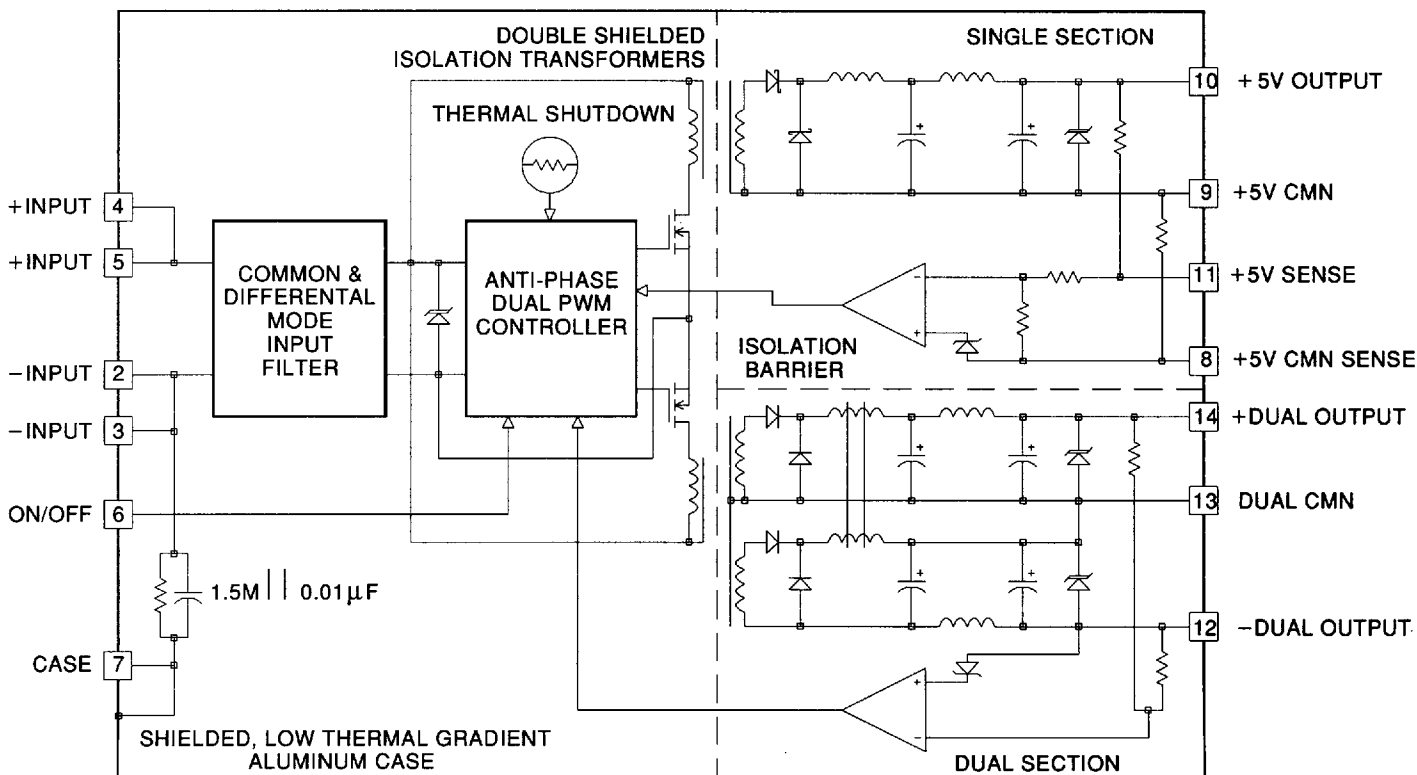
DESCRIPTION

The 55 Watt Triple Series consists of separate power sections for the single and dual outputs. These power sections are operated in anti-phase to each other to reduce the ripple current stress on the input components and the reflected input ripple. The main benefit of two separate power sections is the excellent regulation achieved by all outputs. An order of magnitude regulation improvement is achieved over competitive designs. There is no cross regulation between the +5 output and the dual outputs. This means that the dual output voltages are independent of the +5 Volt loading and visa versa.

Excellent noise performance is achieved by using a 0.040 inch thick aluminum case, pot core and toroidal magnetics, double shielded transformers and both normal mode and common mode input filtering.

The input and outputs are protected from overvoltage by transient voltage suppressor diodes. The outputs are protected from faults with pulse by pulse digital current limiting.

55 WATT TRIPLE SERIES BLOCK DIAGRAM



55 Watt Triple Output DC/DC Converters



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INPUT PARAMETERS (1)								
MODEL		12T5.12K	12T5.15K	24T5.12K	24T5.15K	48T5.12K	48T5.15K	UNITS
Voltage Range	MIN	9.0		18.0		36.0		VDC
	MAX	18.0		36.0		72.0		
Reflected Ripple (2), 0-20MHz BW	TYP	110		60		35		mA p-p
	MAX	250		150		80		
Input Current Full Load	TYP	5630		2715		1325		mA
	No Load	TYP	50	35		30		
Efficiency	TYP	81		85		87		%
Switching Frequency	TYP			80				kHz
Maximum Input Over Voltage, 100mS No Damage	MAX	23		45		85		VDC
Turn-on Time, 1% Output Error	TYP			70		T-57-11		mSec
Recommended Fuse				(3)				

OUTPUT PARAMETERS (1)									
MODEL		12T5.12K 24T5.12K 48T5.12K	12T5.15K 24T5.15K 48T5.15K	12T5.12K 24T5.12K 48T5.12K	12T5.15K 24T5.15K 48T5.15K	UNITS			
Output Voltage		+5		±12		±15		VDC	
Rated Current (4)	MIN	50		310		250		mA	
	MAX	5000		1250		1000			
Voltage Range 100% Load (5)	MIN	4.950		11.925		14.925		VDC	
	TYP	5.000		12.000		15.000			
	MAX	5.050		12.075		15.075			
Output Balance (Plus to Minus Output, Full Load)	TYP	N/A		0.5		0.5		%	
	MAX			0.7		0.7			
Load Regulation 25%-100% Load	TYP	0.02		0.8		0.7		%	
	MAX	0.2		1.2		1.2			
	0-100% Load	TYP	N/A		2.0		2.0		%
		MAX	N/A		4.0		4.0		
Cross Regulation (6)	TYP	N/A		0.8		0.7		%	
	MAX	N/A		1.2		1.2			
Line Regulation Vin = Min-Max VDC	TYP	0.02		0.02		0.02		%	
	MAX	0.2		0.2		0.2			
Short Term Stability (7)	TYP	0.02		0.02		0.02		%	
Long Term Stability	TYP	0.2		0.2		0.2		%/kHrs	
Transient Response (8)	TYP	500		300		300		µSec	
Dynamic Response (9)	TYP	250		250		250		mV peak	
Input Ripple Rejection (10)	TYP	25		25		25		dB	
Noise, 0-20MHz BW	TYP	15		15		15		mV p-p	
	MAX	50		50		50			
Temperature Coefficient	TYP	50		50		50		ppm/°C	
	MAX	100		200		200			
Overvoltage Clamp (11)	TYP	6.8		15		18		VDC	
Short Circuit Protection to Common for all Outputs		Continuous, 8 Hours Minimum Current Limit and Thermal Overload							

Notes:

- (1) All parameters measured at 25 °C, nominal input voltage and full rated load unless otherwise noted. Refer to the CALEX Application Notes for the definition of terms, measurement circuits and other information.
- (2) Noise is measured per CALEX Application Notes. Measurement bandwidth is 0 - 20 MHz.
- (3) Determine the correct fuse size by calculating the maximum DC current drain at low line input, maximum (or use the supplied curves) load and then adding 20 to 25 percent to get the desired fuse size. A slow blow fuse type is recommended. For reverse voltage protection on the input this fuse must be used.
- (4) Minimum load is required for proper regulation only, no module damage is sustained if run less than minimum load. Regulation of the Dual Output degrades with substantial load unbalance.
- (5) The remote sense pins must be connected to their respective output pins for proper output voltage and regulation. The combined drop on each output line to it's respective remote sense pin must be less than 0.3 Volts or 0.6 Volts for both sense lines combined.
- (6) Cross regulation is defined as the change in one output when the other output is changed from minimum to maximum load.
- (7) Short term stability is specified after a 30 minute warmup at full load, constant line, load and ambient conditions.
- (8) Transient response is defined as the time for the output to settle from a 50 to 75 % step load change to a 2% error band (rise time of step = 2 µSec).
- (9) Dynamic response is defined as the peak overshoot during a transient as defined in note 8 above.
- (10) The input ripple rejection is specified for DC to 120 Hz ripple with a modulation amplitude of 1% of Vin.
- (11) For module protection only, see also note 3.
- (12) The logic shutdown pin is Open Collector TTL, CMOS, and relay compatible. The input to this pin is referenced to -input (pin 2).

CALEX MANUFACTURING CO 52E D 1811250 0001190 415 CEX
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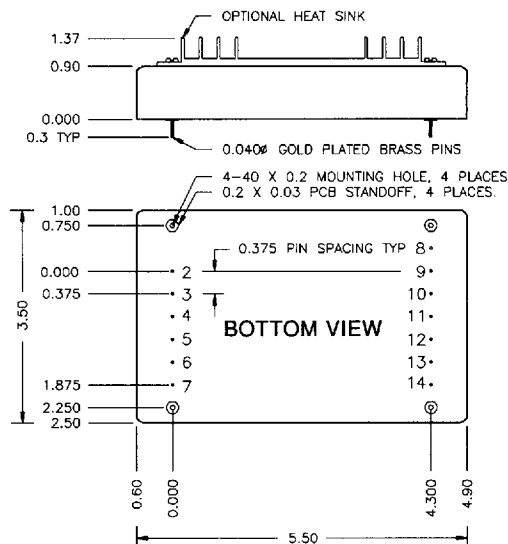
GENERAL SPECIFICATIONS (1)			
MODEL	ALL MODELS		UNITS
LOGIC SHUTDOWN (12)			
On Logic Level or leave pin open	MIN	4.5	VDC
OFF Logic Level	MAX	1.2	VDC
Input Resistance	TYP	2	kOhms
Converter Idle Current, Shut Down Pin Low	TYP	25	mA
ISOLATION			
Isolation Voltage			
Input to Either Output	MIN	500	VDC
Single to Dual Output	MIN	500	
Input to Case	MIN	250	
Either Output to Case	MIN	500	
Input to Output Capacitance			
Input to Single Output	TYP	100	pF
Input to Dual Output	TYP	150	
Single to Dual Output	TYP	75	
ENVIRONMENTAL			
Case Operating Range, No Derating	MIN MAX	-25 80	°C
Case Functional Range (13)	MIN MAX	-40 90	
Storage Range	MIN MAX	-40 100	°C
Thermal Impedance (14) Pin Mount Version			
Option -1/Heat Sink	TYP	2.7	°C/Watt
Option -3/Chassis Mount	TYP	1.8	
Thermal Shutdown Case Temperature	TYP	90	°C
Unit Weight	TYP	14	oz
Case	K		
Mounting Kits	See Case Options		

(13) The functional temperature range is intended to give an additional data point for use in evaluating this power supply. At the low functional temperature the power supply will function with no side effects, however sustained operation at the high functional temperature will reduce expected operational life. The data sheet specifications are not guaranteed over the functional temperature range.

(14) The case thermal impedance is specified as the case temperature rise over ambient per package watt dissipated. The thermal resistance of the Chassis Mount version depends on the mounting surface. If the mounting surface is a poor thermal conductor the thermal resistance can be as high as 2.7 °C/Watt. If the mounting surface is an excellent thermal conductor the thermal resistance can be below 1 °C/Watt.

HEAT SINK OPTION

T-57-11



Mechanical tolerances unless otherwise noted:

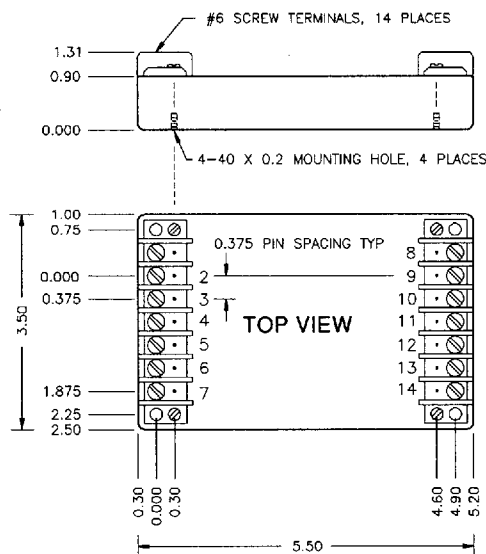
X.XX dimensions: ±0.020 inches

X.XXX dimensions: ±0.005 inches

Seal around terminals is not hermetic. Do not immerse units in any liquid.

PIN	FUNCTION	PIN	FUNCTION
1	NO PIN	8	+5V CMN SENSE
2	- INPUT	9	+5V CMN
3	- INPUT	10	+5V OUTPUT
4	+INPUT	11	+5V SENSE
5	+INPUT	12	- DUAL OUTPUT
6	ON/OFF	13	DUAL CMN
7	CASE	14	+DUAL OUTPUT

CHASSIS MOUNT OPTION



Mounting Configuration Options:

To order the optional Heat Sink on the Pin Mount version place a "-1" suffix on the part number. To order the Chassis Mount version place a "-3" suffix on the part number. The Heat Sink and Chassis Mount options cannot be used together.

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55 WATT TRIPLE DC/DC CONVERTERS APPLICATION GUIDELINES

Inputs:

The input should be fused as per note number 3. The case is connected to the - input pins through 1.5 megohms in parallel with 0.01 μ F. The case may be left floating in most applications. The noise performance of the converter may improve or degrade with the case connected to other inputs or outputs depending on your system grounding.

The remote ON/OFF pin may be left floating if it is not going to be used. See the remote ON/OFF application note for more information on this feature.

The dual input pins (2,3 and 4,5) should be paralleled to share the input current.

Single Output:

The single output is independent of the loading on the dual output section. The single output also features provisions for remote sense connections. These allow the power supply to correct for line drops of up to 0.3 Volts per leg or 0.6 Volts total. The remote sense connections should be made with twisted pair wire or closely coupled PCB traces. There is approximately 2mA of current flowing in the remote sense lines. If the remote sense is not to be used, these pins must be connected to their respective output pins for proper output voltage accuracy and regulation. See our application note on remote sensing for more information. The output common on the single section is electrically isolated from the dual section to aid in proper system grounding.

Dual Output:

The dual outputs are cross regulated to each other but independent of the loading on the single output. The dual output uses a cross regulation scheme where the plus and minus output voltage is regulated as an average voltage. This allows improved regulation on both outputs. If the outputs are loaded equally and the regulation is checked, then the apparent regulation is much closer to 0.5%, that is if both outputs are taken from 20% of full load to 100% of full load. Our data sheet shows the worst case result of changing each output independently. The output common on the dual section is electrically isolated from the +5 Volt section to aid in proper system grounding. If 24 Volts or 30 Volts are required, then the appropriate output can be connected to the system ground and the output current can be taken from the other output. In this situation the dual section's common pin (Pin 13) should be left unconnected. Full output power (30 Watts) is available in this configuration.

Mounting Guidelines:

The 55 Watt Triple Series can be supplied in either a pin mount version or a chassis mount (screw terminal) version. It is suggested that when using the pin mount version, the 4-40 hold down screws provided in the bottom of the case be used to secure the unit to the PCB. These screws should be tightened before soldering to avoid solder joint stress.

The chassis mount version can be mounted on its back with the four 4-40 hold down screws provided. The mounting surface should be flat to within 0.01 inches to prevent warping the case. The mounting can then serve as additional heat sinking. For optimum heatsinking, silicone grease is recommended over the so called "dry pads". Additional heatsinking will lower internal temperatures and increase the expected life. When Chassis Mounting remember that the case is connected to the - INPUT pins through 1.5 megohms in parallel with 0.01 μ F. The breakthrough voltage of this connection is greater than 250VDC.

The "-1" heatsink option lowers the thermal resistance from 2.7 to 1.8°C/Watt dissipated. It also increases the heat removing efficiency of any cooling air flow. If the 55 Watt Triple is to be placed in a small enclosure where the power delivered to the load or the power dissipated in the 55 Watt Triple can raise the ambient temperature. Then care must be taken to insure that the case temperature does not exceed the 80°C limit for proper operation.