



## PLL Product Specification

<b>Model:</b>	PLL401-1750	<b>Rev:</b>	B	<b>Date:</b>	4/17/2003
<b>Customer:</b>	SIRENZA MICRODEVICES, INC.	<b>AppNote:</b>	108		
<b>Operating Temperature Range:</b>	(-35 ° to 85 ° C)				

Parameter	Min	Typ	Max	Units	X	Remarks
Frequency Range -	1700	1750	1800	MHz	X	
Step Size -		200		kHz	X	
Settling Time - To within 1.0 kHz		4	10	mSec		
Output Power -	-6	-3	0	dBm	X	
Output Phase Noise:						
1 kHz		-70	-64	dBc/Hz		
10 kHz		-93	-87	dBc/Hz	X	
100 kHz		-113	-107	dBc/Hz		
Power Supply -	2.85	3	3.15	Volts		
Supply Current -		18	25	mA	X	
Spurious Product - 200 kHz		-75	-70	dBc	X	
Reference Feedthrough -		-80	-70	dBc		
Harmonic Suppression:						
2nd Harmonic		-10	-8	dBc	X	
3rd Harmonic		-15	-12	dBc	X	
Ref Osc Signal:						
Frequency		10		MHz		
Amplitude	0.4		2	Vp-p		
Phase Noise - 1 kHz		-145		dBc/Hz		
Input Impedance		10		k $\Omega$		
Output Impedance -		50		$\Omega$		

### Package Information

Package Type:	PLL400	Drawing Number:	60080
Dimensions:	0.6 x 0.6 x 0.14 inches	Drawing Revision:	B

### Comments

**X** Indicates parameter to be tested 100% in production

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