

TECHNICAL DATA DATA SHEET 4322, REV. -

HERMETIC POWER MOSFET N-CHANNEL

SHD231006S -- S-100 (JANTX level room temp) Screening per Sensitron datasheet

FEATURES:

- 60 Volt, 3.0 Ohm, 0.25 A MOSFET
- Isolated Hermetic, Ceramic Package
- Fast Switching
- Low R_{DS (on)}

MAXIMUM RATINGS

ALL RATINGS ARE AT $T_{\rm c}$ = 25°C UNLESS OTHERWISE SPECIFIED.

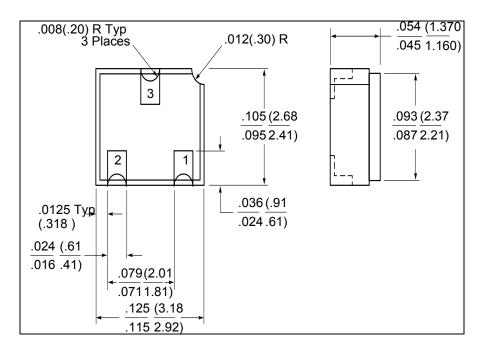
RATING	SYMBOL	MIN.	TYP.	MAX.	UNITS
DRAIN TO SOURCE VOLTAGE	V_{DS}	-	-	60	Volts
GATE TO SOURCE VOLTAGE	V_{GS}	-	-	±20	Volts
ON-STATE DRAIN CURRENT @ T _C = 25°C	I _{D (on)}	-	-	0.25	Amps
PULSED DRAIN CURRENT @ T _C = 25°C	I _{DM}	-	-	1.3	Amps
OPERATING AND STORAGE TEMPERATURE	T_{OP}/T_{STG}	-55	-	+150	°C
TOTAL DEVICE DISSIPATION @ T _C = 25°C	P _D	-	-	2.5	W
THERMAL RESISTANCE, JUNCTION TO CASE	R _{thJC}	-	-	50	°C/W

ELECTRICAL CHARACTERISTICS

DRAIN TO SOURCE BREAKDOWN VOLTAGE	BV _{DSS}	60	-	-	Volts
$V_{GS} = 0V, I_D = 10\mu A$					
ON-STATE DRAIN CURRENT $V_{DS} = 7.5V$, $V_{GS} = 10V$	I _{D(on)}	0.8	-	-	Amp
Pulse width = 300μs, Duty cycle ≤ 2%					
STATIC DRAIN TO SOURCE ON STATE RESISTANCE		-	-		
Pulse width = 300μ s, $V_{GS} = 10V$, $I_D = 500$ mA	$R_{DS(ON)}$			3.0	Ω
Duty cycle \leq 2% $V_{GS} = 5V$, $I_D = 200 \text{mA}$				4.0	
GATE THRESHOLD VOLTAGE $V_{DS} = V_{GS}$, $I_D = 250\mu A$	$V_{GS(th)}$	1.0	-	2.5	Volts
FORWARD TRANSCONDUCTANCE	g_{fs}	_	250	-	S(1/Ω)
$V_{DS} = 15V, I_{D} = 200 \text{mA}$					
ZERO GATE VOLTAGE DRAIN CURRENT		-	-		
$V_{DS} = 60V$, $V_{GS} = 0V$	I _{DSS}			1.0	μΑ
$V_{DS} = 60V, V_{GS} = 0V, T_{C} = 125^{\circ}C$				500	
GATE TO SOURCE LEAKAGE FORWARD V _{GS} = 15V	I_{GSS}	-	-	10	nA
GATE TO SOURCE LEAKAGE REVERSE V _{GS} = -15V				-10	
$V_{DS} = 0V$	<u> </u>				
TOTAL GATE CHARGE $V_{DS} = 30V$,	Q_g		0.4	0.6	nC
GATE-SOURCE CHARGE $V_{GS} = 10V$,	Q_{gs}		0.06		
GATE-DRAIN CHARGE I _D = 250mA	Q _{gd}		0.06		
TURN ON DELAY TIME $V_{DD} = 25V$,	t _{d(ON)}	-	7.5	20	
RISE TIME $I_D = 150 \text{mA}$,	t _r		6.0	00	nsec
TURN OFF DELAY TIME	t _{d(OFF)}		7.5	20	
FALL TIME	t _f		3.0		
INPUT CAPACITANCE $V_{GS} = 0 \text{ V}$	C _{iss}	-	25	-	, F
OUTPUT CAPACITANCE V _{DS} = 25 V,	Coss		6.0		pF
REVERSE TRANSFER CAPACITANCE f = 1.0MHz	C_{rss}		1.2		

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MECHANICAL DIMENSIONS: in Inches / mm



LCC-3

PINOUT TABLE

DEVICE TYPE	PIN 1	PIN 2	PIN 3
N Channel FET	Gate	Source	Drain

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