

0.5 A low V_F MEGA Schottky barrier rectifier Rev. 1 — 12 April 2011

Product data sheet

Product profile 1.

1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD882D leadless ultra small Surface-Mounted Device (SMD) plastic package with visible and solderable side pads.

1.2 Features and benefits

- Forward current: I_F ≤ 0.5 A
- Reverse voltage: V_R ≤ 30 V
- Low forward voltage: $V_F \le 500 \text{ mV}$
- AEC-Q101 qualified
- Ultra small and leadless SMD plastic package
- Solderable side pads
- Package height typ. 0.37 mm

1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications

1.4 Quick reference data

Table 1.	Quick reference d	ata				
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{F(AV)}	average forward current	square wave; δ = 0.5; f = 20 kHz				
		$T_{amb} \le 75 \ ^{\circ}C$	<u>[1]</u> _	-	0.5	А
		T _{sp} ≤ 130 °C	-	-	0.5	А
I _R	reverse current	V _R = 10 V	-	15	200	μA
V _R	reverse voltage		-	-	30	V
V _F	forward voltage	I _F = 500 mA	[2] _	450	500	mV

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[2] Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$.



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2. Pinning information

Pin	Description	Simplified outline	Graphic symbol
1	cathode	<u>[1]</u>	
2	anode		1 <u>+</u> 2 sym001
		Transparent top view	

[1] The marking bar indicates the cathode.

3. Ordering information

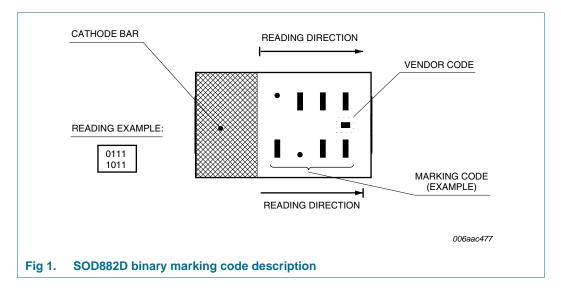
Table 3. Ordering	j information						
Type number	Package	Package					
	Name	Description	Version				
PMEG3005ELD	-	leadless ultra small plastic package; 2 terminals; body 1 \times 0.6 \times 0.4 mm	SOD882D				

4. Marking

Table 4. Marking codes	
Type number	Marking code ^[1]
PMEG3005ELD	0011 0000

[1] For SOD882D binary marking code description, see Figure 1.

4.1 Binary marking code description



PMEG3005ELD Product data sheet

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5. Limiting values

Table 5. In accorda	Limiting values nce with the Absolute Max	imum Rating System (IEC	60134).		
Symbol	Parameter	Conditions	Min	Max	Unit
V _R	reverse voltage		-	30	V
I _{F(AV)}	average forward current	square wave; δ = 0.5; f = 20 kHz			
		$T_{amb} \le 75 \ ^{\circ}C$	<u>[1]</u> -	0.5	А
		$T_{sp} \le 130 \ ^{\circ}C$	-	0.5	А
I _{FRM}	repetitive peak forward current	$t_p \leq 1 \text{ ms; } \delta \leq 0.25$	-	1	A
I _{FSM}	non-repetitive peak forward current	square wave; t _p = 8 ms	<u>[2]</u>	3	A
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	<u>[3]</u> _	340	mW
			<u>[1]</u> -	660	mW
			[4] _	1000	mW
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-55	+150	°C
T _{stg}	storage temperature		-65	+150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[2] $T_j = 25 \ ^{\circ}C$ prior to surge.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[4] Device mounted on a ceramic PCB, Al_2O_3 , standard footprint.

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6. Thermal characteristics

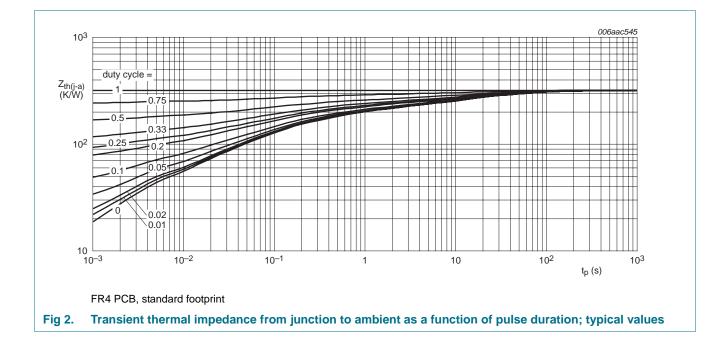
Table 6.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from	in free air	<u>[1][2]</u> _	-	370	K/W
	junction to ambient		<u>[1][3]</u> _	-	190	K/W
			<u>[1][4]</u> _	-	125	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		<u>[5]</u> _	-	50	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

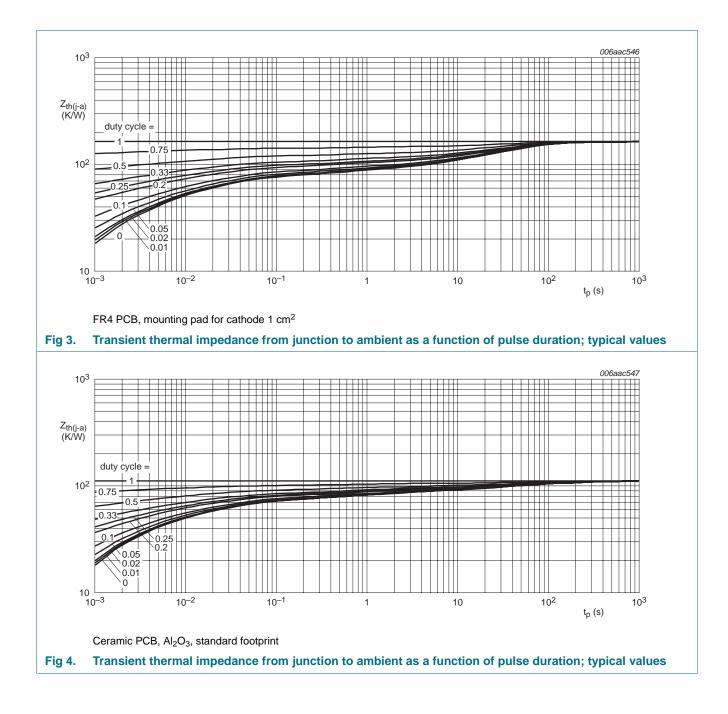
- [4] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.
- [5] Soldering point of cathode tab.



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7. Characteristics

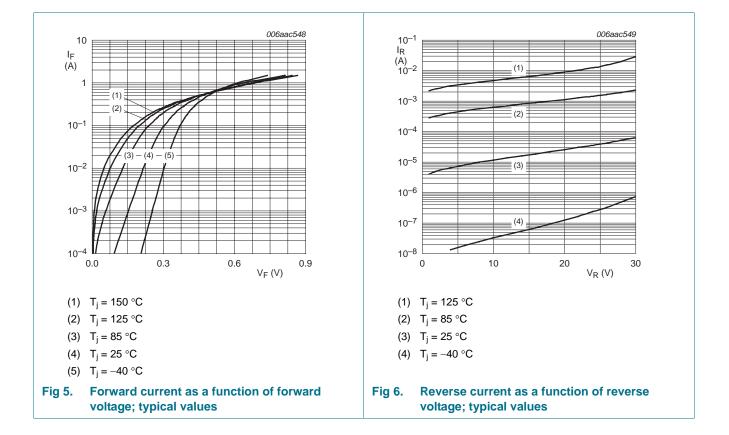
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _F	forward voltage		<u>[1]</u>			
		I _F = 0.1 mA	-	90	180	mV
		I _F = 1 mA	-	150	200	mV
		I _F = 10 mA	-	210	270	mV
		I _F = 100 mA	-	300	360	mV
		I _F = 500 mA	-	450	500	mV
I _R	reverse current	V _R = 10 V	-	15	200	μΑ
		V _R = 30 V	-	80	500	μΑ
C _d	diode capacitance	V _R = 1 V; f = 1 MHz	-	21	30	pF
t _{rr}	reverse recovery time		[2] _	6	-	ns

 $\label{eq:alpha} \begin{tabular}{ll} \mbox{Pulse test: } t_p \leq 300 \ \mu \mbox{s; } \delta \leq 0.02. \end{tabular}$

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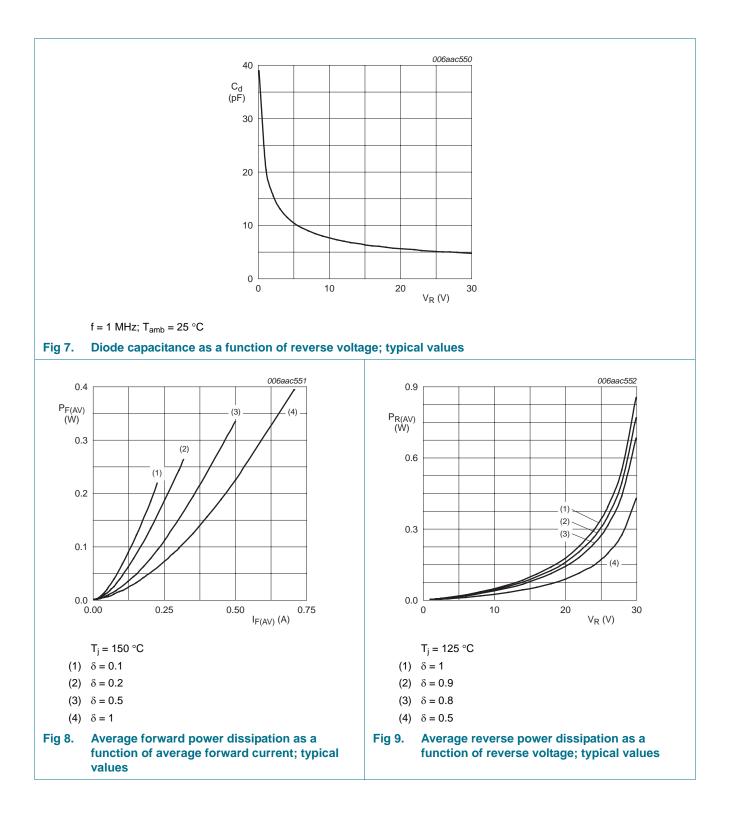
[2] When switched from I_F = 10 mA to I_R = 10 mA; R_L = 100 Ω ; measured at I_R = 1 mA.



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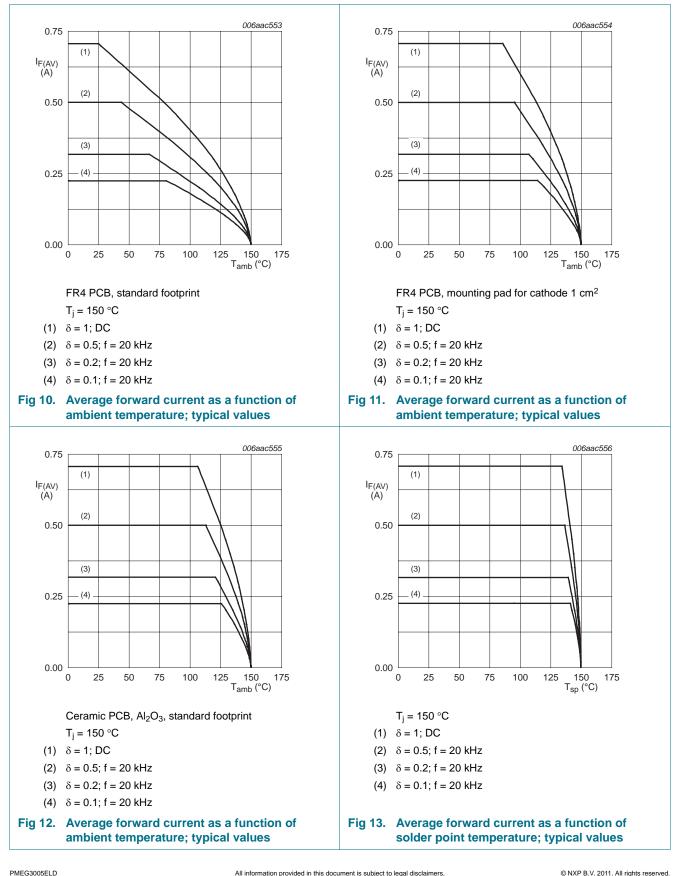
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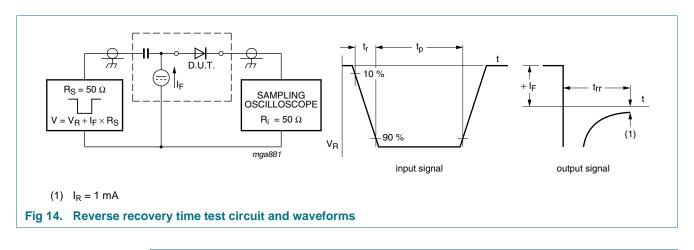
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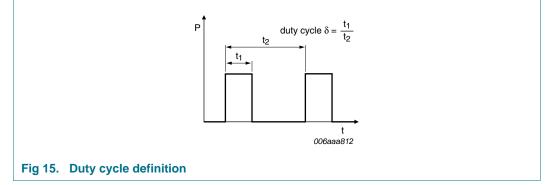


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8. Test information





The current ratings for the typical waveforms as shown in Figure 10, 11, 12 and 13 are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current,

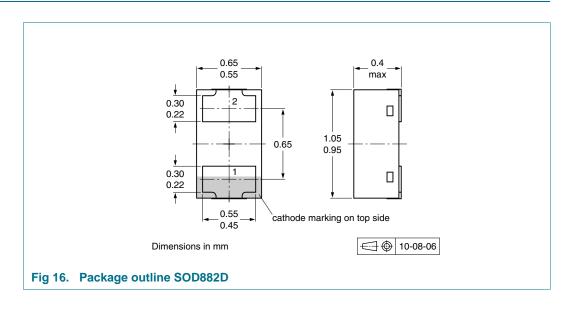
 $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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9. Package outline



10. Packing information

Table 8. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packing quantity
			10000
PMEG3005ELD	SOD882D	2 mm pitch, 8 mm tape and reel	-315

[1] For further information and the availability of packing methods, see <u>Section 14</u>.

1.4 0.2 solder lands solder resist 0.8 06 07 (2×) (2×) (2×) solder paste Dimensions in mm 0.3 0.4 1.3 sod882d fr Reflow soldering is the only recommended soldering method. Fig 17. Reflow soldering SOD882D

11. Soldering

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12. Revision history

Table 9. Revision hist	Revision history					
Document ID	Release date	Data sheet status	Change notice	Supersedes		
PMEG3005ELD v.1	20110412	Product data sheet	-	-		

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13. Legal information

13.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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