## FEATURES

- incorporates GR504, 3 stage amplifer chip
- designed to drive class D integrated receivers
- adjustable symmetrical peak clipper for MPO control
- two low noise inverting preamplifiers
- mid-supply referenced output stage
- anti-aliasing filter network


## STANDARD PACKAGING

- Hybrid Typical Dimensions:
0.244 in $\times 0.102$ in $\times 0.115$ in ( $6.20 \mathrm{~mm} \times 2.59 \mathrm{~mm} \times 2.92 \mathrm{~mm}$ )


## DESCRIPTION

The GS3018 contains Gennum's GR504 class D preamplifier chip, six capacitors and one resistor. Capacitor C1 on the hybrid, in conjunction with $R_{\text {MIC }}$ on the GR504 chip, form a high pass filter which provides a very well regulated supply voltage for the microphone. Resistor R1, capacitor C2 and the output impedance of the microphone ( $\approx 4 \mathrm{k} \Omega$ ) also form a high pass filter with a corner frequency of approximately 240 Hz .

Provisions have been made on the hybrid to allow the manufacturer to increase or decrease this corner frequency with the addition of a capacitor. As the class $D$ receiver utilizes a 100 kHz clock, lower harmonics of the frequency may feed back into the system and interact with the incoming signal from the microphone. Capacitors C5 and C6 have been added to the hybrid to minimize this effect, known as "aliasing".

Blocks A and B on the GR504 are low noise inverting preamplifiers with an open loop voltage gain of typically 50 dB . Typically, the volume control potentiometer would be placed around block A, and block B used either as a Gain Trim stage or as a Telecoil amplifier. The voltage gain of these two blocks is determined by the ratio of the input resistance to the value of the feedback resistor. With $100 \mathrm{k} \Omega$ potentiometer used as a feedback resistor for both stages, block A would have a maximum gain of 20 dB , and block $B$ a gain of 26 dB .

Block C is a mid-supply referenced output stage designed to drive class D receivers. It is typically capable of 670 mV peak to peak output voltage swing and 14 dB of MPO (Maximum Power Output) control with a $10 \mathrm{k} \Omega \mathrm{MPO}$ potentiometer. The gain of the output stage is fixed at typically 16 dB .


All resistors in ohms, all capacitors in farads unless otherwise stated.

ABSOLUTE MAXIMUM RATINGS

| PARAMETER | VALUE / UNITS |
| :--- | ---: |
| Supply Voltage | 3 VDC |
| Power Dissipation | 25 mW |
| Operating Temperature Range |  |
| Storage Temperature Range | $-10^{\circ} \mathrm{C}$ to $40^{\circ} \mathrm{C}$ |
| CAUTION | $-20^{\circ} \mathrm{C}$ to $70^{\circ} \mathrm{C}$ |
| CLASS 1 ESD SENSITIVITY |  |

## PAD CONNECTION



## ELECTRICAL CHARACTERISTICS

Conditions: Frequency $=1 \mathrm{kHz}$, Temperature $=25^{\circ} \mathrm{C}$, Supply Voltage $\mathrm{V}_{\mathrm{B}}=1.3 \mathrm{~V}$

| PARAMETER | SYMBOL | CONDITIONS | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Supply Current | $\mathrm{I}_{\text {AMP }}$ |  | 165 | 240 | 315 | $\mu \mathrm{A}$ |
| STAGE A AND B |  |  |  |  |  |  |
| Input Bias Voltage | $\mathrm{V}_{\text {BIAS }}$ |  | 500 | 580 | 650 | mV |
| Gain (pin 2 to pin 8) | GAINAB | $V_{\text {IN }}=-60 d B V$ | 40 | 44 | 48 | dB |
| OUTPUT STAGE |  |  |  |  |  |  |
| Input Bias Voltage | $\mathrm{V}_{\mathrm{CIN}}$ |  | 550 | 612 | 670 | mV |
| Voltage Gain (pin 15 to pin 10) | GAINC | $V_{\text {IN }}=-50 d B V, R_{M P O}=0$ | 14 | 16 | 18 | dB |
| Maximum Output Level | MPO | $\mathrm{V}_{\mathrm{IN}}=-22 \mathrm{dBV}, \mathrm{R}_{\mathrm{MPO}}=0$ | -14.5 | -12.5 | -10.5 | dBV |
| MPO Range | $\triangle \mathrm{MPO}$ | $\mathrm{V}_{\text {IN }}=-22 \mathrm{dBV}, \mathrm{R}_{\mathrm{MPO}}=10 \mathrm{~K}$ | 12 | 14 | 16 | dB |
| Gain at 10 kHz | GAIN10kHz | $\mathrm{V}_{\mathrm{IN}}=-55 \mathrm{dBV}, \mathrm{R}_{\mathrm{MPO}}=0$ | 0 | 4.5 | 9.0 | dB |

All parameters and switches remain as shown in the Test Circuit unless otherwise stated in CONDITIONS column
$V_{Q X}$-quiescent (unbias) voltage measured on the pin, (nothing connected to the pin). $V_{P X}$-actual voltage measured on the pin at given condition ( X is pin number).


All resistors in ohms, all capacitors
in farads unless otherwise stated.

Fig. 1 Production Test Circuit


Fig. 2 Typical Hearing Instrument Application


Fig. 3 Typical Application Circuit


Fig. 4 Typical Assembly Diagram


All resistors in ohms, all capacitors in farads unless otherwise stated.

Fig. 5 Characterization Circuit (used to generate typical curves)


Fig. 6 Input vs Output $\left(\mathrm{R}_{\mathrm{MPO}}=0\right)$


Fig. 8 Frequency Response vs $\mathrm{C}_{\text {IN }}$


Fig. 7 Maximum Power Output vs $\mathrm{R}_{\text {MPO }}$


Fig. 9 THD vs Input ( $\mathrm{R}_{\mathrm{FA}}=100 \mathrm{k}, \mathrm{R}_{\mathrm{MPO}}=0$ )


Fig. 9 Hybrid Layout and Dimensions

## DOCUMENT IDENTIFICATION: DATA SHEET

The product is in production. Gennum reserves the right to make changes at any time to improve reliability, function or design, in

## REVISION NOTES:

Updated to Data sheet order to provide the best product possible.

