

Memory Module Specification

KVR400X72RC3A/256

256MB 32M x 72-Bit DDR400 CL3 Registered 184-Pin ECC DIMM

DESCRIPTION:

This document describes ValueRAM's 32M x 72-bit (256MB) DDR400 CL3 SDRAM (Synchronous DRAM) Registered ECC memory module. The components on this module include nine 32M x 8-bit (8M x 8-bit x 4 Bank) DDR4003 SDRAM in TSOP packages and three 16-bit drivers in TSSOP packages. This 184-pin DIMM uses gold contact fingers and requires +2.6V. The electrical and mechanical specifications are as follows:

FEATURES:

☑ Clock Cycle Time (tCK) 5ns (min.) / 10ns (max.)

✓ Row Cycle Time (tRC)✓ Refresh Row Cycle Time (tRFC)55ns (min.)70ns (min.)

☑ Row Active Time (tRAS) 40ns (min.) / 70,000ns (max.)

✓ Single Power Supply of +2.6V (+/-.1V) ✓ Power (IDD0) +1.25 W (operating)

☑ UL Rating 94 V - 0

Note: The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 1.200" (30.48mm).

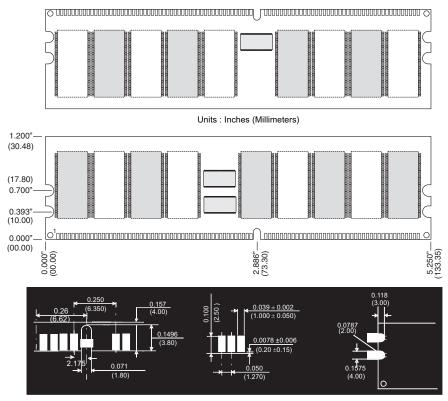


Figure 1: KVR400X72RC3A/256 184-Pin DIMM