

Silicon NPN Power Transistors

2SC1046

DESCRIPTION

- With TO-3 package
- High breakdown voltage

APPLICATIONS

- For CRT horizontal output applications

PINNING(see fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

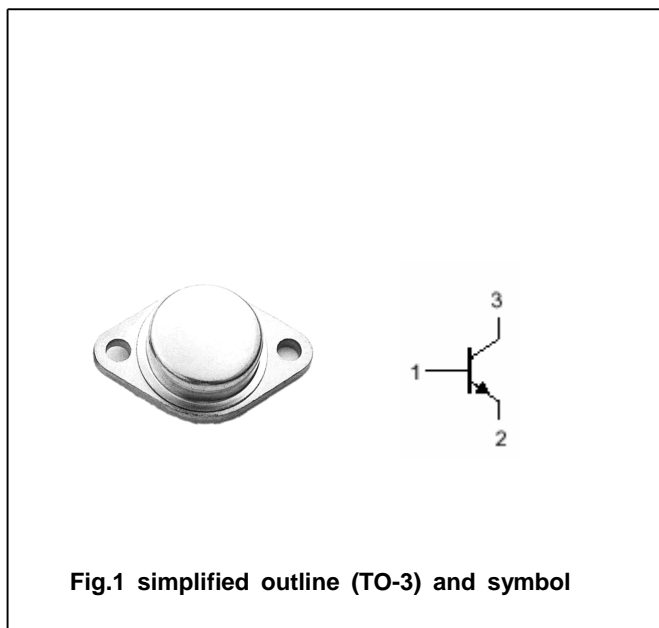


Fig.1 simplified outline (TO-3) and symbol

Absolute maximum ratings(Ta=?)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	1000	V
V_{CEO}	Collector-emitter voltage	Open base	400	V
V_{EBO}	Emitter-base voltage	Open collector	6	V
I_C	Collector current		3	A
P_C	Collector power dissipation	$T_C=25^\circ$	25	W
T_j	Junction temperature		125	?
T_{stg}	Storage temperature		-40~125	?

Silicon NPN Power Transistors

2SC1046

CHARACTERISTICS

T_j=25° unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =5mA; I _B =0	400			V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =1mA; I _E =0	1000			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =1mA; I _C =0	6			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =2 A; I _B =0.4A			2.0	V
I _{CBO}	Collector cut-off current	V _{CB} =800V; I _E =0			10	μA
I _{EBO}	Emitter cut-off current	V _{EB} =5V; I _C =0			10	μA
h _{FE}	DC current gain	I _C =2 A ; V _{CE} =5V	4		20	

PACKAGE OUTLINE

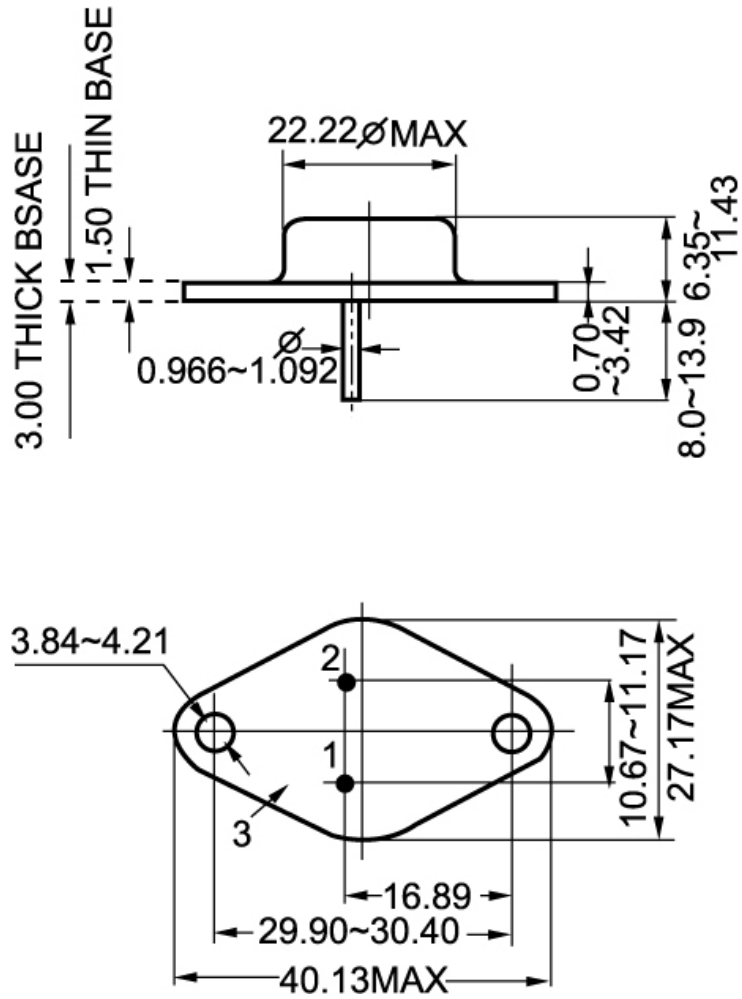


Fig.2 Outline dimensions