



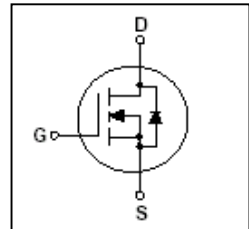
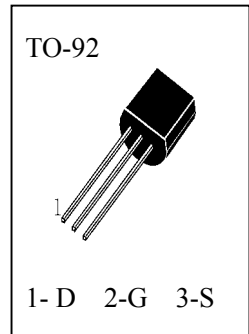
## N-Channel Enhancement Mode Field Effect Transistor

### General Description

These products have been designed to minimize on-state resistance while provide rugged, reliable, and fast switching performance. They can be used in most applications requiring up to 500mA DC. These products are particularly suited for low voltage, low current applications such as small servo motor control, power MOSFET gate drivers, and other switching applications.

### Features

- High density cell design for low  $R_{ds(on)}$ .
- Voltage controlled small signal switch.
- Rugged and reliable.
- High saturation current capability.



### Maximum Ratings ( $T_a=25$ unless otherwise specified )

$T_{stg}$	Storage Temperature	-----	-55~150
$T_j$	Operating Junction Temperature	-----	-55~150
$V_{DSS}$	Drain-Source Voltage	-----	60V
$V_{DGR}$	Drain-Gate Voltage ( $R_{GS} = 1M \Omega$ )	-----	60V
$V_{GSS}$	Gate-Source Voltage	-----	$\pm 20V$
$I_D$	Drain Current (Continuous)	-----	500mA
$P_D$	Maximum Power Dissipation	-----	0.83W

### Electrical Characteristics ( $T_a=25$ unless otherwise specified )

Symbol	Items	Min.	Typ.	Max.	Unit	Conditions
$BV_{DSS}$	Drain-Source Breakdown Voltage	60			V	$V_{GS}=0V, I_D=100\mu A$
$I_{DSS}$	Zero Gate Voltage Drain Current			0.5	$\mu A$	$V_{DS}=25V, V_{GS}=0V$
$I_{GSSF}$	Gate - Body Leakage, Forward			10	nA	$V_{GS}=15V, V_{DS}=0V$
$V_{GS(TH)}$	Gate Threshold Voltage	0.8		3.0	V	$V_{DS}=V_{GS}, I_D=1mA$
$R_{DS(ON)}$	Static Drain-Source On-Resistance			5	$\Omega$	$V_{GS}=10V, I_D=200mA$
$g_{FS}$	Forward Transconductance		320		mS	$V_{DS}=10V, I_D=200mA$
$C_{iss}$	Input Capacitance		24	40	pF	$V_{DS}=10V, V_{GS}=0V,$ $f=1.0MHz$
$C_{oss}$	Output Capacitance		17	30	pF	
$C_{rSS}$	Reverse Transfer Capacitance		7	10	pF	
$t_{on}$	Turn - On Time			10	nS	$V_{DD}=25V, I_D=200mA,$ $V_{GS}=10V, R_{GEN}=25\Omega$
$t_{off}$	Turn - Off Time			10	nS	



## Performance Curves

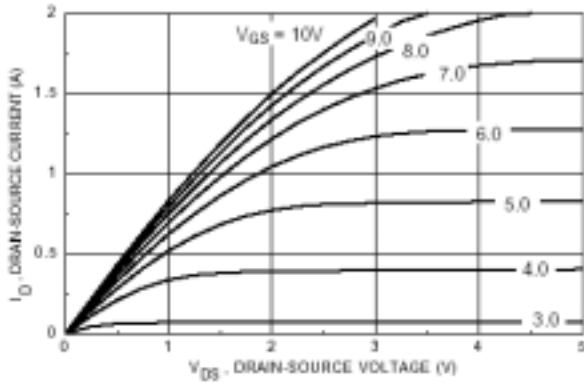


Figure 1. On-Region Characteristics.

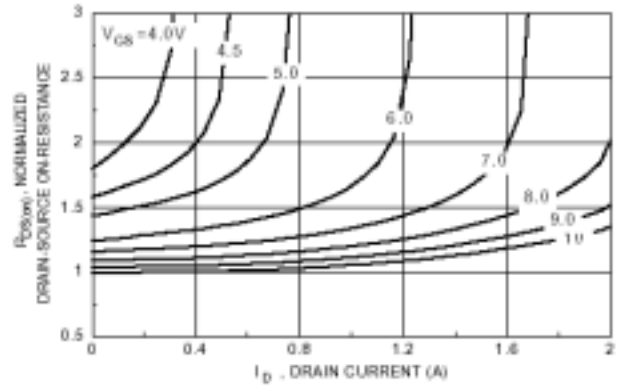


Figure 2. On-Resistance Variation with Gate Voltage and Drain Current.

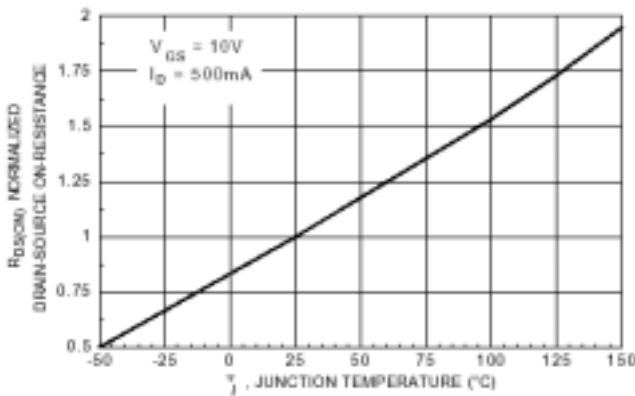


Figure 3. On-Resistance Variation with Temperature.

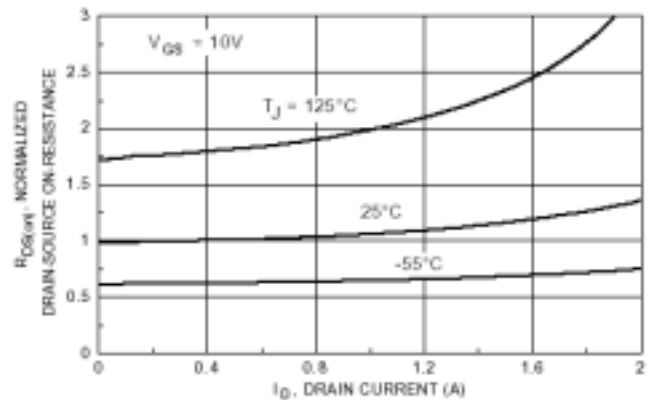


Figure 4. On-Resistance Variation with Drain Current and Temperature.

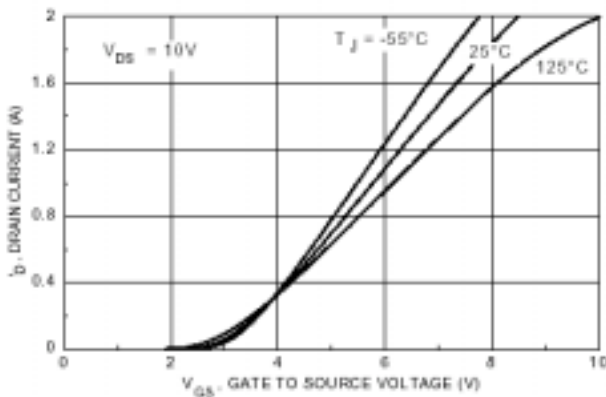


Figure 5. Transfer Characteristics.

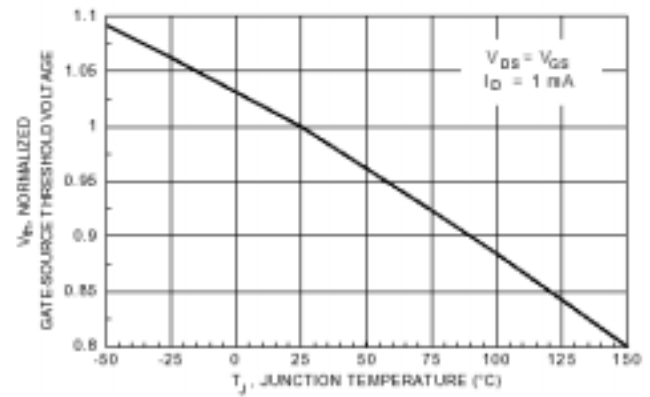


Figure 6. Gate Threshold Variation with Temperature.



## Performance Curves

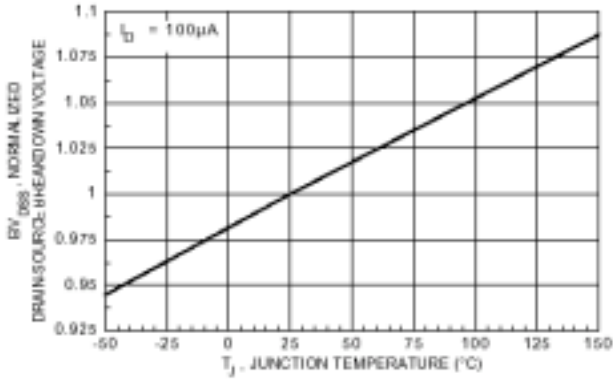


Figure 7. Breakdown Voltage Variation with Temperature.

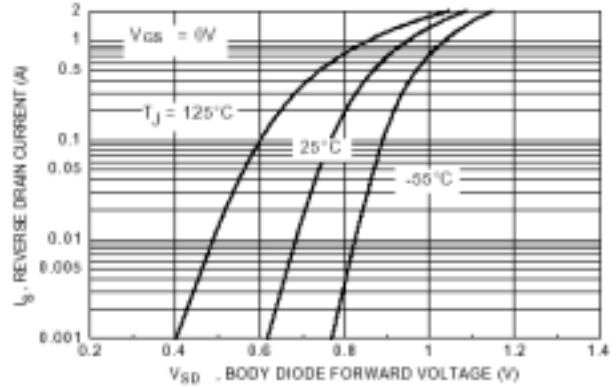


Figure 8. Body Diode Forward Voltage Variation with Current and Temperature.

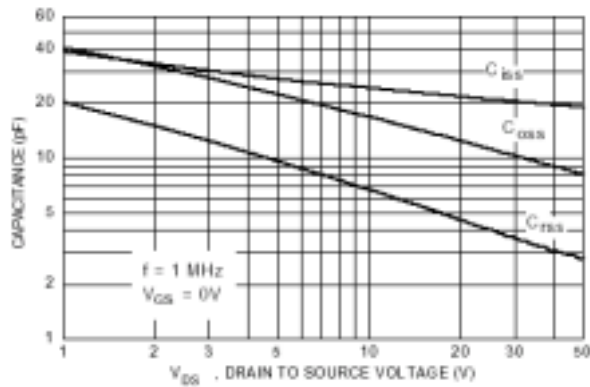


Figure 9. Capacitance Characteristics.

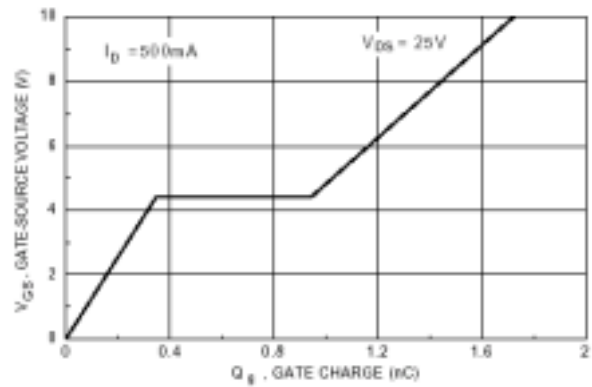


Figure 10. Gate Charge Characteristics.

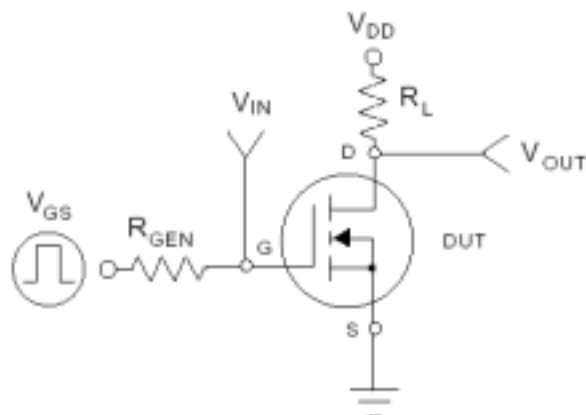


Figure 11. Switching Test Circuit.

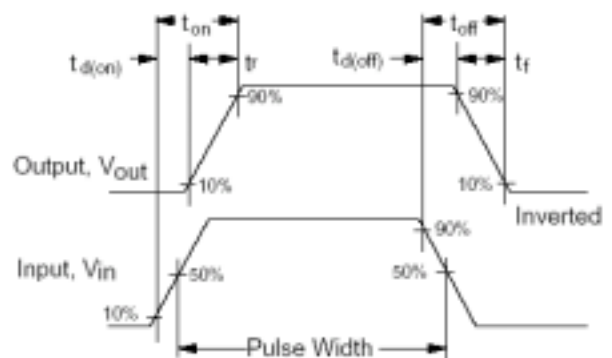


Figure 12. Switching Waveforms.



## Performance Curves

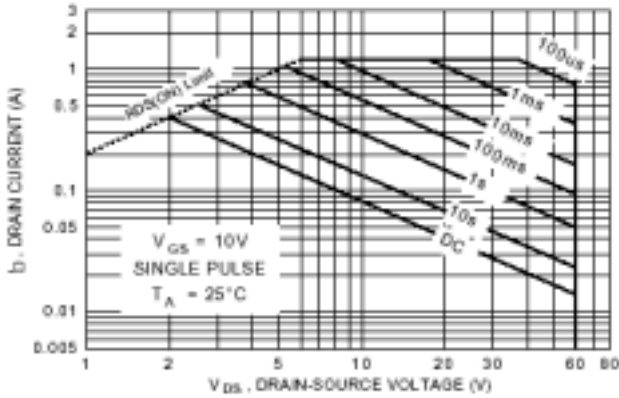


Figure 13. Maximum Safe Operating Area.

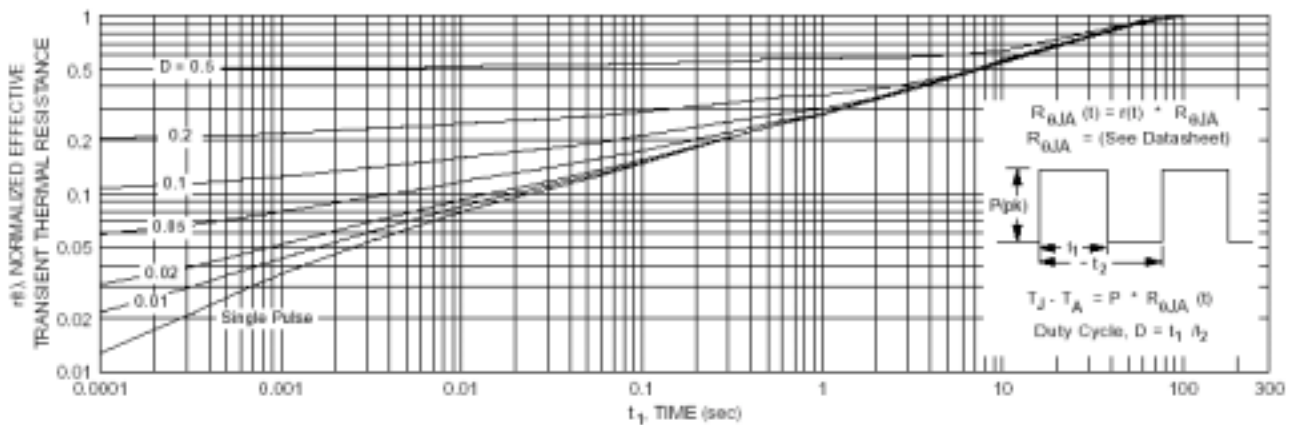


Figure 14. Transient Thermal Response Curve.