

2SA1980S

PNP Silicon Transistor

Description

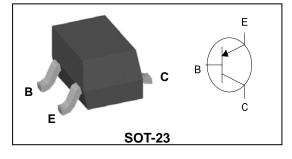
• General small signal amplifier

Features

- Low collector saturation voltage :
 - $V_{CE(sat)} = -0.3V(Max.)$
- Low output capacitance : Cob=4pF(Typ.)
- Complementary pair with 2SC5343S

Ordering Information

PIN Connection



| Type NO. | Marking | Package Code |
|----------|--------------------|--------------|
| 2SA1980S | <u>CA</u> 1 2 3 | SOT-23 |

1)Device Code 2)hFE Rank 3)Year&Week Code

Absolute Maximum Ratings

| Absolute Maximum Ratings | | | (Ta=25°C) |
|-----------------------------|------------------|---------|-----------|
| Characteristic | Symbol | Rating | Unit |
| Collector-base voltage | V _{CBO} | -50 | V |
| Collector-emitter voltage | V _{CEO} | -50 | V |
| Emitter-base voltage | V _{EBO} | -5 | V |
| Collector current | Ι _C | -150 | mA |
| Collector power dissipation | P _C * | 350 | mW |
| Junction temperature | Tj | 150 | °C |
| Storage temperature range | T _{stg} | -55~150 | °C |

* Package mounted on 99.5% alumina 10×8×0.6mm

Electrical Characteristics

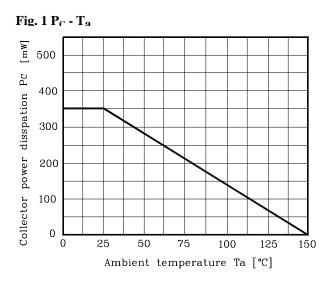
| Electrical Characteristics (1a=25 | | | | | | - <u>4</u> 3 C) |
|--------------------------------------|----------------------|---|------|------|------|-----------------|
| Characteristic | Symbol | Test Condition | Min. | Тур. | Max. | Unit |
| Collector-emitter breakdown voltage | BV_{CEO} | I_{C} =-1mA, I_{B} =0 | -50 | - | - | V |
| Collector cut-off current | I _{CBO} | V_{CB} =-50V, I_{E} =0 | - | - | -0.1 | μΑ |
| Emitter cut-off current | I _{EBO} | V_{EB} =-5V, I_{C} =0 | - | - | -0.1 | μΑ |
| DC current gain | h _{FE} * | V_{CE} =-6V, I_{C} =-2mA | 70 | - | 700 | - |
| Collector-emitter saturation voltage | V _{CE(sat)} | I_{C} =-100mA, I_{B} =-10mA | - | - | -0.3 | V |
| Transition frequency | f_{T} | V_{CE} =-10V, I_{C} =-1mA | 80 | - | - | MHz |
| Collector output capacitance | C _{ob} | V_{CB} =-10V, I_{E} =0, f=1MHz | - | 4 | - | pF |
| Noise figure | NF | V_{CE} =-6V, I_{C} =-0.1mA f=1KHz, Rg=10K Ω | - | 10 | - | dB |

*: h_{FE} rank / O : 70~140, Y : 120~240, G : 200~400, L : 300~700.

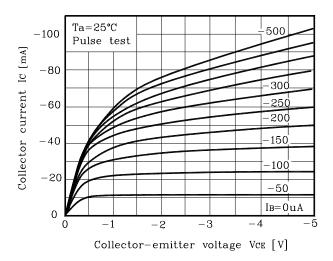
 $(T_9 - 25^{\circ}C)$

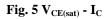
2SA1980S

Electrical Characteristic Curves









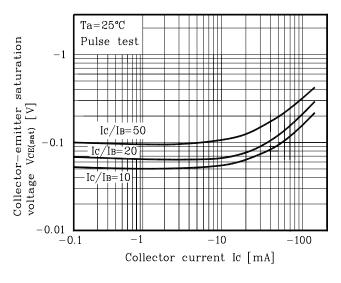


Fig. 2 $I_C - V_{BE}$

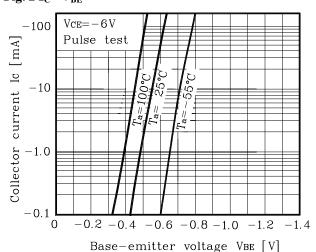
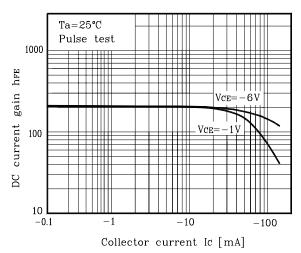
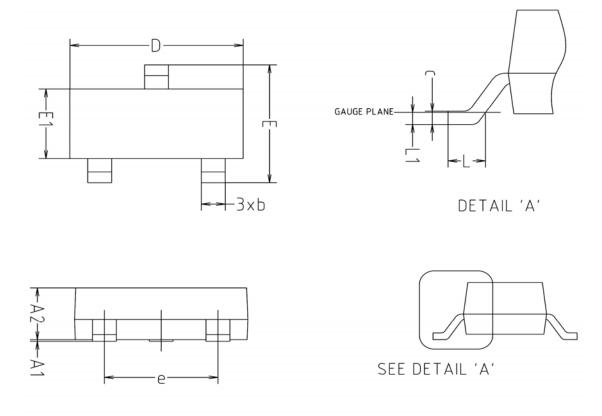


Fig. 4 h_{FE} - I_C



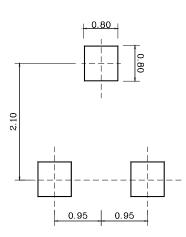
2SA1980S

Outline Dimension



| SYMBOL | MILLIMETERS | | | NOTE |
|--------|-------------|---------|---------|------|
| STIDUL | MINIMUM | NOMINAL | MAXIMUM | NOTE |
| A1 | 0.00 | - | 0.10 | |
| A2 | 0.82 | - | 1.02 | |
| Ь | 0.39 | 0.42 | 0.45 | |
| С | 0.09 | 0.12 | 0.15 | |
| D | 2.80 | 2.90 | 3.00 | |
| E | 2.20 | 2.40 | 2.60 | |
| E1 | 1.20 | 1.30 | 1.40 | |
| e | 1.90BSC | | | |
| L | 0.20 | - | - | |
| L1 | | 0.12BSC | | |

*Recommend PCB solder land [Unit: mm]



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