

HZU6.2Z

Silicon Epitaxial Planar Zener Diode for Surge Absorb

HITACHI

ADE-208-581A (Z)

Rev.1
Nov. 2001

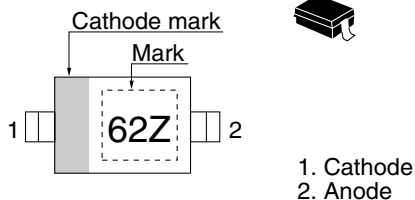
Features

- Low capacitance ($C = 8.5 \text{ pF max}$) and can protect ESD of signal line.
- Ultra small Resin Package (URP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Code
HZU6.2Z	62Z	URP

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Power dissipation	Pd *	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: Two device total, See Fig.2.

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Zener voltage	V _z	5.90	—	6.50	V	I _z = 5 mA, 40 ms pulse
Reverse current	I _R	—	—	3	μA	V _R = 5.5 V
Capacitance	C	—	8.0	8.5	pF	V _R = 0 V, f = 1 MHz
Dynamic resistance	r _d	—	—	60	Ω	I _z = 5 mA

Main Characteristic

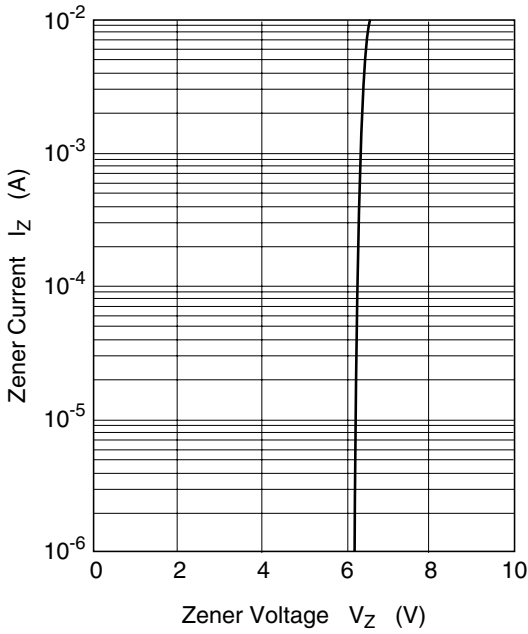


Fig.1 Zener current vs. Zener voltage

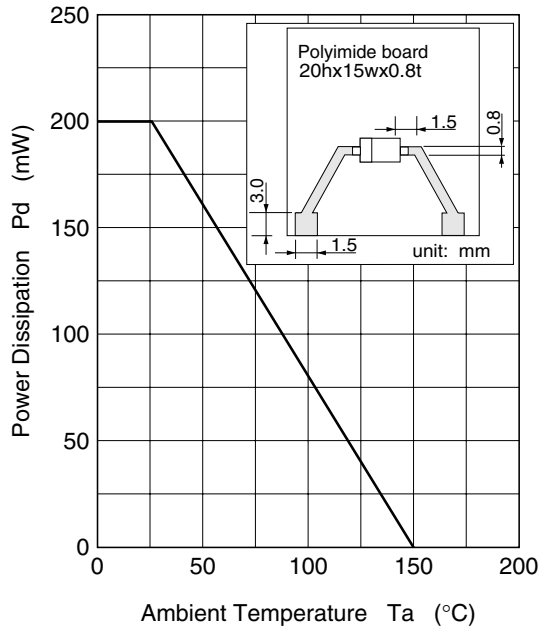


Fig.2 Power Dissipation vs. Ambient Temperature

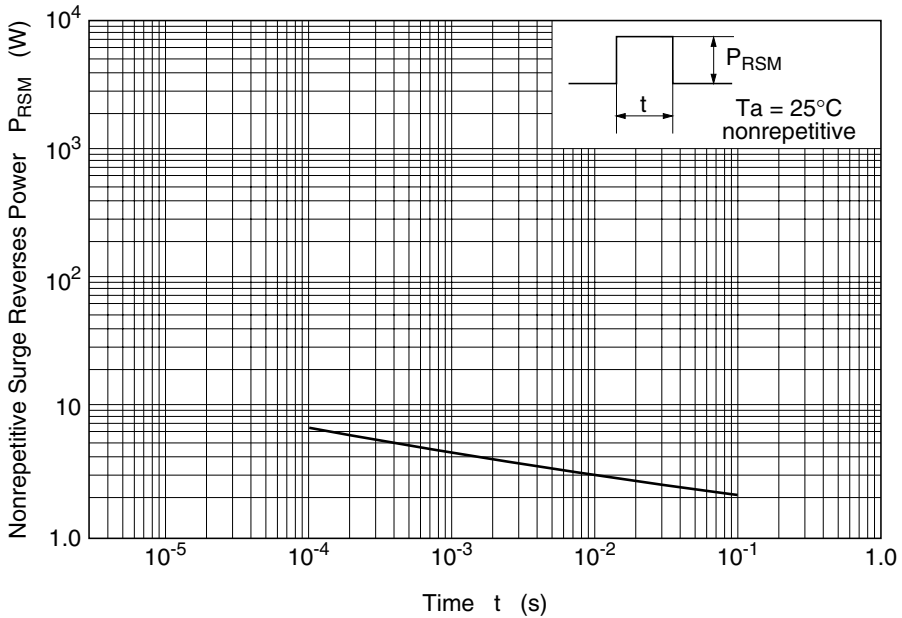


Fig.3 Surge Reverse Power Ratings

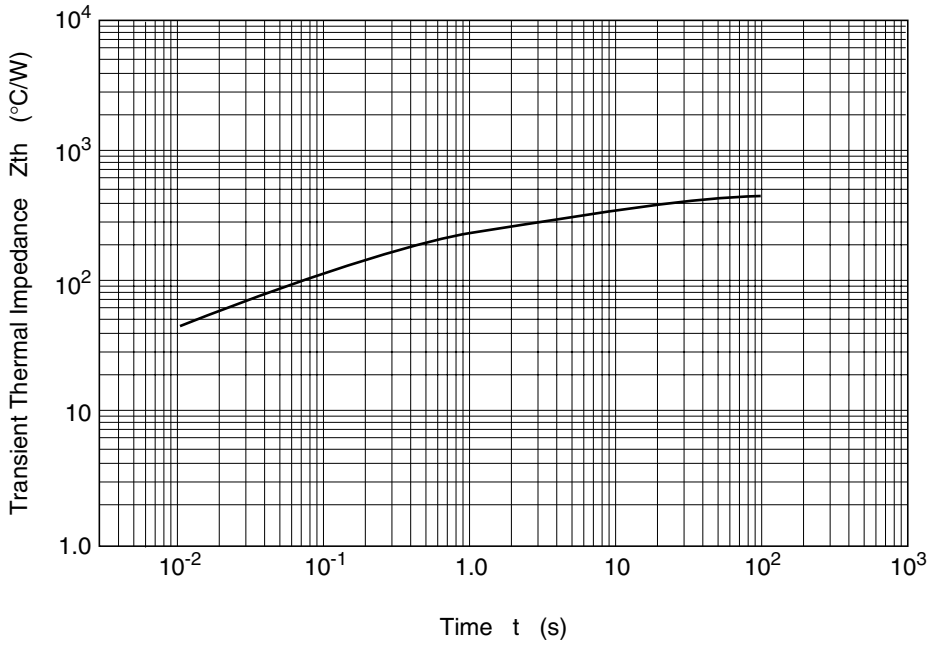
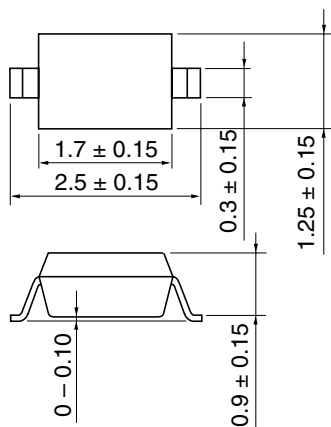


Fig.4 Transient Thermal Impedance

Package Dimensions

As of July, 2001

Unit: mm



Hitachi Code	URP
JEDEC	—
JEITA	—
Mass (reference value)	0.004 g

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