

Display Controllers/Drivers

DS8885 MOS to High Voltage Cathode Buffer

General Description

The DS8885 interfaces MOS calculator or counterlatch-decoder-driver circuits directly to 7-segment, high-voltage, gas-filled displays. The six inputs A, B, D, E, F, G are decoded to drive the 7-segment of the tube.

Each output constitutes a switchable, adjustable current source which provides constant current to the tube segment, even with high tube anode supply tolerance or fluctuation. These current sources have a voltage compliance from 3V to at least 80V. Each current source is ratioed to the b-output current as required for even illumination of all segments. Output currents may be varied over the 0.2 to 1.5 mA range for driving various tube types or

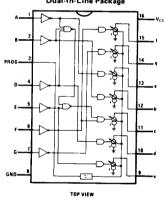
multiplex operation. The output current is adjusted by connecting a program resistor (R_P) from V_{CC} to the program input.

Features

- Current source outputs
- Adjustable output currents 0.2 to 1.5 mA
- High output breakdown voltage 80V min
- Suitable for multiplex operation
- Low fan-in and low power
- Blanking via program input
- Also drives overrange, polarity, decimal point cathodes

Connection Diagram

Dual-In-Line Package



Order Number DS8885J or DS8885N See NS Package J16A or N16A

Truth Tables

	Α	В	D	Ē	F	G	DISPLAY
	- 1	1	1	1	1	0	IJ
	0	1	0	0	0	0	/
	1	1	1	1	0	1	, <u>.</u> ;
	1	1	1	0	0	1	7
	0	1	0	0	1	1	= 7
	1	0	1	0	1	1	15
	1	0	1	1	1	1	E
i	1	1	0	0	0	0	\ ' -'/
	1	1	1	1	1	. 1	
	1	1	1	0	1	1	9
ì	0	0	1	1	1	1	5
ł	1	1	0	0	1	1	9
ı	1	1	0	1	1	3	9
ı	0	1	0	1	1	1	\mathcal{H}
ı	0	1	1	1	1	0	
ı	0	0	0	0	0	1	=
ı	0	0	0	0	0	0	

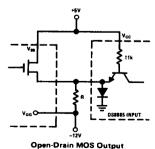
INPUT*	OUTPUT*			
. 0	1 (OFF)			
1	0 (ON)			

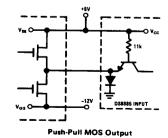
'Positive Logic



C - (A-B-D + E) F

Typical Applications





5-158

UNITS

°C

MIN

4.75

0

MAX

5.25 +70

E

Absolute Maximum Ratings (Note 1) Operating Conditions

Supply Voltage (VCC)

Temperature (TA)

Vcc	7V
Input Voltage	6V
Segment Output Voltage	80V
Power Dissipation	600 mW
Transient Segment Output Current (Note 4)	50 mA
Storage Temperature Range	-65° C to +150° C
Maximum Power Dissipation* at 25°C	
Cavity Package	1509 mW
Molded Package	1476 mW
Lead Temperature (Soldering, 10 seconds)	300° C

Lead Temperature (Soldering, 10 seconds) 300°C

*Derate cavity package 10.06 mW/°C above 25°C; derate molded package 11.81 mW/°C above 25°C.

Electrical Characteristics (Notes 2 and 3)

	PARAMETER	CONDITIONS			MIN	TYP	MAX	UNITS
V _{IH}	Logical "1" Input Voltage	V _{CC} = Min			2.0			
VIL	Logical "0" Input Voltage	V _{CC} = Min					8.0	
I _{IH}	Logical "1" Input Current	V _{CC} = Max	V _{IN} = 2.4	>		2	15	μΑ
		V _{CC} - IVIAX	V _{IN} = 5.5V			4	400	μΑ
I _{IL}	Logical "O" Input Current	V _{CC} = Max, V _{IN} = 0.4V				-300	-600	μΑ
Icc	Power Supply Current V _{CC} = Max, All Inputs = 0V, R _P = 2.2k					22	31	mA
V _I	Input Diode Clamp Voltage	$V_{CC} = 5V$, $I_{1N} = -12$ mA, $T_A = 25^{\circ}C$				-0.9	-1.5	V
SEGM	ENT OUTPUTS					-		
Io	"ON" Current Ratio		Out	puts a, f, and g	0.84	0.93	1.02	
.0		All Outputs = 50\	√, Out	Output c		1.25	1.38	
		I _{OUT} b = Ref.	Out	Output d		1.00	1.10	
		001	Out	Output e		1.10	1.21	
I _{b ON}	Output b "ON" Current			R _P = 18.1k	0.15	0.20	0.25	mA
-D O14		$V_{CC} = 5V, V_{OUT} b = 50V,$ $T_A = 25^{\circ}C$		R _P = 7.03k	0.45	0.50	0.55	mA
				R _P = 3.40k	0.90	1.00	1.10	mA
				R _P = 2.20k	1.35	1.50	1.65	mA
V _{SAT}	Output Saturation Voltage	V _{CC} = Min, I _{OUT}		0.8	2.5	V		
I _{CEX}	Output Leakage Current	V _{OUT} = 75V, V _{IN} = 0.8V, R _P = 1k				0.003	3	μΑ
V _{BR}	Output Breakdown Voltage	Ι _{ΟUT} = 250μΑ, V _{IN} = 0.8V			80	110		V
t _{pd}	Propagation Delay of Input to Segment Output	V _{CC} = 5V, T _A = 25°C				0.4	10	μς

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

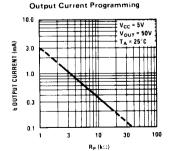
Note 2: Unless otherwise specified min/max limits apply across the 0°C to +70°C range for the DS8885. All typical values are for T_A = 25°C and V_{CC} = 5V.

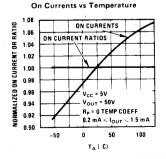
Note 3: All currents into device pins shown as positive, out of device pins as negative, all voltages referenced to ground unless otherwise noted. All values shown as max or min on absolute value basis.

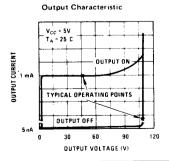
Note 4: In all applications transient segment output current must be limited to 50 mA. This may be accomplished in dc applications by connecting a 2.2k resistor from the anode-supply filter capacitor to the display anode, or by current limiting the anode drive in multiplex applications.

Note 5: For saturation mode the segment output currents are externally limited and ratioed.

Typical Performance Characteristics







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