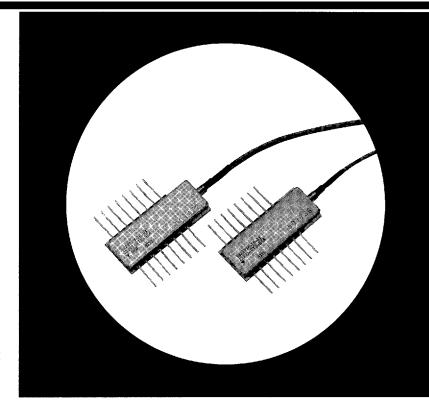


Series 125H Technical Data

125H Military lightwave data link

- · Hermetic, 0.12-inch high, gull-wing package
- · Data rates: 10 Mbits/s to 125 Mbits/s
- Wide operating temperature range: -55°C to +125°C
- 1.3 µm LED and PIN photodiode
- · Link distance: up to 3 km
- Typical link budget: 23.0 dB at 25°C
- Single -5.2 V power supply
- 100K ECL compatibility
- · High level of integration
- 100/140 µm fiber pigtail
- Connector: Size 16 fiber-optic terminus (compatible with the MIL-C-38999 connector; optional connectors available
- · Link status flag
- · Chatter-free output with no light input



Applications

The 125H Data Link is recommended for use in on-board aircraft systems to transmit information from:

- Sensor to processor parallel data bus
- Radar sensors to a CPU within the fuselage
- · CPU to display screen
- CPU to weaponry systems (including stores management).

Other Uses

- · Compressed video transmission
- High-bandwidth, token passing ring architectures (e.g., FDDI-type nodes)
- · Tethered weapons/vehicles
- · Remote antennae
- Secure point-to-point, long-distance data communications

 Other military/commercial avionics and harsh environment data link applications

Benefits

- Low-profile package can be used to populate both sides of Standard Electronic Module circuit cards, format F
- Improved board manufacturing process through surface-mount technology
- 0.53-square-inch package minimizes mounting space
- Immediate delivery (prototypes four- to six-weeks)
- · Technical product support

Description

The LDL125H Lightwave Data Link is a high-performance link that operates at data rates from 10 Mbits/s to 125

Mbits/s (NRZ) at a typical distance of 3 km. The link has a typical power margin of 21.0 dB BOL (beginning of life). A 14.5 dB power margin is predicted for worst-case conditions over a 10⁵ hour service life. Complementary bipolar integrated circuits are used to achieve high data rates.

Characterized by an extremely wide operating temperature range of -55°C to +125°C, the link is ideally suited for use in harsh environments, specifically military avionic applications. It is recommended for applications where high reliability and an extended service life are the critical concerns.

Both the 902392 Transmitter and the 902393 Receiver are produced using identical, fully hermetic, 16-lead gullwing packages. The pigtail consists of 100/140 µm hermetically sealed optical fiber with a size 16 fiber-optic

Technical Data Sheet 125H

terminus. The terminus is designed for use with the MIL-C-38999 connector. Other size 16 termini supporting MIL-C-28876 guidelines and ST^{\circledR} Connectors are also available.

For high reliability and maximum EMI/RFI protection, the metal/ceramic package should be used with a heat sink and mounted on a grounded portion of the printed circuit board.

Transmitter

The 902392 transmitter consists of a long-wavelength, high-speed LED, a silicon IC, and several discrete components. The device operates from a positive power supply (Vcc = +4.5 V TO +5.46 V), or a negative power supply (VEE = -4.5 V to -5.46). The LED driver is a microwave complementary bipolar IC (MCBIC) capable of a peak LED drive current of 85 mA. To minimize turn-on delay, the LED is biased by a small current in the off condition.

Along with the LED and driver circuitry, the only other components

inside the package are two militaryqualified ceramic capacitors and the optical subassembly.

The estimated mean time between failures (MTBF) at 85°C, calculated per MIL-HDBK-217D and using uninhabited aircraft conditions, is 877,000 hours.

Receiver

The 902393 receiver is equipped with a PIN photodetector and includes components similar to those in the transmitter. The receiver operates from a positive power supply (Vcc = +4.5 V TO +5.46 V) and in conjunction with the transmitter, exhibits a typical dynamic range of 23 db at 25°C. The photodetector delivers an average power sensitivity of –32 dBm at a a 2.5 x 10⁻¹⁰ bit error rate (BER) at 125 Mb/s.

The data outputs are 100K ECL compatible.

The package also includes an InGaAs PIN photodetector, four military-qualified ceramic capacitors,

and a discrete resistor.

Link Status Flag

Represented in the block diagram as a receiver output pin, the link status flag is a system monitor used to oversee the integrity of the optical communication channel. Like the data outputs, the link status flag outputs are 100K ECL compatible.

For the receiver, the estimated MTBF at 85°C, calculated per MIL-HDBK-217D and using uninhabited aircraft conditions, is greater than 1.9 million hours.

Chatter-Free Output

Under conditions of no light input, a chatter-free output is available (typical 0.4 µs between transitions). This results in a 2 dB sensitivity penalty.

The LDL 125H Data Link is designed to meet all the electrical and optical specifications defined in the FDDI PMD document.

Pin Diagrams **Outline Drawing** 902392 Transmitter 902393 Receiver Dimensions are in inches and [millimeters]. Weight: transmitter/receiver pair = 10 grams. .120 .660 [3.048][16.764] 020 [0.508]460 [11.684] Case 8 Case 16 10 7 Case Case 7 x .100 Case 11 6 VPD [2.540]1.160 12 5 Vcc Case [29.464] 13 Case 4 VCCA Signal Detect 14 3 Data Signal Detect 15 2 Data 16 Case Case Case 8 Case .350 7 Case 10 Case [8.890] 11 6 VEE Vcc. 12 5 Vcc Case 13 4 Case 14 3 Data Case .500 [12.700] Data 15 2 VBB 16 Case

Electrical Characteristics 125H Transmitter*

Parameter	Symbol	Min	—55°C	+25°C	+85°C	+125°C	Max	Unit
Input Voltage—Low	VIL	-1.810		_	_		-1.475	
Input Voltage—High	VIH	-1.165			_	_	-0.880	V
Average Optical Power†	Po (AVG)	-18.5	-12.4	-14.0	-15.2	-16.0	-8.0	dBm
Duty Cycle Distortion‡	DCD		0.45	0.45	0.5	0.6	.65	ns
Extinction Ratio	EXT	10	15	16	17	18	_	dB
Output Rise Time§	tr	0.6	1.0	1.1	1.2	1.3	3.5	ns
Output Fall Time§	tf	0.6	1.8	2.1	2.3	2.5	3.5	ns
Optical Wavelength	λ	1245	1295	1320	1340	1352	1380	nm
Spectral Width	Δλ		120	145	177	190	215	nm
Power Supply Current	Icc	_	108	114	125	150	150	mA
Data Rate	BR	0	-				125	Mbits/s

Typical values from -55°C to +125°C measured at 125 Mb/s.

Maximum Ratings 125H Transmitter

Parameter	Min	Max	Unit	
Storage Temperature	-65	+150	°C	
Lead Soldering Temp./Time*	_	240/10	°C/s	
Supply Voltage	-6.0		V	
Input Voltage	VEE	VCC + 0.5	٧	
Differential Input Voltage		2.0	٧	

^{*} Leads only; not the entire package.

Electrical Characteristics 125H Receiver*

Parameter	Symbol	Min	—55°C	+25°C	+85°C	+125°C	Max	Unit
Output Voltage—Low Output Voltage—High	VOL	-1.810					-1.620	V
Output Voltage—nigh	Vон	-1.025		_			-0.880	V
Power Supply Current	lee	75	115	120	117	115	130	mA
Optical Sensitivity†	Pin	_	-38.5	-37.0	-35.5	-34.0	-31.5	dBm
Eye Width	EW	1.4	4.1	4.1	4.1	4.1	_	ns
Saturation Power‡	Psat	-17	-10	-11	-12.5	-14	- 8	dBm
Output Rise Time§	tr	0.5	1.4	1.4	1.4	1.4	2.5	ns
Output Fall Time§	tf	0.5	1.5	1.5	1.4	1.4	2.5	ns
Optical Wavelength	λ	1245			-		1380	nm
Data Rate	BR	10	-				125	Mbits/s

Typical values from -55°C to +125°C measured at 200 mb/s.

Maximum Ratings 125H Receiver

Parameter	Min	Max	Unit	
Storage Temperature	-65	+150	°C	
Lead Soldering Temp./Time*		240/10	°C/s	
Supply Voltage	4.5	5.46	V	
Output Current†	_	50	mA	

^{*} Leads only; not the entire package.

[†] Power at the end of a 1-meter, 100/140 μm GI, NA = 0.29 fiber pigtail.

[‡] Measured with a 1010 . . . pattern.

[§] Measured between the 10% and 90% points.

[†] Expressed as average power and at 2.5 x 10 ⁻¹⁰ BER.

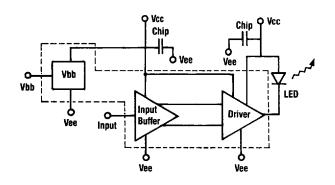
[‡] Includes jitter and duty cycle distortion; measured with a PRW and –18 dBm peak power input; includes effect of test instrumentation.

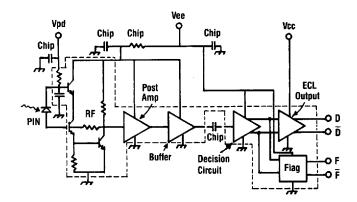
[§] Measured between the 10% and 90% points.

[†] From data and data outputs of the receiver.

Transmitter Block Diagram

Receiver Block Diagram





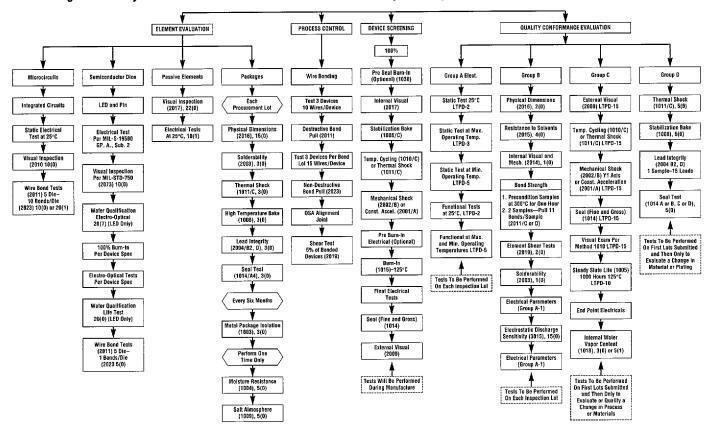
Quality Process

The 125H Military Data Link is manufactured on a dedicated

production line. The quality process in effect on the production line meets the intent of MIL-I-45208. Screening

and test procedures are identified in the following process flow chart.

Screening and Quality Conformance Test Procedures for 125H Military Fiber Optic Transmitters and Receivers*



^{*} The first number in parentheses refers to a test method or procedure of MIL-STD-883C.

The second number, where applicable, denotes the test sample size followed by the allowable failures in parentheses.

Printed-Wiring Board Layout

As with any sensitive or high-speed electronic component, to obtain optimum performance from the fiber optic transceiver, careful attention must be given to the printed-wiring board. The routing of sensitive input traces relative to other components and signal lines must be considered in great detail. Data lines must be of controlled impedance and properly terminated to minimize reflections that might degrade performance. Power-supply pins must be protected from noisy operating conditions by proper filtering.

Printed-Wiring Board

As a minimum, a double-sided printed wiring board having a large ground plane on the component side should be utilized. In applications where a large number of other devices are included on the circuit card, a multilayer circuit board is preferred. This allows for the separation of power and ground connections, and provides isolation for sensitive traces from high level signals that might couple to the sensitive inputs. In either case, the ideal approach is to have the ground plane as close to the transceiver as possible and to cover as much of the printed wiring board as possible.

Lavout Considerations

A fiber-optic receiver employs a very high-gain, wide-bandwidth, transimpedance amplifier. The amplifier detects and amplifies signal levels that are only nanoamperes in amplitude. Any unwanted signals that couple into the receiver circuitry cause a decrease in the receiver's sensitivity and can also degrade the performance of the receiver's signal-detect indicators.

To minimize the coupling of unwanted noise into the receiver, route transmitter-input traces and other traces carrying high-level signals as far away as possible from the receiver pins. If wide separation of the receiver pins from other high-level signal lines is not possible, the receiver pins and traces connected to them should be shielded by placing a ground trace between the receiver's pins and connecting traces and other high-level signal paths.

Power Supply Filtering

Noise that couples into the transceiver through the power-supply pins can also degrade device performance. High quality 0.1 μ F capacitors, rated for RF applications, should be placed as close as physically possible to the VCC pins.

In some instances, especially on double-sided circuit boards in noisy environments, it may be necessary to provide separate filtering for the VPD bias pin on the receiver. Do this by placing a separate 0.1 µF capacitor from pin 2 to ground, and connecting pin 2 to VCC through a separate ferrite bead.

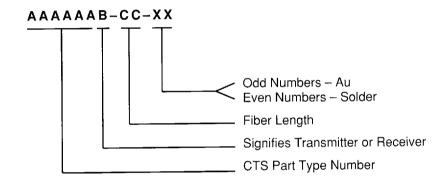
Data Lines

The signals on the data lines typically have rise and fall times on the order of 1 ns to 2 ns. If the data lines are not properly handled, the fast transitions cause EMI problems as well as electrical reflections and excessive ringing, which degrade the performance of the transceiver. When laying out the traces for the data lines, follow high-speed ECL design guidelines as described in the Motorola MECL System Design Handbook.

- All high-speed output lines must use controlled-impedance traces and have the termination impedance match the trace impedance. Controlled-impedance interruptions must be avoided (i.e., 90° bends, etc.), and paired lines (i.e., DATA and DATA) should be of equal length.
- Each output line should be terminated at the end of the line and must have a bypass capacitor on the voltage side of the resistor for each termination resistor.
- Data and signal-detect output lines should be as short and straight as possible and should be isolated from noise sources (and each other) to prevent noise from feeding back into the receiver.

Ordering Information

ORDER PART NUMBER			FIDED	CONNECTOR OF THE PROPERTY OF T					
TRANSMITTER	RECEIVER	LEAD FINISH	FIBER LENGTH (INCHES)	MFG.	FRANKS AND THE STATE OF THE STA	CONTACT TYPE	USED IN CONNECTOR BODY STYLE	CONTACT	
902392T-18-01 902392T-18-02	902393R-18-01 902393R-18-02	Au Solder	18.00 ± 2.00	AT&T	MIL-ST P2050A C-140	Bayonêt	日本ので、 1000年 100	MIL-C-83522/E1-02	
902392T-18-03 902392T-18-04	902393R-18-03 902393R-18-04	Au Solder	18.00 ± 2.00	Bendix	CF-198036-017	1	MIL-C-38999 Series III	MIL-T-29504/4	
902392T-18-05 902392T-18-06	902393R-18-05 902393R-18-06	Au Solder	18.00 ± 2.00	Bendix	CF-198060-017	Socket		MIL-T 29504/5	
902392T-18-07 902392T-18-08	902393R-18-07 902393R-18-08	Au Solder	5.630 ± .500	Hughes	4568331-056-1H	Socket	MIL-C-28876, MIL-C-83526 Size 16	## MIL-T-29504/2 @	



Order From:

CTS CORPORATION, MICROELECTRONICS/1201 Cumberland Avenue/West Lafayette, Indiana 47906/(317) 463-2565/FAX: (317) 497-5399

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