

9941743 XICOR INC

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Xicor[®]

DATA SHEET SUPPLEMENT

16K

Mil-Std-883C

X2816BMB

2048 x 8 Bit

Electrically Erasable PROM**A.C. AND D.C. REQUIREMENTS FOR CHIP ERASE**

The X2816BMB provides a mode of operation that erases the entire contents of the memory in one write cycle. This mode is entered by raising $\overline{OE}$ to between +20V and +22V, placing all I/Os at V_{IH} and performing a standard write operation. The erasure will be completed in 10 ms.

With the exception of V_{OE} , all device A.C. and D.C. parameters are the same as those for normal operation.

The chip erase operation is only guaranteed on Mil-Std-883C product.

A.C. CHIP ERASE CHARACTERISTICS

Symbol	Parameter	Limits		Units
		Min.	Max.	
t_{CS}	$\overline{OE}$ to $\overline{WE}$ Setup Time	10		ns
t_{DSW}	Data to $\overline{WE}$ Setup Time	10		ns
t_{DH}	Data Hold Time	50		ns
t_{WP}	Write Pulse Width	175		ns
t_{CH}	$\overline{OE}$ Hold Time	50		ns
t_{OS}	V_{OE} Setup Time	10		ns
t_{OEH}	V_{OE} Hold Time	10		ns
t_{WC}	Write Cycle Time		10	ms

D.C. CHARACTERISTIC FOR V_{OE}

Symbol	Parameter	Limits		Units	Note
		Min.	Max.		
V_{OE}	$\overline{OE}$ Chip Erase Voltage	+20	+22	V	$I_{OE} = 10 \mu A$

Chip Erase Cycle