TOSHIBA INSULATED GATE BIPOLAR TRANSISTOR SILICON N CHANNEL IGBT

GT50J322

THE 4TH GENERATION CURRENT RESONANCE INVERTER SWITCHING APPLICATIONS

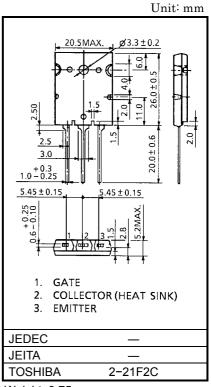
• FRD Included Between Emitter and Collector

• Enhancement-Mode

 $\begin{array}{ll} \bullet & \mbox{High Speed} & : t_f = 0.25 \mu s \mbox{ (Typ.) (I$_C} = 50 \mbox{A}) \\ \bullet & \mbox{Low Saturation Voltage} & : V_{CE} \mbox{ (sat)} = 2.1 \mbox{ (Typ.) (I$_C} = 50 \mbox{A}) \end{array}$

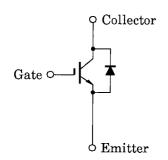
MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Collector-Emitter Voltage		V _{CES}	600	V	
Gate-Emitter Voltage		V _{GES}	±20	V	
Collector Current	DC	I _C	50	А	
	1ms	I _{CP}	100	Α .	
Emitter-Collector Foward Current	DC	I _F	30	А	
	1ms	I _{FP}	60	τ.	
Collector Power Dissipation (Tc = 25°C)		PC	130	W	
Junction Temperature		Тј	150	°C	
Storage Temperature Range		T _{stg}	-55~150	°C	



Weight: 9.75 g

EQUIVALENT CIRCUIT

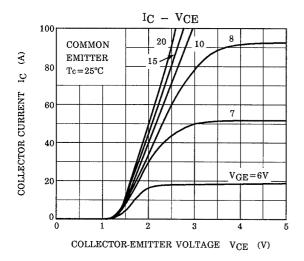


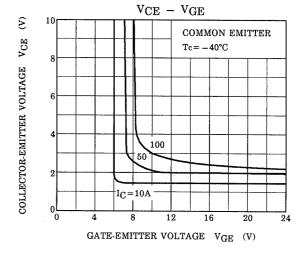
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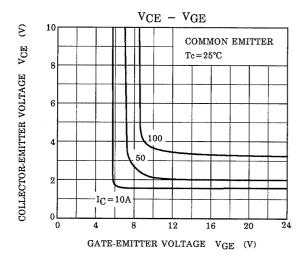
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

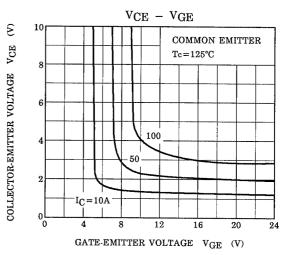
CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Gate Leakage Current		I _{GES}	V _{GE} = ±20V, V _{CE} = 0	_	_	±500	nA
Collector Cut-off Current		I _{CES}	V _{CE} = 600V, V _{GE} = 0	_	_	1.0	mA
Gate-Emitter Cut-off Voltage		V _{GE} (OFF)	I _C = 50mA, V _{CE} = 5V	3.0	_	6.0	V
Collector-Emitter Saturation Voltage		V _{CE} (sat)	I _C = 50A, V _{GE} = 15V	_	2.1	2.8	V
Input Capacitance		C _{ies}	V _{CE} = 10V, V _{GE} = 0, f = 1MHz	_	2500	_	pF
Switching Time	Rise Time	t _r	15V 0 1 2 39Ω 15V 300V	_	0.20	_	- μs
	Turn-on Time	t _{on}		_	0.30	_	
	Fall Time	t _f		_	0.25	0.40	
	Turn-off Time	t _{off}		_	0.40	_	
Forward Voltage		V _F	I _F = 30A, V _{GE} = 0	_	_	2.0	V
Reverse Recovery Time		t _{rr}	I _F = 30A, V _{GE} = 0 di / dt = -100A / μs	_	_	0.2	μs
Thermal Resistance		R _{th (j-c)}	IGBT	_	_	0.96	°C/W
Thermal Resistance		R _{th (j-c)}	Diode	_	_	1.56	°C/W

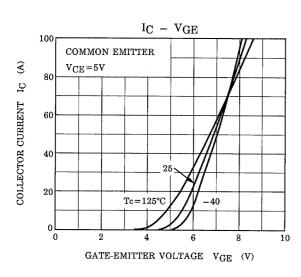
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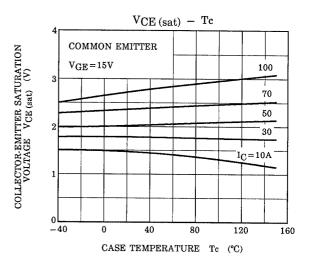


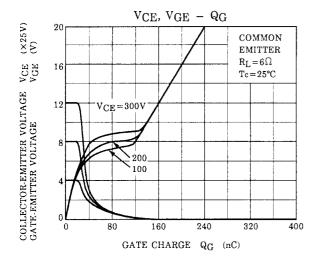


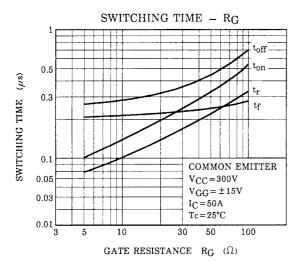


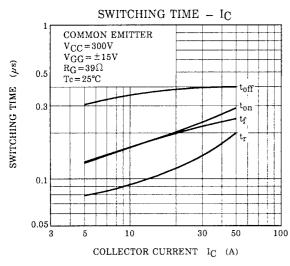


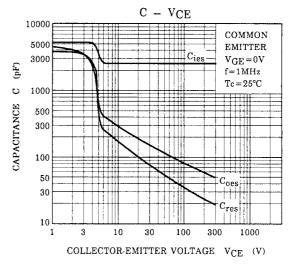


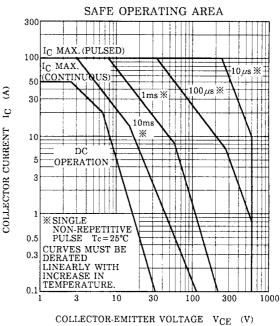




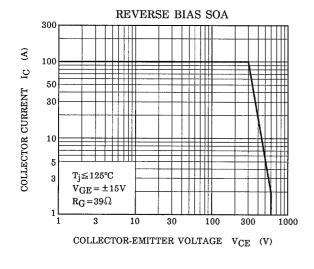


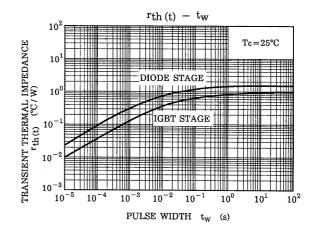


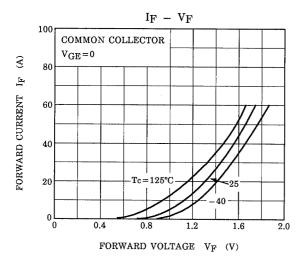


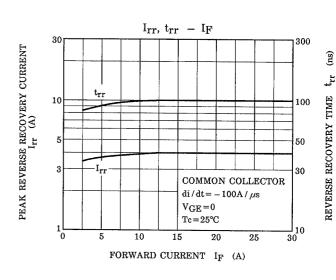


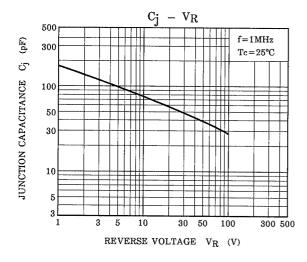
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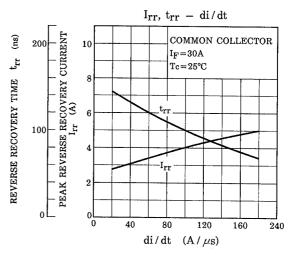












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