## Features

- Ultra-Low Forward Voltage Drop
- Excellent High Temperature Stability
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- Lead Free Finish, RoHS Compliant (Note 2)
- "Green" Molding Compound (No Br, Sb)


## Mechanical Data

- Case: D Pak (TO-252)
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Matte Tin Finish annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 ©3)
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.4 grams (approximate)


Maximum Ratings $@ T_{A}=25^{\circ} \mathrm{C}$ unless otherwise specified
Single phase, half wave, 60 Hz , resistive or inductive load.
For capacitance load, derate current by 20\%.

| Characteristic | Symbol | Value | Unit |
| :---: | :---: | :---: | :---: |
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | VRRM <br> $V_{\text {RWM }}$ <br> $V_{\text {RM }}$ | 45 | V |
| RMS Reverse Voltage | $\mathrm{V}_{\mathrm{R}(\mathrm{RMS})}$ | 31 | V |
| Average Rectified Output Current @ $\mathrm{T}_{\mathrm{C}}=110^{\circ} \mathrm{C}$ | Io | 10 | A |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load | IfsM | 125 | A |

Thermal Characteristics $@ \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ unless otherwise specified

| Characteristic | Symbol | Value | Unit |
| :---: | :---: | :---: | :---: |
| Typical Thermal Resistance |  |  |  |
| Thermal Resistance Junction to Case |  | 2.0 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Thermal Resistance Junction to Ambient (Note 3) | $\mathrm{R}_{\theta \mathrm{JA}}$ | 34 |  |
| Operating and Storage Temperature Range | TJ, Tstg | -65 to +150 | ${ }^{\circ} \mathrm{C}$ |

Electrical Characteristics $@ \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ unless otherwise specified

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Forward Voltage Drop | $\mathrm{V}_{\mathrm{F}}$ | - | - | $\begin{aligned} & 0.57 \\ & 0.54 \end{aligned}$ | V | $\begin{aligned} & \mathrm{I}_{\mathrm{F}}=10 \mathrm{~A}, \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C} \\ & \mathrm{I}_{\mathrm{F}}=10 \mathrm{~A}, \mathrm{~T}_{\mathrm{J}}=125^{\circ} \mathrm{C} \end{aligned}$ |
| Leakage Current (Note 1) | $\mathrm{I}_{\mathrm{R}}$ | - | - | 0.5 | mA | $\mathrm{V}_{\mathrm{R}}=45 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ |

Notes: 1. Short duration pulse test used to minimize self-heating effect.
2. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes.
. Polymide PCB 2 oz. Copper, minimum recommended pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.


Fig. 1 Forward Power Dissipation


Fig. 3 Typical Reverse Characteristics


Fig. 5 Forward Current Derating Curve


Fig. 2 Typical Forward Characteristics

$\mathrm{V}_{\mathrm{R}}$, DC REVERSE VOLTAGE (V)
Fig. 4 Total Capacitance vs. Reverse Voltage


Fig. 6 Operating Temperature Derating

Ordering Information (Note 4)

| Part Number | Case | Packaging |
| :---: | :---: | :---: |
| SBR10U45D1-13 | D Pak (TO-252) | 80 pieces/tube |
| 2500 pieces/reel |  |  |

Notes: 4. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

## Marking Information



SBR10U45 = Product Type Marking Code
$A B=$ Foundry and Assembly Code
YYWW = Date Code Marking
YY = Last two digits of year, $(\mathrm{ex}: 07=2007)$
WW = Week (01-52)

## Package Outline Dimensions



## Suggested Pad Layout



| Dimensions | Value (in mm) |
| :---: | :---: |
| $\mathbf{Z}$ | 11.6 |
| $\mathbf{X 1}$ | 1.5 |
| $\mathbf{X 2}$ | 7.0 |
| $\mathbf{Y 1}$ | 2.5 |
| $\mathbf{Y 2}$ | 7.0 |
| $\mathbf{C}$ | 6.9 |
| $\mathbf{E 1}$ | 2.3 |

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