

To all our customers

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## **Regarding the change of names mentioned in the document, such as Mitsubishi Electric and Mitsubishi XX, to Renesas Technology Corp.**

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The semiconductor operations of Hitachi and Mitsubishi Electric were transferred to Renesas Technology Corporation on April 1st 2003. These operations include microcomputer, logic, analog and discrete devices, and memory chips other than DRAMs (flash memory, SRAMs etc.) Accordingly, although Mitsubishi Electric, Mitsubishi Electric Corporation, Mitsubishi Semiconductors, and other Mitsubishi brand names are mentioned in the document, these names have in fact all been changed to Renesas Technology Corp. Thank you for your understanding. Except for our corporate trademark, logo and corporate statement, no changes whatsoever have been made to the contents of the document, and these changes do not constitute any alteration to the contents of the document itself.

Note : Mitsubishi Electric will continue the business operations of high frequency & optical devices and power devices.

Renesas Technology Corp.  
Customer Support Dept.  
April 1, 2003

**PRELIMINARY**  
 Notice: This is not a final specification.  
 Some parametric limits are subject to change.

MITSUBISHI POWER MOSFET

# FY4AEJ-03

HIGH-SPEED SWITCHING USE  
 Nch/Pch POWER MOSFET

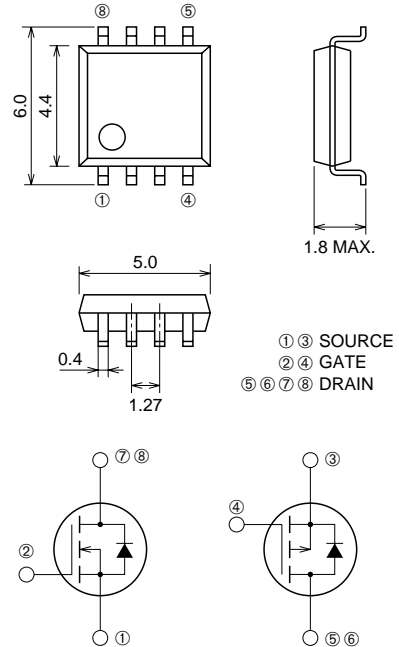
## FY4AEJ-03



- 4V DRIVE
- $V_{DSS}$  .....  $\pm 30V$
- $r_{DS(ON)}$  (MAX) .....  $30/80m\Omega$
- $I_D$  .....  $\pm 4A$

## OUTLINE DRAWING

Dimensions in mm



**SOP-8**

## APPLICATION

Motor control, Lamp control, Solenoid control,  
 DC-DC converter, Li-ionbattery, notebook p/c, etc

## MAXIMUM RATINGS (Tc = 25°C)

Symbol	Parameter	Conditions	Ratings		Unit
			n-ch	p-ch	
$V_{DSS}$	Drain-source voltage	$V_{GS} = 0V$	30	-30	V
$V_{GSS}$	Gate-source voltage	$V_{DS} = 0V$	$\pm 20$	$\pm 20$	V
$I_D$	Drain current		4	-4	A
$I_{DM}$	Drain current (Pulsed)		28	-28	A
$I_{DA}$	Avalanche current (Pulsed)	$L = 10\mu H$	4	-4	A
$I_S$	Source current		1.7	-1.7	A
$I_{SM}$	Source current (Pulsed)		6.8	-6.8	A
$P_D$	Maximum power dissipation		1.6	1.6	W
$T_{ch}$	Channel temperature		-55~+150		°C
$T_{stg}$	Storage temperature		-55~+150		°C
—	Weight	Typical value	0.07		g

Aug. 1999

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**ELECTRICAL CHARACTERISTICS (Tch = 25°C)**

**N-ch**

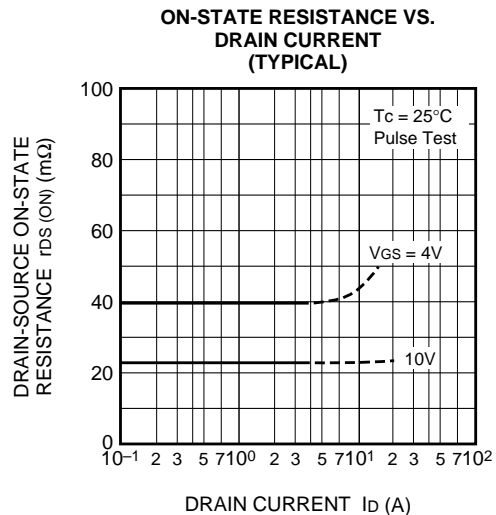
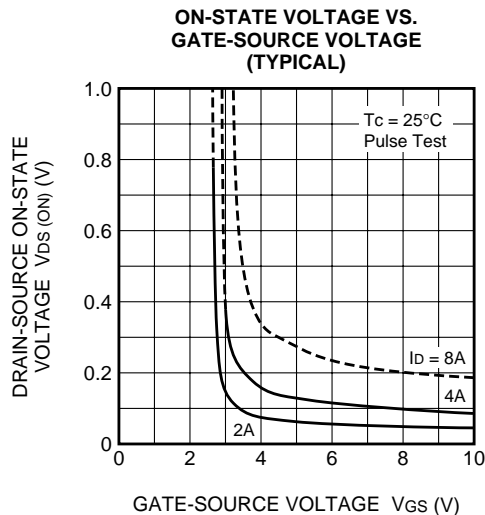
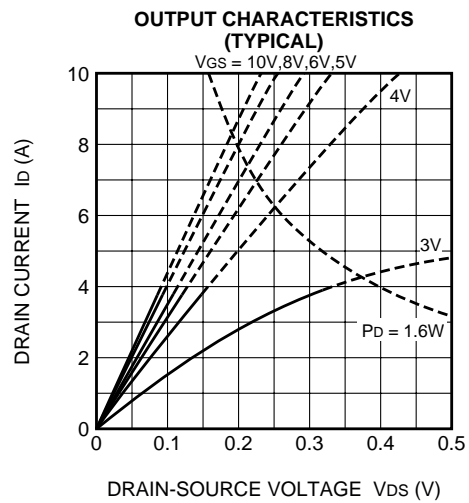
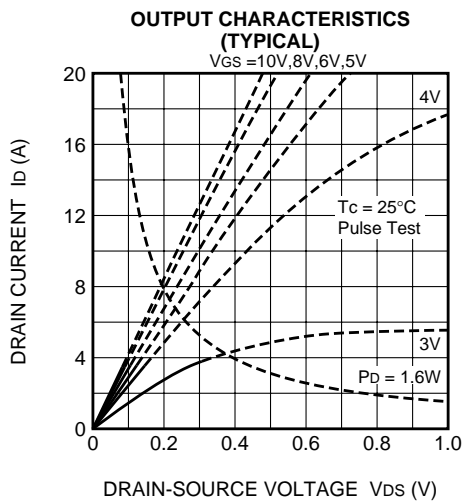
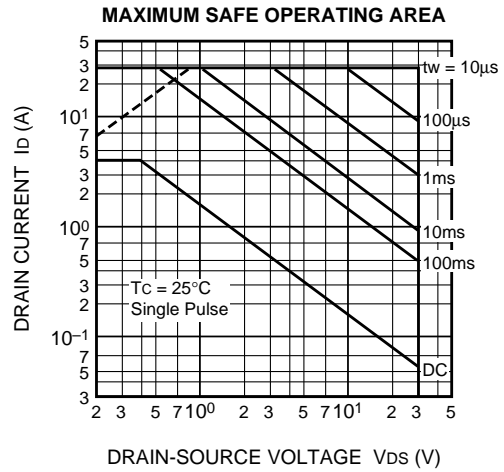
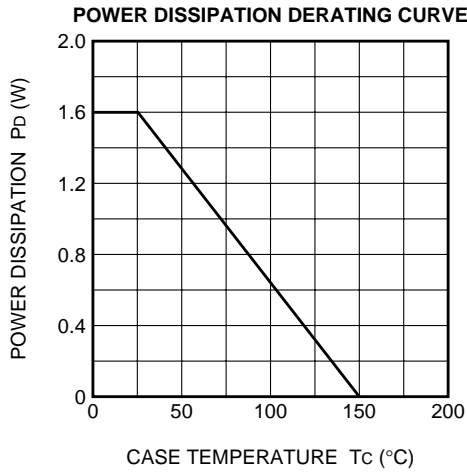
Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V (BR) DSS	Drain-source breakdown voltage	Id = 1mA, VGS = 0V	30	—	—	V
IGSS	Gate-source leakage current	VGS = ±20V, VDS = 0V	—	—	±0.1	µA
IDSS	Drain-source leakage current	VDS = 30V, VGS = 0V	—	—	0.1	mA
VGS (th)	Gate-source threshold voltage	Id = 1mA, VDS = 10V	1.0	1.5	2.0	V
rDS (ON)	Drain-source on-state resistance	Id = 4A, VGS = 10V	—	23	30	mΩ
rDS (ON)	Drain-source on-state resistance	Id = 2A, VGS = 4V	—	40	55	mΩ
yfs	Forward transfer admittance	Id = 4A, VDS = 10V	—	8	—	S
Ciss	Input capacitance	VDS = 10V, VGS = 0V, f = 1MHz	—	550	—	pF
Coss	Output capacitance		—	220	—	pF
Crss	Reverse transfer capacitance		—	115	—	pF
td (on)	Turn-on delay time	VDD = 15V, Id = 2A, VGS = 10V, RGEN = RGS = 50Ω	—	12	—	ns
tr	Rise time		—	20	—	ns
td (off)	Turn-off delay time		—	40	—	ns
tf	Fall time		—	40	—	ns
VSD	Source-drain voltage	IS = 1.7A, VGS = 0V	—	0.75	1.10	V
Rth (ch-a)	Thermal resistance	Channel to ambient	—	—	78.1	°C/W
trr	Reverse recovery time	IS = 1.7A, dis/dt = -50A/µs	—	100	—	ns

**P-ch**

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V (BR) DSS	Drain-source breakdown voltage	Id = 1mA, VGS = 0V	-30	—	—	V
IGSS	Gate-source leakage current	VGS = ±20V, VDS = 0V	—	—	±0.1	µA
IDSS	Drain-source leakage current	VDS = -30V, VGS = 0V	—	—	-0.1	mA
VGS (th)	Gate-source threshold voltage	Id = -1mA, VDS = -10V	-1.5	-2.0	-2.5	V
rDS (ON)	Drain-source on-state resistance	Id = -4A, VGS = -10V	—	60	80	mΩ
rDS (ON)	Drain-source on-state resistance	Id = -2A, VGS = -4V	—	115	180	mΩ
yfs	Forward transfer admittance	Id = -4A, VDS = -10V	—	6	—	S
Ciss	Input capacitance	VDS = -10V, VGS = 0V, f = 1MHz	—	680	—	pF
Coss	Output capacitance		—	180	—	pF
Crss	Reverse transfer capacitance		—	90	—	pF
td (on)	Turn-on delay time	VDD = -15V, Id = -2A, VGS = -10V, RGEN = RGS = 50Ω	—	10	—	ns
tr	Rise time		—	15	—	ns
td (off)	Turn-off delay time		—	50	—	ns
tf	Fall time		—	30	—	ns
VSD	Source-drain voltage	IS = -1.7A, VGS = 0V	—	-0.88	-1.20	V
Rth (ch-a)	Thermal resistance	Channel to ambient	—	—	78.1	°C/W
trr	Reverse recovery time	IS = -1.7A, dis/dt = 50A/µs	—	70	—	ns

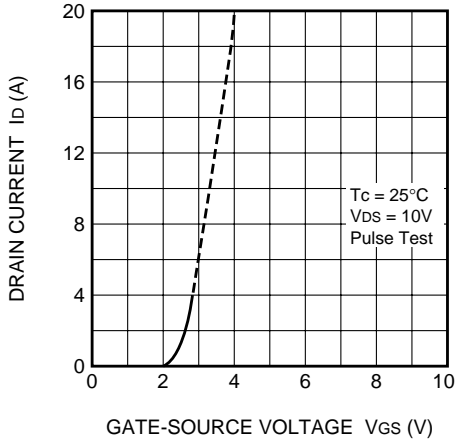
**PRELIMINARY**  
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**PERFORMANCE CURVES (N-ch)**

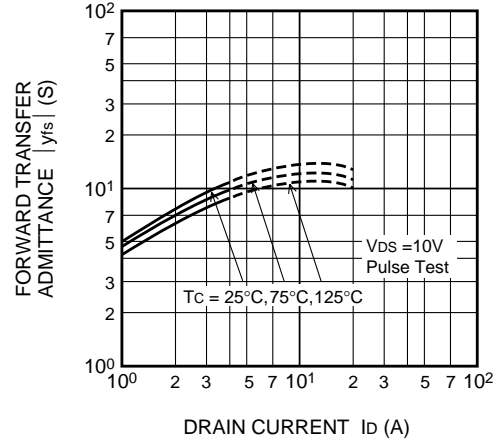


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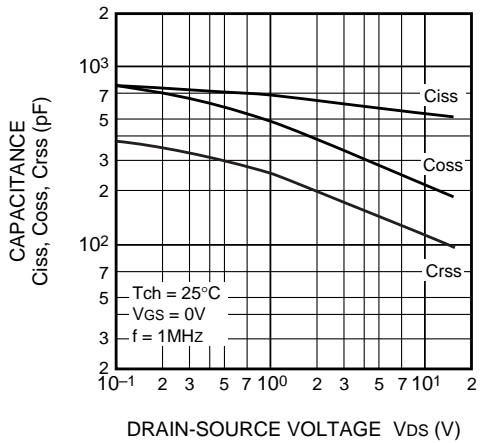
**TRANSFER CHARACTERISTICS (TYPICAL)**



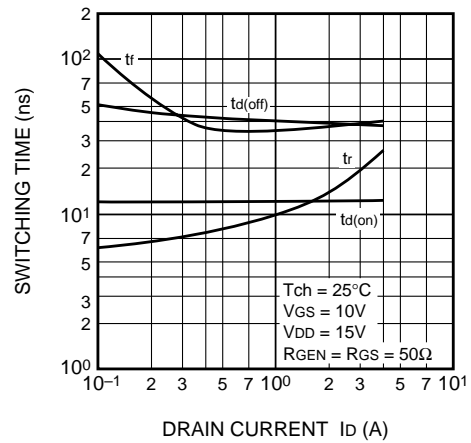
**FORWARD TRANSFER ADMITTANCE VS. DRAIN CURRENT (TYPICAL)**



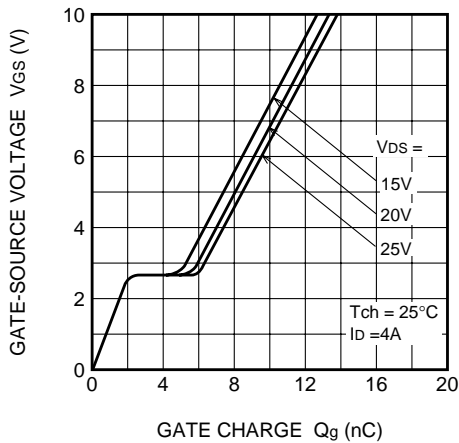
**CAPACITANCE VS. DRAIN-SOURCE VOLTAGE (TYPICAL)**



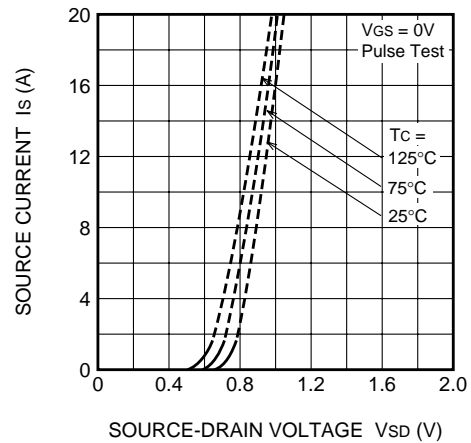
**SWITCHING CHARACTERISTICS (TYPICAL)**



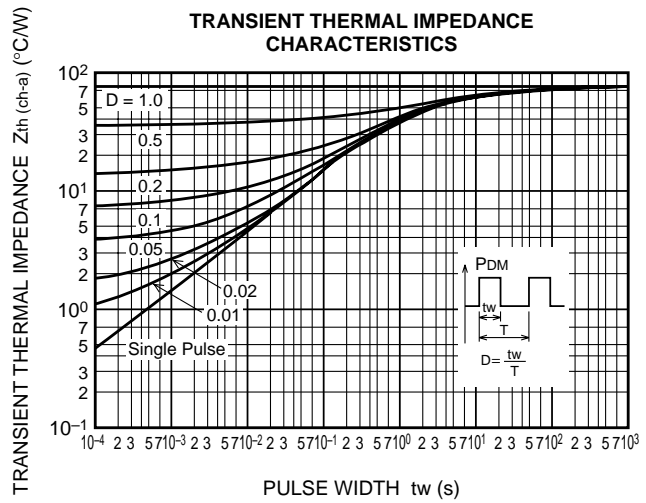
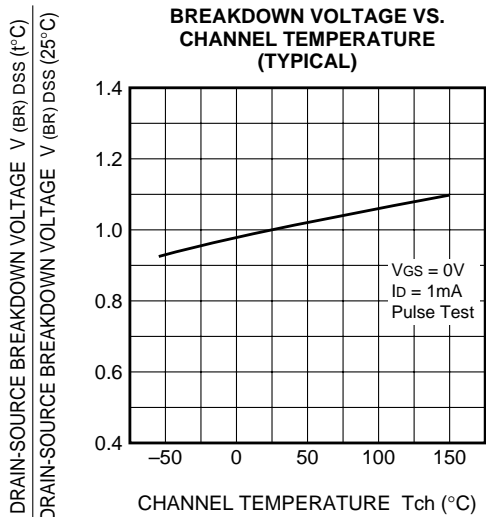
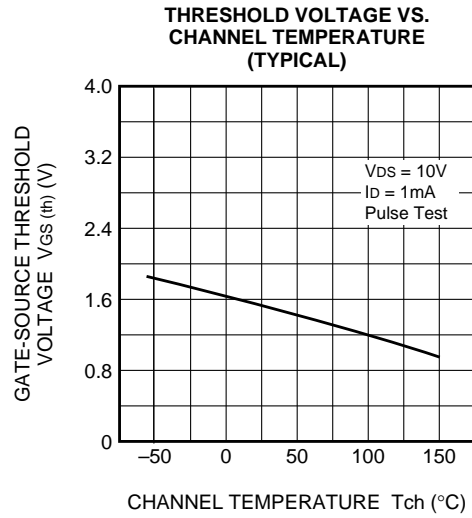
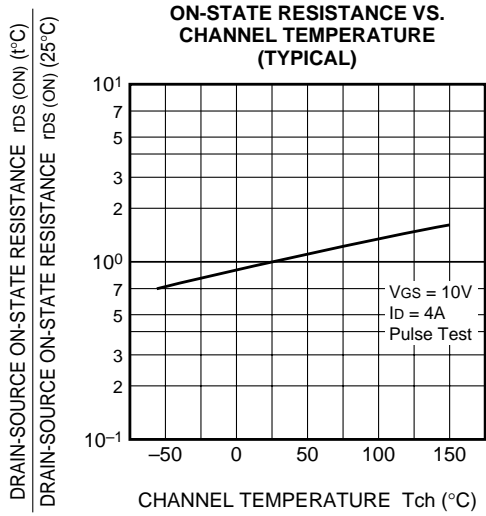
**GATE-SOURCE VOLTAGE VS. GATE CHARGE (TYPICAL)**



**SOURCE-DRAIN DIODE FORWARD CHARACTERISTICS (TYPICAL)**

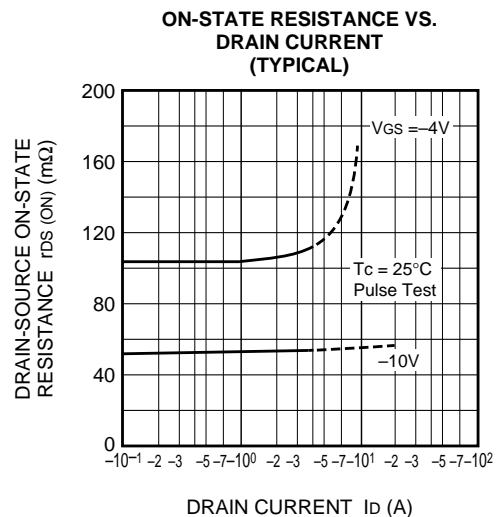
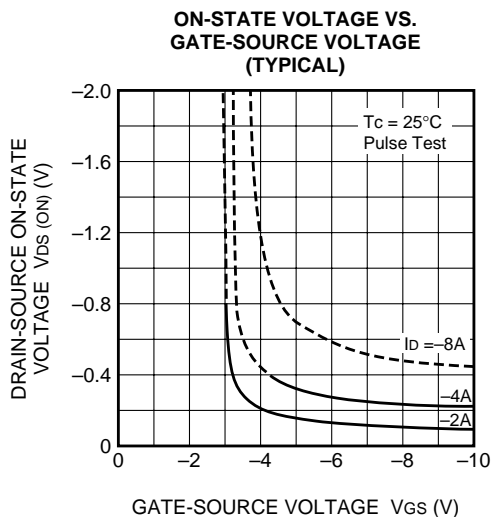
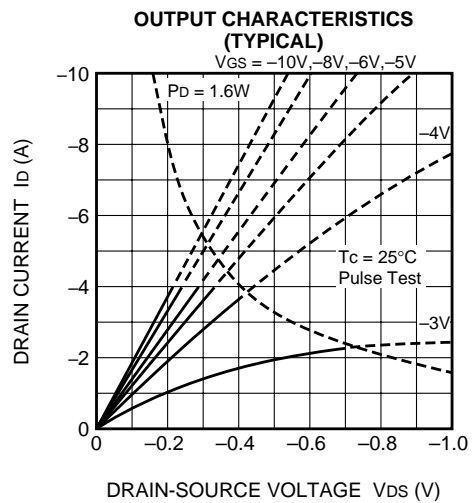
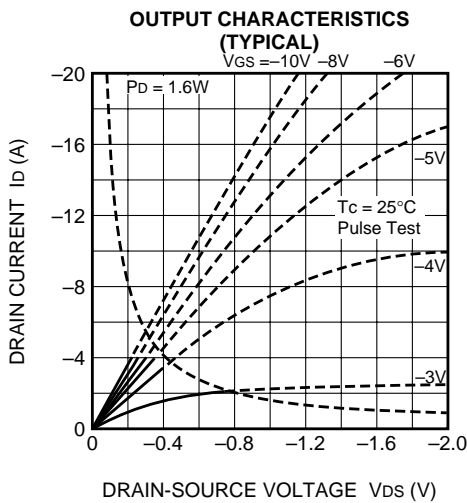
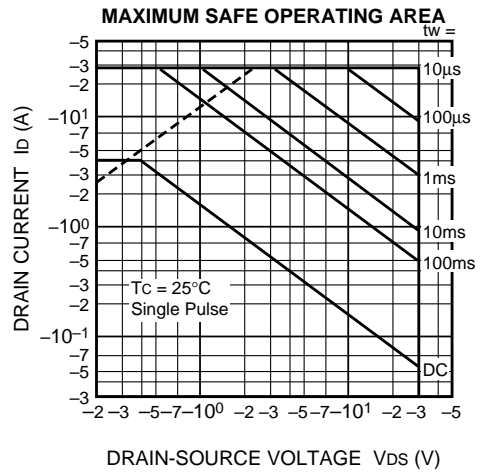
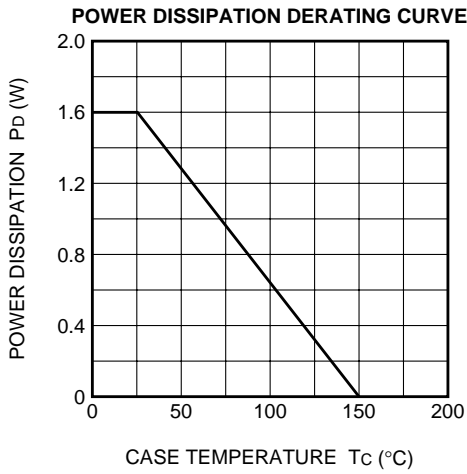


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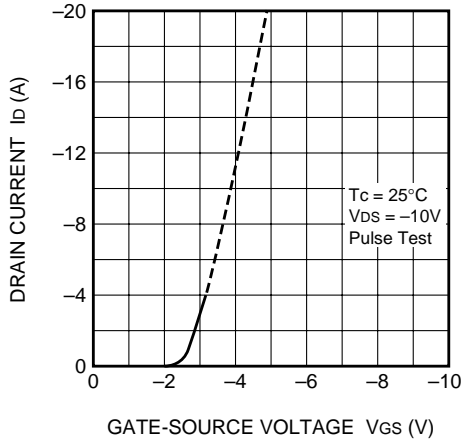
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PERFORMANCE CURVES (P-ch)

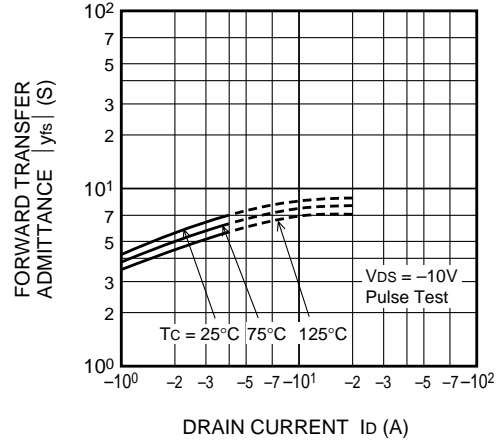


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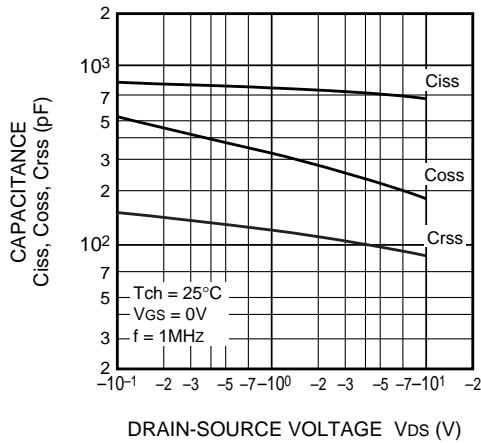
**TRANSFER CHARACTERISTICS (TYPICAL)**



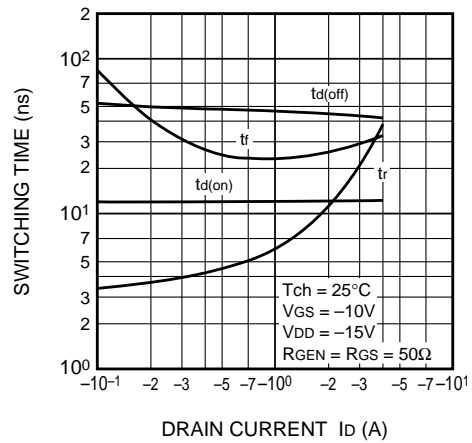
**FORWARD TRANSFER ADMITTANCE VS. DRAIN CURRENT (TYPICAL)**



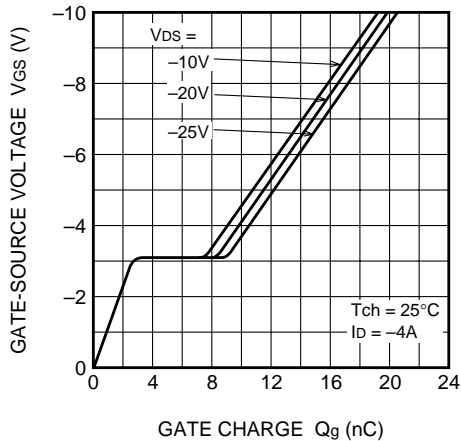
**CAPACITANCE VS. DRAIN-SOURCE VOLTAGE (TYPICAL)**



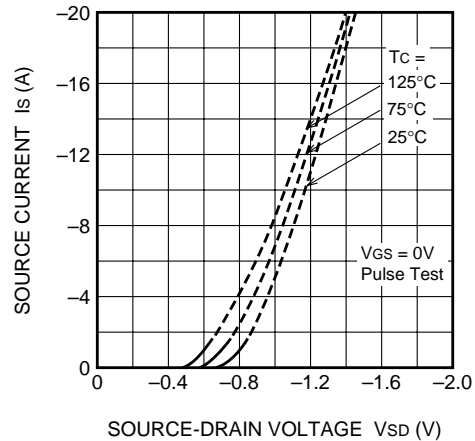
**SWITCHING CHARACTERISTICS (TYPICAL)**



**GATE-SOURCE VOLTAGE VS. GATE CHARGE (TYPICAL)**



**SOURCE-DRAIN DIODE FORWARD CHARACTERISTICS (TYPICAL)**





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