

Micro MINI E0C6006

4-bit Single Chip Microcomputer



- E0C6200B Core CPU
- Low Voltage and Low Power
- Built-in LCD Driver
- Ir Output

■ DESCRIPTION

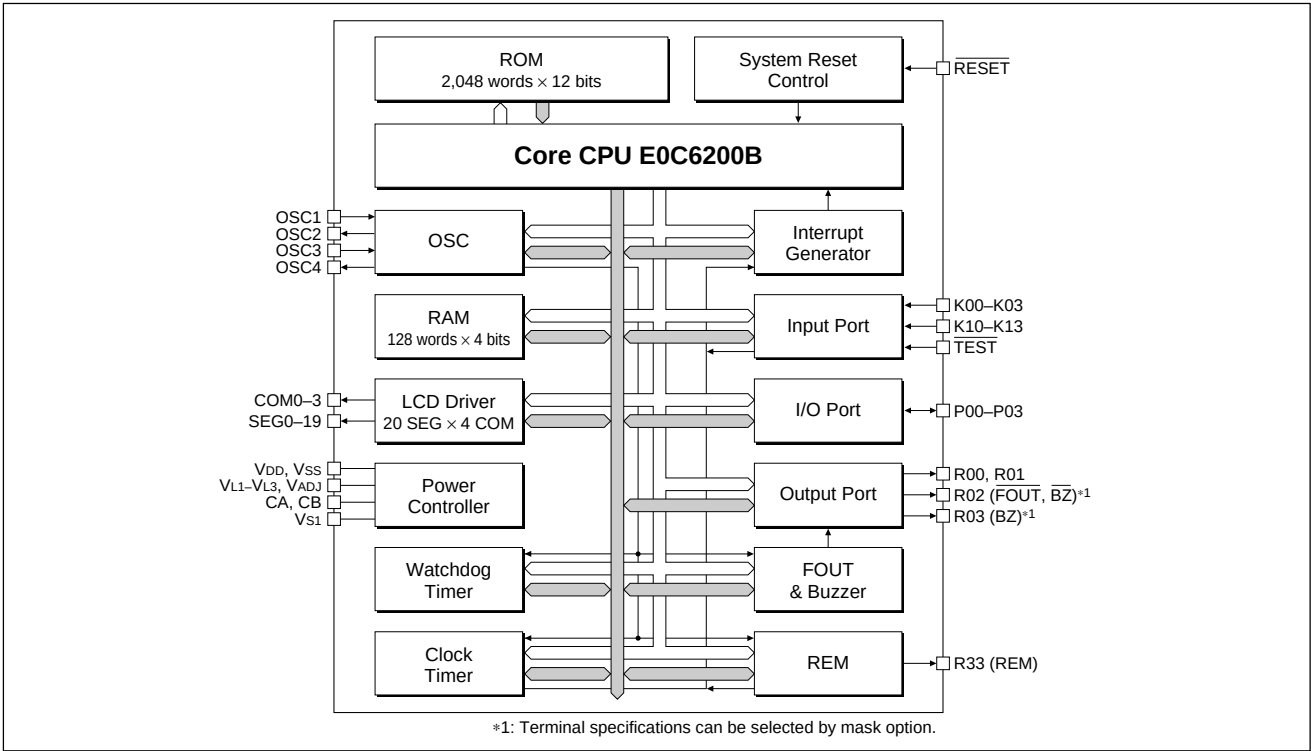
Micro MINI "E0C6006" is a single chip microcomputer for battery-driven products with 7-segment LCD display. It achieves low cost performance, and is suitable for a product added some feature instead of standard IC. It consists that Seiko Epson's original core CPU E0C6200B, LCD driver (20 segments × 4 commons), 128 words RAM, 2K words ROM, clock timer and so on.

■ FEATURES

- CMOS LSI 4-bit parallel processing
- Clock 32.768kHz/455kHz
- Instruction set 100 instructions
- ROM capacity 2K × 12 bits
- RAM capacity 128 × 4 bits
- I/O port I: 8 bits (with pull-down resistor selectable by mask option)
O: 4 bits
I/O: 4 bits
- Supply voltage detector (SVD) No support
- Clock timer 1ch.
- LCD driver 20 segments × 4/3 commons
- Interrupt External : 2 input interrupts
Internal : 3 timer interrupts
: 1 remote control output control interrupt
- Operation voltage 2.2 to 3.5V
- Power consumption 2.0μA (32.768kHz X'tal, 3.0V, HALT)
9.0μA (32.768kHz X'tal, 3.0V, RUN)
- Package Die form (pad pitch = 130μm), QFP6-60pin or QFP13-64pin

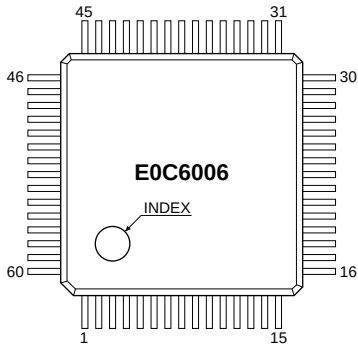
E0C6006

■ BLOCK DIAGRAM



■ PIN CONFIGURATION

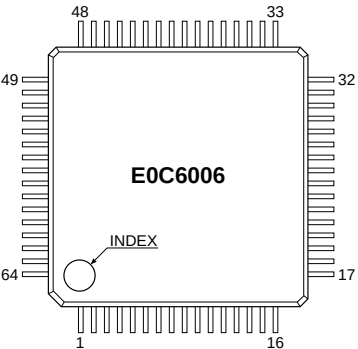
QFP6-60pin



No.	Pin name	No.	Pin name	No.	Pin name	No.	Pin name
1	N.C.	16	R33(REM)	31	RESET	46	VL3
2	N.C.	17	SEG0	32	SEG12	47	VADJ
3	N.C.	18	SEG1	33	SEG13	48	CA
4	K00	19	SEG2	34	SEG14	49	CB
5	K01	20	SEG3	35	SEG15	50	VSS
6	K02	21	SEG4	36	SEG16	51	OSC4
7	K03	22	SEG5	37	SEG17	52	OSC3
8	K10	23	SEG6	38	SEG18	53	VS1
9	K11	24	SEG7	39	SEG19	54	OSC2
10	K12	25	SEG8	40	COM3	55	OSC1
11	K13	26	SEG9	41	COM2	56	VDD
12	R00	27	SEG10	42	COM1	57	P03
13	R01	28	N.C.	43	COM0	58	P02
14	R02	29	SEG11	44	VL1	59	P01
15	R03	30	TEST	45	VL2	60	P00

N.C. = No connection

QFP13-64pin



No.	Pin name	No.	Pin name	No.	Pin name	No.	Pin name
1	N.C.	17	N.C.	33	RESET	49	N.C.
2	N.C.	18	R33(REM)	34	SEG12	50	VADJ
3	N.C.	19	SEG0	35	SEG13	51	CA
4	N.C.	20	SEG1	36	SEG14	52	CB
5	K00	21	SEG2	37	SEG15	53	VSS
6	K01	22	SEG3	38	SEG16	54	OSC4
7	K02	23	SEG4	39	SEG17	55	OSC3
8	K03	24	SEG5	40	SEG18	56	VS1
9	K10	25	SEG6	41	SEG19	57	OSC2
10	K11	26	SEG7	42	COM3	58	OSC1
11	K12	27	SEG8	43	COM2	59	VDD
12	K13	28	SEG9	44	COM1	60	P03
13	R00	29	SEG10	45	COM0	61	P02
14	R01	30	SEG11	46	VL1	62	P01
15	R02	31	N.C.	47	VL2	63	P00
16	R03	32	TEST	48	VL3	64	N.C.

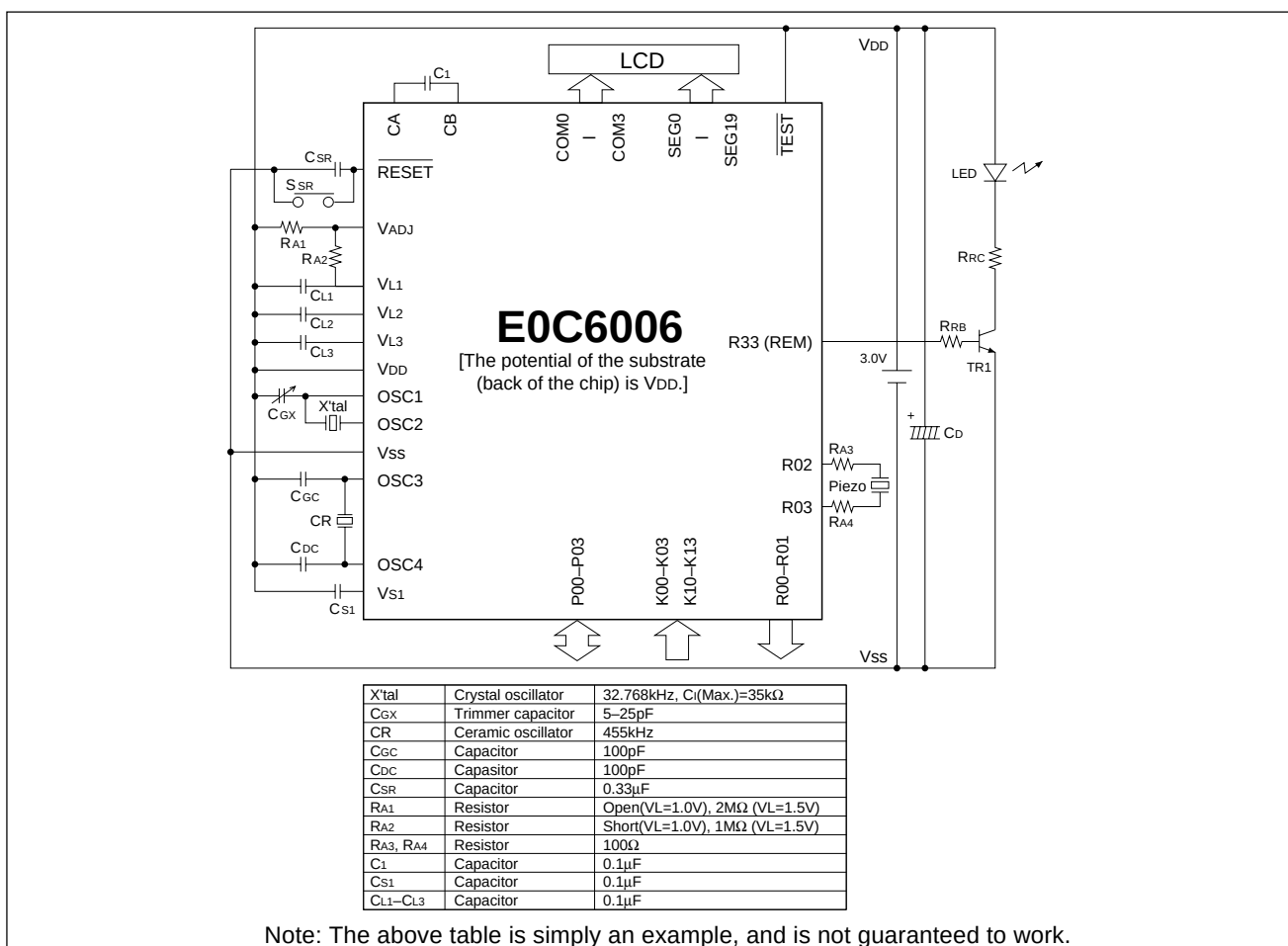
N.C. = No connection

■ PIN DESCRIPTION

Pin name	Pin No.		In/Out	Function
	QFP6-60pin	QFP13-64pin		
VDD	56	59	(I)	Power supply pin (+)
VSS	50	53	(I)	Power supply pin (-)
Vs1	53	56	–	Oscillation and internal logic system voltage output pin
VL1	44	46	–	LCD drive voltage output pin
VL2	45	47	–	LCD drive voltage output pin
VL3	46	48	–	LCD drive voltage output pin
CA, CB	48, 49	51, 52	–	Boost capacitor connecting pin
VADJ	47	50	I	VL1 adjustment input pin
OSC1	55	58	I	Oscillation input pin (crystal)
OSC2	54	57	O	Oscillation output pin (crystal)
OSC3	52	55	I	Oscillation input pin (ceramic or CR *)
OSC4	51	54	O	Oscillation output pin (ceramic or CR *)
K00–K03	4–7	5–8	I	Input port pin
K10–K13	8–11	9–12	I	Input port pin
P00–P03	60–57	63–60	I/O	I/O port pin
R00, R01	12, 13	13, 14	O	Output port pin
R02	14	15	O	Output port pin, BZ or FOUT output pin *
R03	15	16	O	Output port pin or BZ output pin *
R33(REM)	16	18	O	Remote control carrier output port pin
SEG0–19	17–27, 29, 32–39	19–30, 34–41	O	LCD segment output pin or DC output pin *
COM0–3	43–40	45–42	O	LCD common output pin (1/3 duty or 1/4 duty are selectable *)
RESET	31	33	I	Initial reset input pin
TEST	30	32	I	Input pin for test

* Can be selected by mask option

■ BASIC EXTERNAL CONNECTION DIAGRAM



Note: The above table is simply an example, and is not guaranteed to work.

■ ELECTRICAL CHARACTERISTICS

● Absolute Maximum Ratings

(V_{DD}=0V)

Rating	Symbol	Value	Unit
Supply voltage	V _{SS}	-5.2 to 0.5	V
Input voltage (1)	V _I	V _{SS} - 0.3 to 0.3	V
Input voltage (2)	V _I OSC	V _{S1} - 0.3 to 0.3	V
Operating temperature	T _{opr}	-20 to 70	°C
Storage temperature	T _{stg}	-65 to 150	°C
Soldering temperature / time	T _{sol}	260°C, 10sec (lead section)	—
Permissible dissipation *1	P _D	250	mW

*1: In case of plastic package (QFP6-60pin, QFP13-64pin).

● Recommended Operating Conditions

(T_a=-20 to 70°C)

Condition	Symbol	Remark	Min.	Typ.	Max.	Unit
Supply voltage	V _{SS}	V _{DD} =0V	-3.5	-3.0	-2.2	V
Oscillation frequency	f _{OSC1}		—	32.768	—	kHz
	f _{OSC3}	Duty: 50±5%	50	455	600	kHz
LCD drive voltage	V _{L1}		-1.6	-1.03	—	V
CR oscillation external resistor	R _{CR}		100	140	500	kΩ

● DC Characteristics

(Unless otherwise specified: V_{DD}=0V, V_{SS}=-2.2 to -3.5V, V_{L3}=-3.0V, T_a=-20 to 70°C)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
High level input voltage (1)	V _{IH1}	K00-03, K10-13, P00-03	0.2·V _{SS}		0	V
Low level input voltage (1)	V _{IL1}	K00-03, K10-13, P00-03	V _{SS}		0.8·V _{SS}	V
High level input voltage (2)	V _{IH2}	RESET	0.1·V _{SS}		0	V
Low level input voltage (2)	V _{IL2}	RESET	V _{SS}		0.9·V _{SS}	V
High level input current	I _{IH}	V _{IH} =V _{DD}			1	μA
Low level input current	I _{IL1}	V _{IL1} =V _{SS}	-1			μA
	I _{IL2}	V _{IL2} =V _{SS}	-5		-0.35	μA
	I _{IL3}	V _{IL3} =V _{SS}	-5		-0.35	μA
	I _{IL4}	V _{IL4} =0.2·V _{SS}	-50			μA
	I _{IL5}	V _{IL5} =0.2·V _{SS}	-50			μA
	I _{IL6}	V _{IL6} =V _{SS}	-13		-2	μA
High level output current (1)	I _{OH1}	V _{OH1} =0.1·V _{SS}			-250	μA
Low level output current (1)	I _{OL1}	V _{OL1} =0.9·V _{SS}	1.0			mA
High level output current (2)	I _{OH2}	V _{OH2} =0.1·V _{SS}			-250	μA
Low level output current (2)	I _{OL2}	V _{OL2} =0.9·V _{SS}	1.0			mA
High level output current (3)	I _{OH3}	V _{OH3} =0.1·V _{SS}			-1.8	mA
Low level output current (3)	I _{OL3}	V _{OL3} =0.9·V _{SS}	1.0			mA
Common output current	I _{OH4}	V _{OH4} =-0.05V			-3.0	μA
	I _{OL4}	V _{OL4} =V _{L3} +0.05V	3.0			μA
Segment output current (during LCD output)	I _{OH5}	V _{OH5} =-0.05V			-3.0	μA
	I _{OL5}	V _{OL5} =V _{L3} +0.05V	3.0			μA

*1: Only at read cycle using internal program.

● Analog Circuit Characteristics and Current Consumption

(Unless otherwise specified: V_{DD}=0V, V_{SS}=-2.2 to -3.5V, T_a=25°C)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
LCD drive voltage	V _{L1}	V _{ADJ} =V _{L1} , I _{L1} =5μA	-1.11	-1.03	-0.95	V
	V _{L2}	1 MΩ load connected between V _{DD} and V _{L2} (no panel load)	2·V _{L1}		2·V _{L1} +0.1	V
	V _{L3}	1 MΩ load connected between V _{DD} and V _{L3} (no panel load)	3·V _{L1}		3·V _{L1} +0.3	V
Current consumption	I _{OP}	HALT mode, OSCC=0		2	5	μA
		OSC1 mode, OSCC=0		9	18	μA
		OSC3 mode *1		130	250	μA

*1: Ceramic oscillation (455 kHz) or CR oscillation (R_{CR}=140 kΩ)

● Oscillation Characteristics

Oscillation characteristics will vary according to different conditions (elements used, board pattern). Use the following characteristics as reference values.

OSC1 (Crystal Oscillation)

(Unless otherwise specified: $V_{DD}=0V$, $V_{SS}=-3.0V$, Crystal oscillator: C-002R ($C_1=35k\Omega$), $C_G=25pF$, C_D =built-in, $T_a=25^\circ C$)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
Oscillation start time	t_{sta}	$V_{SS}=-2.2$ to $-3.5V$	—	—	3	sec
Built-in capacitance (drain)	C_D	Package as assembled	—	20	—	pF
		Bare chip	—	19	—	pF
Frequency/voltage deviation	$\partial f/\partial V$	$V_{SS}=-2.2$ to $-3.5V$	—	—	5	ppm
Frequency/IC deviation	$\partial f/\partial IC$		-10	—	10	ppm
Frequency adjustment range	$\partial f/\partial C_G$	$C_G=5$ to $25pF$	40	—	—	ppm
Harmonic oscillation start voltage	V_{hho}	$C_G=5pF$ (V_{SS})	—	—	-3.5	V
Permitted leak resistance	R_{leak}	Between OSC1 and other pins	200	—	—	$M\Omega$

OSC3 (Ceramic Oscillation)

(Unless otherwise specified: $V_{DD}=0V$, $V_{SS}=-3.0V$, Ceramic oscillator: CSB455E*1, $C_{GC}=C_{DC}=100pF$, $T_a=25^\circ C$)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
Oscillation start voltage	V_{sta}	(V_{DD})	-2.2	—	—	V
Oscillation start time	t_{sta}	$V_{SS}=-2.2$ to $-3.5V$	—	3	—	mS
Oscillation stop voltage	V_{stp}	(V_{DD})	-2.2	—	—	V

*1: CSB455E: made by Murata Mfg.Co.

OSC3 (CR Oscillation)

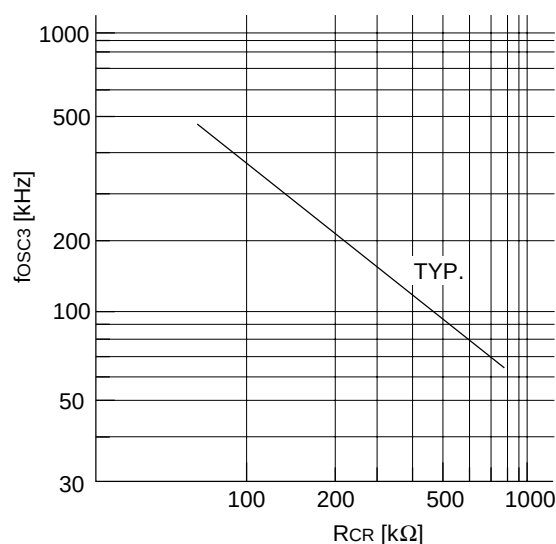
(Unless otherwise specified: $V_{DD}=0V$, $V_{SS}=-3.0V$, $R_{CR}=140k\Omega$, $T_a=25^\circ C$)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
Oscillation frequency	f_{osc3}		—	280	—	kHz
Oscillation start voltage	V_{sta}	(V_{DD})	-2.2	—	—	V
Oscillation start time	t_{sta}	$V_{SS}=-2.2$ to $-3.5V$	—	3	—	mS
Oscillation stop voltage	V_{stp}	(V_{DD})	-2.2	—	—	V

E0C6006 oscillation characteristics — f_{osc3} vs R_{CR} — (for reference)

Condition: $T_a = 25^\circ C$, $V_{DD} = GND$, $V_{SS} = -3.0V$,
Non board and package capacitance

Note: Oscillation characteristics are affected by various conditions (board pattern, parts used, etc.).

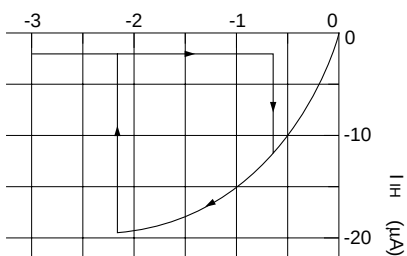


● Input Current Characteristics (For Reference)

Condition: $T_a = 25^\circ\text{C}$, $V_{DD} = 0\text{ V}$, $V_{SS} = -3.0\text{ V}$

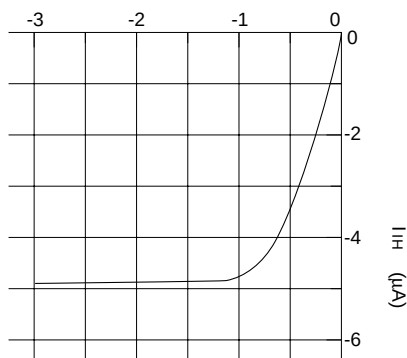
RESET

V_{IH} (V)



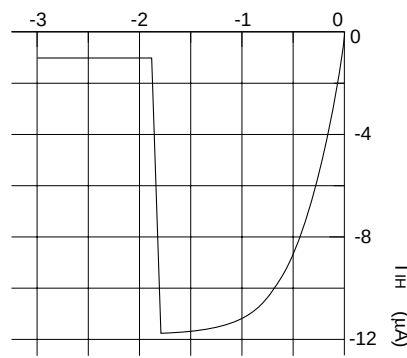
P**

V_{IH} (V)



K**

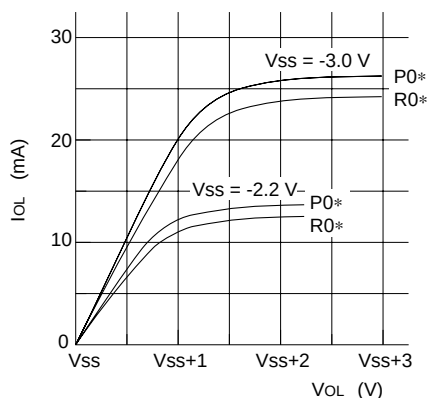
V_{IH} (V)



● Output Current Characteristics (For Reference)

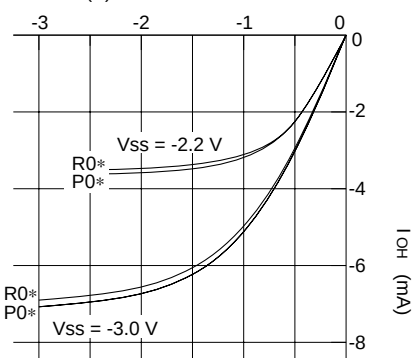
Condition: $T_a = 25^\circ\text{C}$, $V_{DD} = 0\text{ V}$

R0*, P**



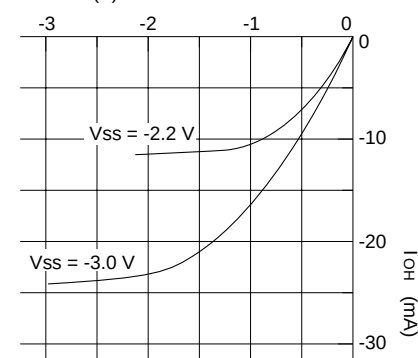
R0*, P**

V_{OH} (V)



R33 (REM)

V_{OH} (V)



NOTICE:

No part of this material may be reproduced or duplicated in any form or by any means without the written permission of Seiko Epson. Seiko Epson reserves the right to make changes to this material without notice. Seiko Epson does not assume any liability of any kind arising out of any inaccuracies contained in this material or due to its application or use in any product or circuit and, further, there is no representation that this material is applicable to products requiring high level reliability, such as, medical products. Moreover, no license to any intellectual property rights is granted by implication or otherwise, and there is no representation or warranty that anything made in accordance with this material will be free from any patent or copyright infringement of a third party. This material or portions thereof may contain technology or the subject relating to strategic products under the control of the Foreign Exchange and Foreign Trade Control Law of Japan and may require an export license from the Ministry of International Trade and Industry or other approval from another government agency.

© Seiko Epson Corporation 1999 All right reserved.

SEIKO EPSON CORPORATION

ELECTRONIC DEVICES MARKETING DIVISION

IC Marketing & Engineering Group

ED International Marketing Department I (Europe & U.S.A.)

421-8, Hino, Hino-shi, Tokyo 191-8501, JAPAN

Phone : 042-587-5812 FAX : 042-587-5564

ED International Marketing Department II (Asia)

421-8, Hino, Hino-shi, Tokyo 191-8501, JAPAN

Phone : 042-587-5814 FAX : 042-587-5110

