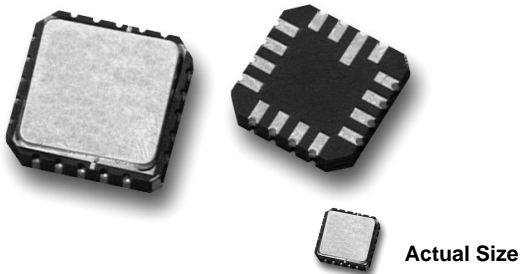


SLCC

Vishay Thin Film



Hermetic, 50 Mil Pitch, Leadless Chip Resistor Networks



FEATURES

- High stability Ultrafilm®
- Low noise
- Hermetic package

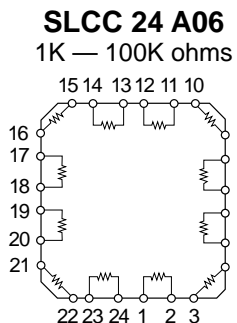
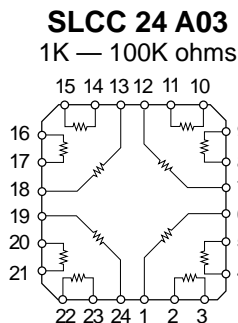
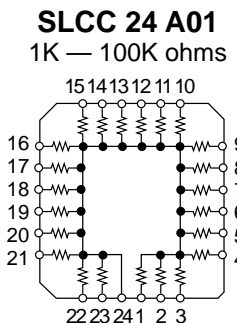
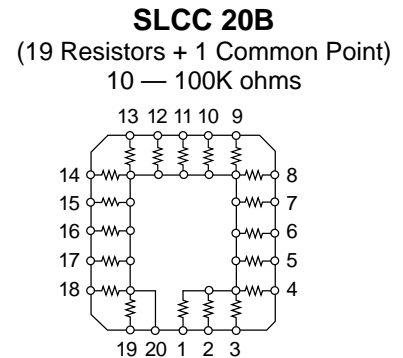
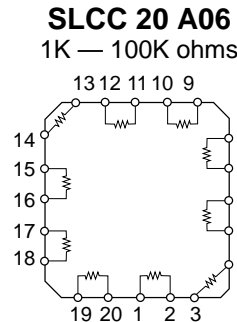
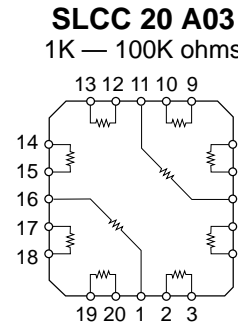
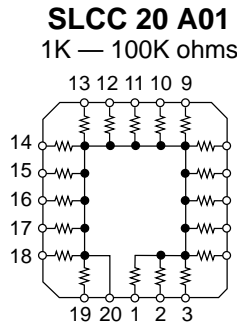
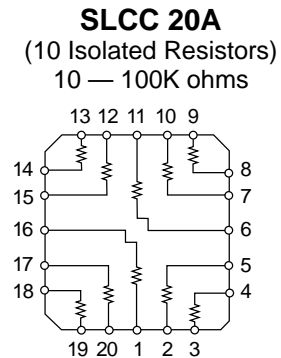
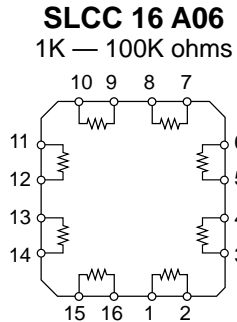
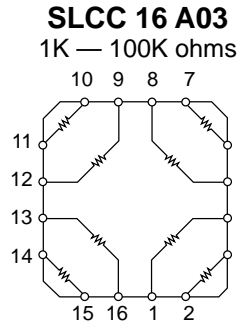
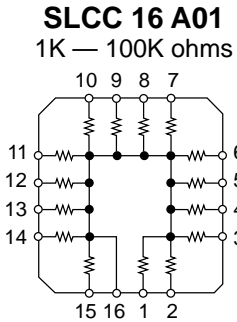
TYPICAL PERFORMANCE

	ABS	TRACKING
TCR	25	2
	ABS	RATIO
TOL	0.1	0.1

Capable of meeting the characteristics of MIL-PRF-83401 these networks are available in a wide range of resistance values: several standard configurations are presented with the SLCC series.

SCHEMATICS

Resistance Range: Noted on schematics

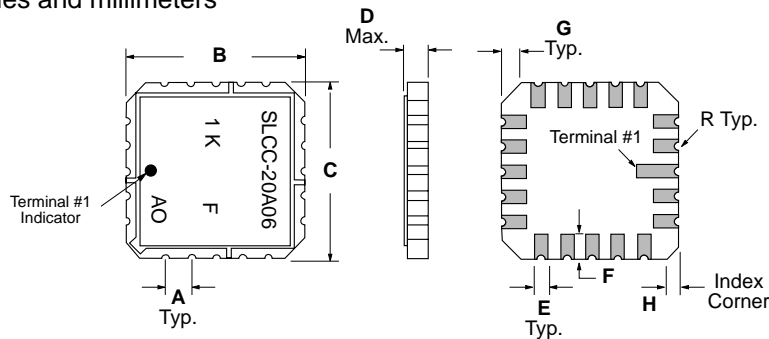


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STANDARD ELECTRICAL SPECIFICATIONS		
TEST	SPECIFICATIONS	CONDITIONS
PIN NUMBER	16, 20, 24	
TCR:	Tracking	± 2ppm/°C typical/5ppm/°C Max.
	Absolute	± 25ppm/°C Max.
Tolerance:	Ratio	± 0.1% to ± 1% tighter specs on request
	Absolute	± 0.1% to ± 5% tighter specs on request
Power Rating:	Resistor	50mW
	Package	500mW 250mW 1W
Stability (Δ R Ratio)	0.05%	Maximum
Voltage Coefficient	< 0.1ppm/V	1000 hrs. @ + 70°C
Working Voltage	100VDC on R	
Operating Temperature Range	- 55°C + 125°C	
Storage Temperature Range	- 55°C + 150°C	
Noise	- 35dB typical	
Thermal EMF	0.1μV/°C	
Shelf Life Stability	< 50ppm	1 year @ + 25°C

DIMENSIONS in inches and millimeters



	A	B	C	D	E	F	G	H
16 Pin	0.050"	0.300"	0.300"	0.077"	0.025"	0.050"	0.040"	0.020"
mm	1.27	7.62	7.62	1.96	0.635	1.27	1.02	0.508
20 Pin	0.050"	0.350"	0.350"	0.077"	0.025"	0.050"	0.040"	0.020"
mm	1.27	8.89	8.89	1.96	0.635	1.27	1.02	0.508
24 Pin	0.050"	0.400"	0.400"	0.077"	0.025"	0.050"	0.040"	0.020"
mm	1.27	10.16	10.16	1.96	0.635	1.27	1.02	0.508

How to Order

Series	Number of Pins	Schematic	Ohmic Value	Absolute Tolerance	Ratio
SLCC	16 16 20 24	A A B A01 A03 A06	5.8K ohms 5.8K ohms	B B = ±0.1% D = ±0.5% F = ±1.0% G = ±2.0% J = ±5.0%	0.1% 0.25% 0.5% 1.0% 1.0%

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