Single Non-Inverting Buffer with Schmitt Trigger

The NL17SZ17 is a single Non-inverting Schmitt Trigger Buffer in a tiny footprint package. The SC70-5/SC-88A occupies a very small board area. The device performs much as LCX multi-gate products in speed and drive.

- Tiny SC70–5/SC–88A Package
- Source/Sink 24 mA at 3.0 Volts
- Over-Voltage Tolerant Inputs and Outputs
- Chip Complexity: FETs = TBD
- Designed for 1.65 V to 5.5 V V_{CC} Operation

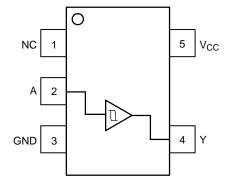


Figure 1. Pinout (Top View)



Figure 2. Logic Symbol



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SC70-5/SC-88A/SOT-353 DF SUFFIX CASE 419A



MARKING

Pin 1

d = Date Code

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

PIN ASSIGNMENT

Pin	Function
1	NC
2	А
3	GND
4	Y
5	V _{CC}

FUNCTION TABLE

A Input	₹ Output
L	L
Н	н

MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{CC}	DC Supply Voltage	-0.5 to +7.0	V
VI	DC Input Voltage	$-0.5 \le V_1 \le +7.0$	V
Vo	DC Output Voltage Output in High or LOW State (Note 1)	$-0.5 \le V_{O} \le 7.0$	V
I _{IK}	DC Input Diode Current $V_1 < GND$	-50	mA
I _{OK}	DC Output Diode Current $V_O < GND$	-50	mA
I _O	DC Output Sink Current	±50	mA
I _{CC}	DC Supply Current per Supply Pin	± 100	mA
I _{GND}	DC Ground Current per Ground Pin	±100	mA
T _{STG}	Storage Temperature Range	-65 to +150	°C
TL	Lead Temperature, 1 mm from Case for 10 Seconds	260	°C
TJ	Junction Temperature under Bias	+ 150	°C
θ_{JA}	Thermal Resistance (Note 2)	333	°C/W
P _D	Power Dissipation in Still Air at 85°C	200	mW
MSL	Moisture Sensitivity	Level 1	
F _R	Flammability Rating Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	
I _{Latch-Up}	Latch–Up Performance Above V _{CC} and Below GND at 85°C (Note 6)	±500	mA

Maximum Ratings are those values beyond which damage to the device may occur. Exposure to these conditions or conditions beyond those indicated may adversely affect device reliability. Functional operation under absolute maximum–rated conditions is not implied. Functional operation should be restricted to the Recommended Operating Conditions.

- 1. I_O absolute maximum rating must be observed.
- Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2-ounce copper trace with no air flow.
 Tested to EIA/JESD22-A114-A.
 Tested to EIA/JESD22-A115-A.

- 5. Tested to JESD22-C101-A.
- 6. Tested to EIA/JESD78.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit	
V _{CC}	Supply Voltage	Operating Data Retention Only	1.65 1.5	5.5 5.5	٧
VI	Input Voltage	(Note 7)	0	5.5	V
Vo	Output Voltage	(HIGH or LOW State)	0	5.5	V
T _A	Operating Free-Air Temperature		-40	+85	°C
Δt/ΔV	Input Transition Rise or Fall Rate	$V_{CC} = 2.5 \text{ V} \pm 0.2 \text{ V}$ $V_{CC} = 3.0 \text{ V} \pm 0.3 \text{ V}$ $V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$	0 0 0	20 10 5	ns/V

7. Unused inputs may not be left open. All inputs must be tied to a high-logic voltage level or a low-logic input voltage level.

DC ELECTRICAL CHARACTERISTICS

			v _{cc}	T _A =	= 25°C		$-40^{\circ}C \leq T_{A}$	≤ 85°C	
Symbol	Parameter	Condition	(V)	Min	Тур	Max	Min	Max	Unit
V _T +	Positive Input Threshold Voltage		1.65 2.3 2.7 3.0 4.5 5.5	0.6 1.0 1.2 1.3 1.9 2.2	1.0 1.5 1.7 1.9 2.7 3.3	1.4 1.8 2.0 2.2 3.1 3.6	0.6 1.0 1.2 1.3 1.9 2.2	1.4 1.8 2.0 2.2 3.1 3.6	٧
V _T -	Negative Input Threshold Voltage		1.65 2.3 2.7 3.0 4.5 5.5	0.2 0.4 0.5 0.6 1.0	0.5 0.75 0.87 1.0 1.5 1.9	0.8 1.15 1.4 1.5 2.0 2.3	0.2 0.4 0.5 0.6 1.0	0.8 1.15 1.4 1.5 2.0 2.3	٧
V _H	Input Hysteresis Voltage		1.65 2.3 2.7 3.0 4.5 5.5	0.1 0.25 0.3 0.4 0.6 0.7	0.48 0.75 0.83 0.93 1.2 1.4	0.9 1.1 1.15 1.2 1.5 1.7	0.1 1.25 0.3 0.4 0.6 0.7	0.9 1.1 1.15 1.2 1.5 1.7	V
V _{OH}	High-Level Output Voltage V _{IN} = V _{IH} or V _{IL}	$\begin{split} V_{IN} &= V_{IL} \text{ or } I_{OH} = -100 \ \mu\text{A} \\ I_{OH} &= 100 \ \mu\text{A} \\ I_{OH} &= -3 \ \text{mA} \\ I_{OH} &= -8 \ \text{mA} \\ I_{OH} &= -12 \ \text{mA} \\ I_{OH} &= -16 \ \text{mA} \\ I_{OH} &= -24 \ \text{mA} \\ I_{OH} &= -32 \ \text{mA} \end{split}$	1.65 2.3 to 5.5 1.65 2.3 2.7 3.0 3.0 4.5	1.55 V _{CC} = 0.1 1.29 1.9 2.2 2.4 2.3 3.8	1.65 V _{CC} 1.52 2.1 2.4 2.7 2.5 4.0		1.55 V _{CC} =0.1 1.29 1.9 2.2 2.4 2.3 3.8		V
V _{OL}	Low-Level Output Voltage V _{IN} = V _{IH} or V _{IL}	$\begin{split} I_{OL} &= 100 \; \mu\text{A} \\ I_{OL} &= 100 \; \mu\text{A} \\ I_{OL} &= 4 \; \text{mA} \\ I_{OL} &= 8 \; \text{mA} \\ I_{OL} &= 12 \; \text{mA} \\ I_{OL} &= 16 \; \text{mA} \\ I_{OL} &= 24 \; \text{mA} \\ I_{OL} &= 32 \; \text{mA} \\ \end{split}$	1.65 2.3 to 5.5 1.65 2.3 2.7 3.0 3.0 4.5		0.0 0.08 0.2 0.22 0.28 0.38 0.42	0.1 0.24 0.3 0.4 0.4 0.55 0.55		0.1 0.24 0.3 0.4 0.4 0.55 0.55	V
I _{IN}	Input Leakage Current	V _{IN} or V _{OUT} = V _{CC} or GND	0 to 5.5			±0.1		±1.0	μΑ
I _{OFF}	Power Off–Output Leakage Current	V _{OUT} = 5.5 V	0			1.0		10	μΑ
I _{CC}	Quiescent Supply Current	$V_{IN} = V_{CC}$ or GND	5.5			1.0		10	μΑ

AC ELECTRICAL CHARACTERISTICS (Input $t_f = t_f = 3.0 \text{ ns}$)

			v _{cc}	T _A = 25°C		-40°C ≤			
Symbol	Parameter	Condition	(V)	Min	Тур	Max	Min	Max	Unit
t _{PLH}	Propagation Delay Input A to Y (Figures 3 and 4)	$R_L = 1 \text{ M}\Omega, C_L = 15 \text{ pF}$	1.65 1.8 2.5 ± 0.2 3.3 ± 0.3 5.0 ± 0.5	2.0 2.0 1.0 1.0 0.5	9.1 7.6 5.0 3.7 3.1	15 12.5 9.0 6.3 5.2	2.0 2.0 1.0 1.0 0.5	15.6 13 9.5 6.5 5.5	ns
		$R_L = 500 \Omega, C_L = 50 pF$	3.3 ± 0.3 5.0 ± 0.5	1.5 0.8	4.4 3.7	7.2 5.9	1.5 0.8	7.5 6.2	

CAPACITIVE CHARACTERISTICS

Symbol	Parameter	Condition	Typical	Unit
C _{IN}	Input Capacitance	$V_{CC} = 5.5 \text{ V}, V_I = 0 \text{ V or } V_{CC}$	2.5	pF
C _{PD}	Power Dissipation Capacitance (Note 8)	10 MHz, $V_{CC} = 3.3 \text{ V}$, $V_I = 0 \text{ V or } V_{CC}$ 10 MHz, $V_{CC} = 5.5 \text{ V}$, $V_I = 0 \text{ V or } V_{CC}$	9 11	pF

8. C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation: $I_{CC(OPR)} = C_{PD} \bullet V_{CC} \bullet f_{in} + I_{CC}$. C_{PD} is used to determine the no–load dynamic power consumption; $P_D = C_{PD} \bullet V_{CC}^2 \bullet f_{in} + I_{CC} \bullet V_{CC}$.

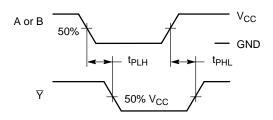
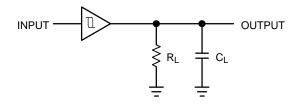


Figure 3. Switching Waveforms



A 1 MHz square input wave is recommended for propagation delay tests.

Figure 4. Test Circuit

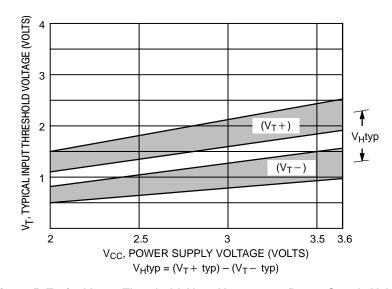
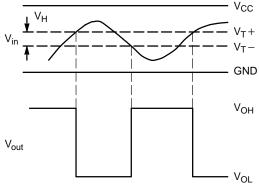
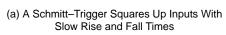
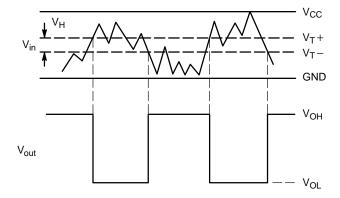


Figure 5. Typical Input Threshold, $V_{T}{}^{+}$, $V_{T}{}^{-}$ versus Power Supply Voltage







(b) A Schmitt-Trigger Offers Maximum Noise Immunity

Figure 6. Typical Schmitt-Trigger Applications

DEVICE ORDERING INFORMATION

Device Nomenclature									
Device Order Number	Logic Circuit Indicator	No. of Gates per Package	Temp Range Identifier	Technology	Device Function	Package Suffix	Tape and Reel Suffix	Package Type	Tape and Reel Size
NL17SZ17DFT2	NL	1	7	SZ	17	DF	T2	SC70-5/ SC-88A/ SOT-353	178 mm, 3000 Units

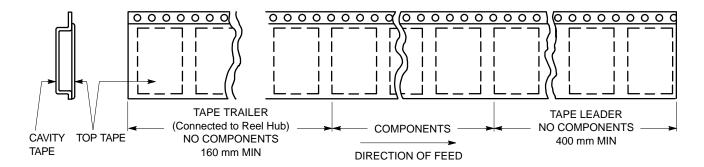


Figure 7. Tape Ends for Finished Goods

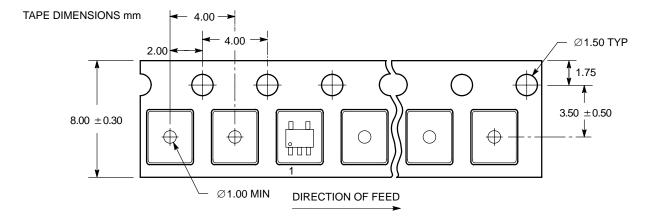


Figure 8. SC-70/SC-88A/SOT-353 DFT2 Reel Configuration/Orientation

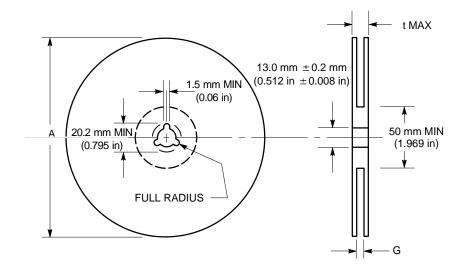


Figure 9. Reel Dimensions

REEL DIMENSIONS

Tape Size	T and R Suffix	A Max	G	t Max
8 mm	T1, T2	178 mm (7 in)	8.4 mm, + 1.5 mm, -0.0 (0.33 in + 0.059 in, -0.00)	14.4 mm (0.56 in)

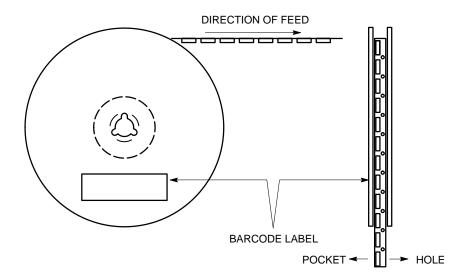
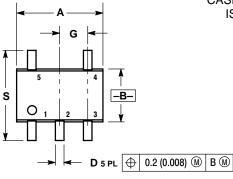


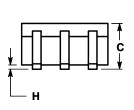
Figure 10. Reel Winding Direction

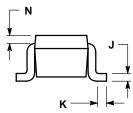
PACKAGE DIMENSIONS

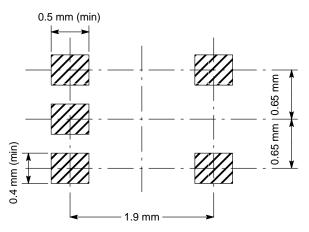
SC70-5/SC-88A/SOT-353 **DF SUFFIX**

5-LEAD PACKAGE CASE 419A-02 ISSUE F









NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH. 419A-01 OBSOLETE. NEW STANDARD 419A-02.

	INC	HES	MILLIM	ETERS
DIM	MIN	MAX	MIN	MAX
Α	0.071	0.087	1.80	2.20
В	0.045	0.053	1.15	1.35
С	0.031	0.043	0.80	1.10
D	0.004	0.012	0.10	0.30
G	0.026	BSC	0.65	BSC
Н		0.004		0.10
J	0.004	0.010	0.10	0.25
K	0.004	0.012	0.10	0.30
N	0.008 REF		0.20	REF
9	0.070	0.087	2.00	2 20

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