

HD74LV1G04A

Inverter

REJ03D0064-0700Z (Previous ADE-205-317E (Z)) Rev.7.00 Aug.28.2003

Description

The HD74LV1G04A has an inverter in a 5 pin package. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Electrical characteristics equivalent to the HD74LV04A

Supply voltage range: 1.65 to 5.5 V

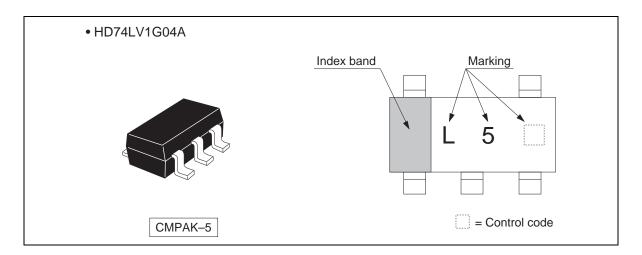
Operating temperature range: -40 to +85°C

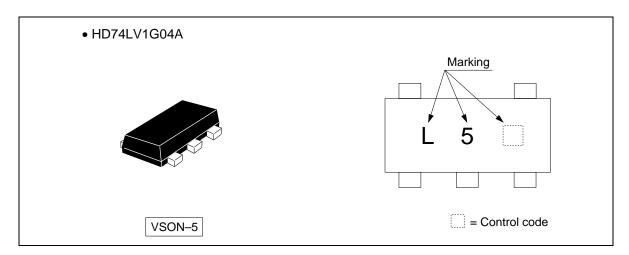
- All inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V) All outputs V_{O} (Max.) = 5.5 V (@ V_{CC} = 0 V)
- Output current ± 6 mA (@V_{CC} = 3.0 V to 3.6 V), ± 12 mA (@V_{CC} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV1G04ACME	CMPAK-5 pin	CMPAK-5V	СМ	E (3,000 pcs/reel)
		CMPAK-5V(O)	_	
HD74LV1G04AVSE	VSON-5 pin	TNP-5DV	VS	_

Note: Please consult the sales office for the above package availability.

Outline and Article Indication



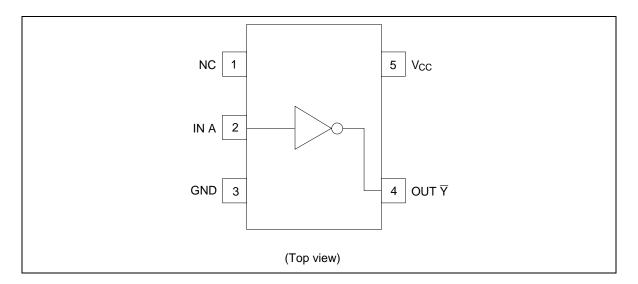


Function Table

Input A	Output Y
Н	L
L	Н

H : High level L : Low level

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{CC}	-0.5 to 7.0	V	
Input voltage range *1	VI	-0.5 to 7.0	V	
Output voltage range *1, 2	Vo	-0.5 to V_{CC} + 0.5	V	Output : H or L
		-0.5 to 7.0		V _{CC} : OFF
Input clamp current	I _{IK}	-20	mA	V _I < 0
Output clamp current	l _{OK}	±50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	Io	±25	mA	$V_O = 0$ to V_{CC}
Continuous current through V _{CC} or GND	I _{CC} or I _{GND}	±50	mA	
Maximum power dissipation at Ta = 25°C (in still air) *3	P _T	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

HD74LV1G04A

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V _{CC}	1.65	5.5	V	
Input voltage range	VI	0	5.5	V	
Output voltage range	Vo	0	Vcc	V	
Output current	I _{OL}	_	1	mA	$V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$
		_	2		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
		_	6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		_	12		$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
	I _{OH}	_	-1		V_{CC} = 1.65 to 1.95 V
		_	-2		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
		_	-6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		_	-12		$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
Input transition rise or fall rate	Δt / Δν	0	300	ns / V	V _{CC} = 1.65 to 1.95 V
		0	200		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
		0	100		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		0	20		$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
Operating free-air temperature	Ta	-40	85	°C	

Note: Unused or floating inputs must be held high or low.

Electrical Characteristic

• $Ta = -40 \text{ to } 85^{\circ}C$

Item	Symbol	V _{CC} (V) *	Min	Тур	Max	Unit	Test condition
Input voltage	V _{IH}	1.65 to 1.95	V _{CC} ×0.75	_	_	V	
		2.3 to 2.7	V _{CC} ×0.7	_	_	=	
		3.0 to 3.6	V _{CC} ×0.7	_	_	-	
		4.5 to 5.5	V _{CC} ×0.7	_	_	-	
	V _{IL}	1.65 to 1.95	_	_	V _{CC} ×0.25	-	
		2.3 to 2.7	_	_	V _{CC} ×0.3	=	
		3.0 to 3.6	_	_	V _{CC} ×0.3	-	
		4.5 to 5.5	_	_	V _{CC} ×0.3	=	
Hysteresis voltage	V _H	1.8	_	0.25	_	V	$V_T^+ - V_T^-$
		2.5	_	0.30	_	-	
		3.3	_	0.35	_	-	
		5.0	_	0.45	_	=	
Output voltage	V_{OH}	Min to Max	V _{CC} -0.1	_	_	V	$I_{OH} = -50 \mu A$
		1.65	1.4	_	_	-	$I_{OH} = -1 \text{ mA}$
		2.3	2.0	_	_	-	$I_{OH} = -2 \text{ mA}$
		3.0	2.48	_	_	-	$I_{OH} = -6 \text{ mA}$
		4.5	3.8	_	_	-	$I_{OH} = -12 \text{ mA}$
	V _{OL}	Min to Max	_	_	0.1	-	I _{OL} = 50 μA
		1.65	_	_	0.3	-	I _{OL} = 1 mA
		2.3	_	_	0.4	-	I _{OL} = 2 mA
		3.0	_	_	0.44	=	I _{OL} = 6 mA
		4.5	_	_	0.55	-	I _{OL} = 12 mA
Input current	I _{IN}	0 to 5.5	_	_	±1	μΑ	$V_{IN} = 5.5 \text{ V or GND}$
Quiescent supply current	Icc	5.5	_	_	10	μΑ	$V_{IN} = V_{CC}$ or GND, $I_O = 0$
Output leakage current	loff	0	_	_	5	μΑ	V_{IN} or $V_O = 0$ to 5.5 V
Input capacitance	C _{IN}	3.3	_	3.0	_	pF	V _{IN} = V _{CC} or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

$\bullet \quad V_{CC} = 1.8 \pm 0.15 \ V$

Item	Symbol	Ta = 2	25°C		$Ta = -40 \text{ to } 85^{\circ}C$		Unit		FROM	то
		Min	Тур	Max	Min	Max		Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	12.6	22.0	1.0	24.0	ns	C _L = 15 pF	Α	Y
delay time	t _{PHL}	_	19.7	33.0	1.0	36.0	_	$C_L = 50 pF$	_	

$\bullet \quad V_{CC} = 2.5 \pm 0.2 \ V$

Item	Symbol	Ta = 2	25°C		Ta = -40 to 85°C		Unit		FROM	то
		Min	Тур	Max	Min	Max		Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	7.0	11.7	1.0	14.0	ns	C _L = 15 pF	Α	Y
delay time	t _{PHL}	_	10.5	15.5	1.0	18.0	_	C _L = 50 pF	_	

$\bullet \quad V_{CC} = 3.3 \pm 0.3 \ V$

Item	Symbol	Ta = 2	25°C		$Ta = -40 \text{ to } 85^{\circ}\text{C}$		Unit		FROM	TO
		Min	Тур	Max	Min	Max	_	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	5.0	7.1	1.0	8.5	ns	C _L = 15 pF	Α	Y
delay time	t _{PHL}	_	7.5	10.6	1.0	12.0	-	C _L = 50 pF	_	

$\bullet \quad V_{CC} = 5.0 \pm 0.5 \ V$

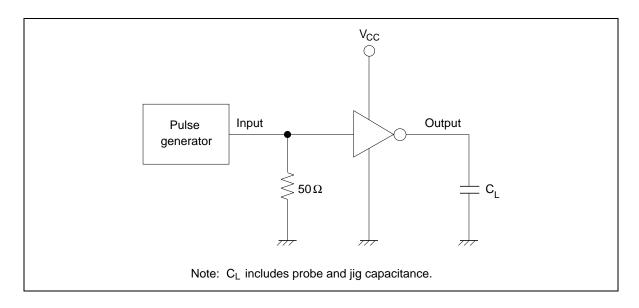
Item	Symbol	Ta = 2	25°C		Ta = -40 to 85°C		Unit		FROM	ТО
		Min	Тур	Max	Min	Max	_	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	3.8	5.5	1.0	6.5	ns	C _L = 15 pF	Α	Y
delay time	t _{PHL}	_	5.3	7.5	1.0	8.5	_	C _L = 50 pF	-	

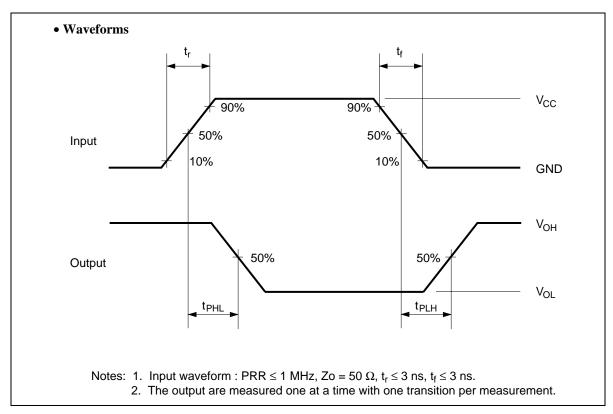
Operating Characteristics

• $C_L = 50 pF$

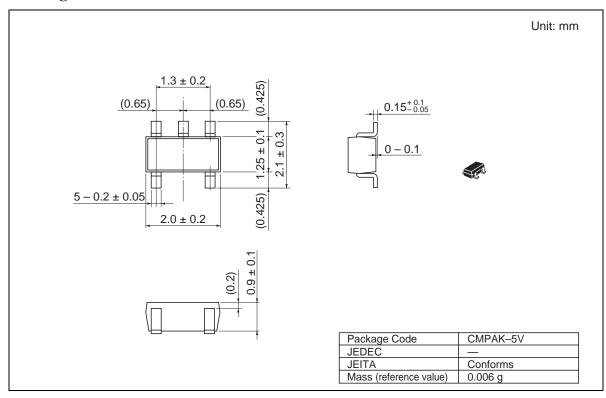
Item	Symbol	V _{CC} (V)	Ta = 25°C			Unit	Test Conditions
			Min	Тур	Max	<u> </u>	
Power dissipation	C_{PD}	3.3	_	8.5	_	pF	f = 10 MHz
capacitance		5.0	_	10.0	_		

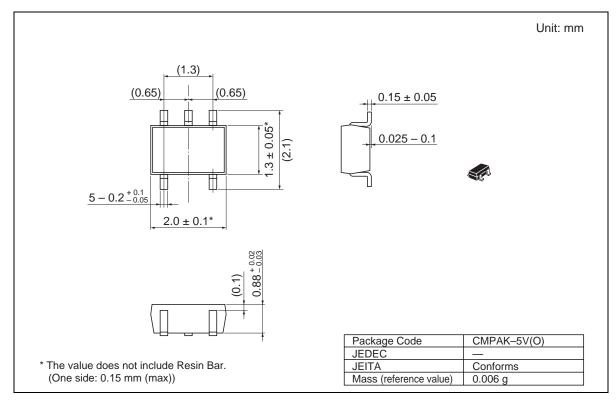
Test Circuit

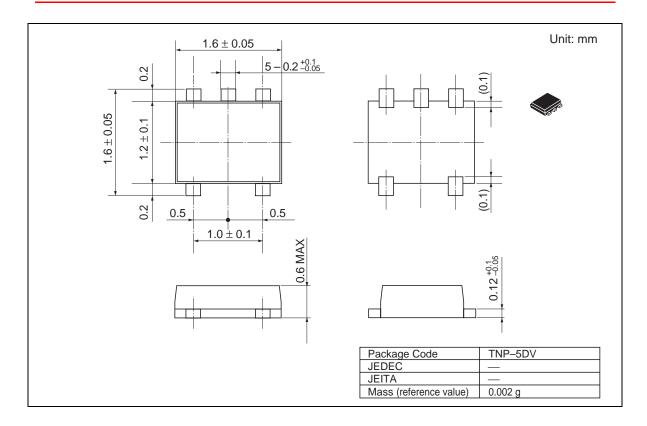




Package Dimensions







Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

The pastery in a survival circula designs; and the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.

Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corp. product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corp. or a third party.

2. Renesas Technology Corp. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.

3. All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corp. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor for the latest product information before purchasing a product listed herein.

The information described here may contain technical inaccuracies or typographical errors.

Renesas Technology Corp. assumes no responsibility for any damage, liability, or other loss resident product product of the renewal product of the product of the renewal product of the rene

- use.

 6. The prior written approval of Renesas Technology Corp. is necessary to reprint or reproduce in whole or in part these materials.

 7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.

 Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.

 8. Please contact Renesas Technology Corp. for further details on these materials or the products contained therein.



RENESAS SALES OFFICES

http://www.renesas.com

Renesas Technology America, Inc. 450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500 Fax: <1> (408) 382-7501

Renesas Technology Europe Limited.

Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, United Kingdom Tel: <44> (1628) 585 100, Fax: <44> (1628) 585 900

Renesas Technology Europe GmbH Dornacher Str. 3, D-85622 Feldkirchen, Germany Tel: <49> (89) 380 70 0, Fax: <49> (89) 929 30 11

Renesas Technology Hong Kong Ltd. 7/F., North Tower, World Finance Centre, Harbour City, Canton Road, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2375-6836

Renesas Technology Taiwan Co., Ltd. FL 10, #99, Fu-Hsing N. Rd., Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd. 26/F., Ruijin Building, No.205 Maoming Road (S), Shanghai 200020, China Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.
1, Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001