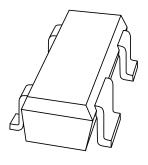
DATA SHEET



BFG25AW; BFG25AW/X NPN 5 GHz wideband transistors

Product specification Supersedes data of August 1995



NPN 5 GHz wideband transistors

BFG25AW/X

FEATURES

- Low current consumption (100 μA to 1 mA)
- Low noise figure
- Gold metallization ensures excellent reliability.

APPLICATIONS

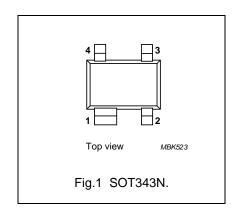
Wideband applications in UHF low power amplifiers, such as pocket telephones and paging systems.

DESCRIPTION

NPN silicon planar epitaxial transistor in a 4-pin dual-emitter SOT343N plastic package.

PINNING

PIN	DESCRIPTION			
BFG25A	W			
1	collector			
2	base			
3	emitter			
4	emitter			
BFG25A	W/X			
1	collector			
2	emitter			
3	base			
4	emitter			



MARKING

TYPE NUMBER	CODE
BFG25AW	N6
BFG25AW/X	V1

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	_	8	V
V_{CEO}	collector-emitter voltage	open base	_	=	5	V
I _C	collector current (DC)		_	_	6.5	mA
P _{tot}	total power dissipation	T _s ≤ 85 °C	_	_	500	mW
h _{FE}	DC current gain	$I_C = 0.5 \text{ mA}; V_{CE} = 1 \text{ V}$	50	80	200	
C _{re}	feedback capacitance	$I_C = 0$; $V_{CE} = 1 \text{ V}$; $f = 1 \text{ MHz}$	_	0.2	0.3	pF
f_{T}	transition frequency	$I_C = 1 \text{ mA}; V_{CE} = 1 \text{ V}; f = 500 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$	3.5	5	_	GHz
G _{UM}	maximum unilateral power gain	$I_C = 0.5 \text{ mA}; V_{CE} = 1 \text{ V}; f = 1 \text{ GHz}; T_{amb} = 25 \text{ °C}$	-	16	-	dB
F	noise figure	$\Gamma_{s} = \Gamma_{opt}$; $I_{C} = 1$ mA; $V_{CE} = 1$ V; $f = 1$ GHz	=	2	=	dB

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	8	V
V_{CEO}	collector-emitter voltage	open base	_	5	V
V _{EBO}	emitter-base voltage	open collector	_	2	V
Ic	collector current (DC)		_	6.5	mA
P _{tot}	total power dissipation	T _s ≤ 85 °C; see Fig.2; note 1	_	500	mW
T _{stg}	storage temperature		-65	+150	°C
T _i	junction temperature		_	175	°C

Note

1. T_s is the temperature at the soldering point of the collector pin.

NPN 5 GHz wideband transistors

BFG25AW; BFG25AW/X

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-s}	thermal resistance from junction to soldering point	$T_s \le 85$ °C; note 1	180	K/W

Note

1. T_s is the temperature at the soldering point of the collector pin.

CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

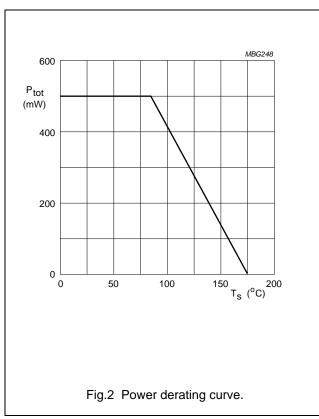
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{(BR)CBO}	collector-base breakdown voltage	$I_C = 100 \mu A; I_E = 0$	_	_	8	V
V _{(BR)CEO}	collector-emitter breakdown voltage	$I_C = 1 \text{ mA}; I_B = 0$	_	_	5	V
V _{(BR)EBO}	emitter-base breakdown voltage	$I_E = 100 \mu A; I_C = 0$	-	_	2	V
I _{CBO}	collector leakage current	open emitter; V _{CB} = 5 V; I _E = 0	-	_	50	nA
h _{FE}	DC current gain	$I_C = 0.5 \text{ mA}; V_{CE} = 1 \text{ V}$	50	80	200	
C _{re}	feedback capacitance	I _C = 0; V _{CE} = 1 V; f = 1 MHz	_	0.2	0.3	pF
f _T	transition frequency	$I_C = 1 \text{ mA}; V_{CE} = 1 \text{ V}; f = 1 \text{ GHz};$ $T_{amb} = 25 \text{ °C}$	3.5	5	_	GHz
G _{UM}	maximum unilateral power gain; note 1	$I_C = 0.5 \text{ mA}; V_{CE} = 1 \text{ V};$ f = 1 GHz; $T_{amb} = 25 ^{\circ}\text{C}$	_	16	_	dB
		$I_C = 0.5 \text{ mA}; V_{CE} = 1 \text{ V};$ $f = 2 \text{ GHz}; T_{amb} = 25 ^{\circ}\text{C}$	_	8	_	dB
F	noise figure	$\begin{split} \Gamma_{\text{S}} = \Gamma_{\text{opt}}; \ I_{\text{C}} = 0.5 \ \text{mA}; \ V_{\text{CE}} = 1 \ \text{V}; \\ f = 1 \ \text{GHz} \end{split}$	_	1.9	_	dB
		$\Gamma_{\text{S}} = \Gamma_{\text{opt}}$; I_{C} = 1 mA; V_{CE} = 1 V; f = 1 GHz	_	2		dB

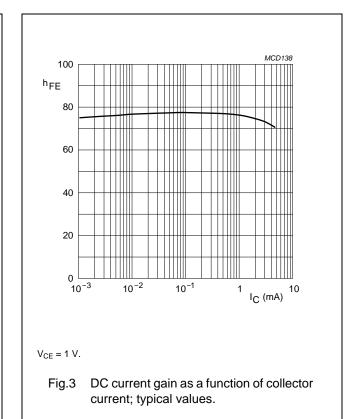
Note

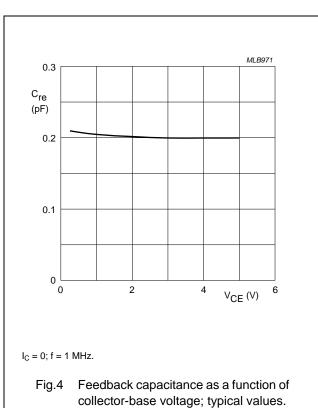
1.
$$G_{UM}$$
 is the maximum unilateral power gain, assuming S_{12} is zero. $G_{UM} = 10 \log \frac{|S_{21}|^2}{(1-|S_{11}|^2)(1-|S_{22}|^2)} dB$.

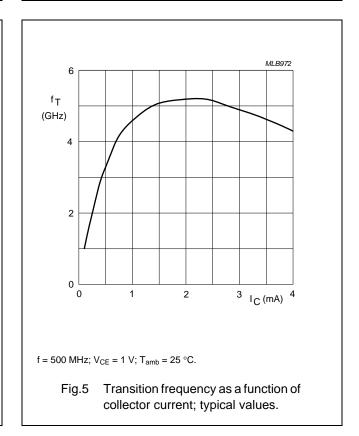
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BFG25AW; BFG25AW/X



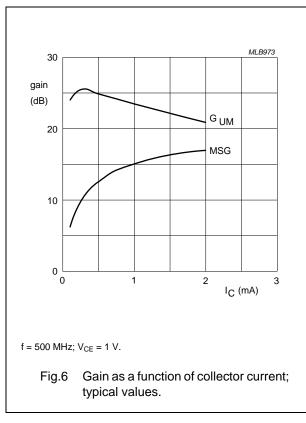


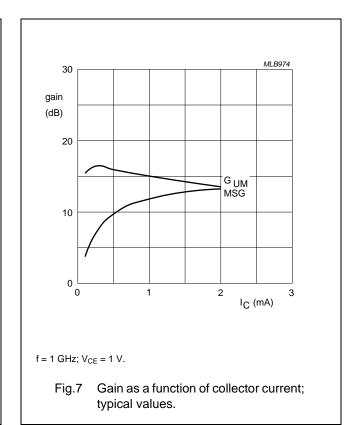


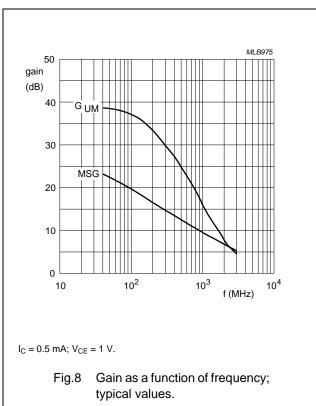


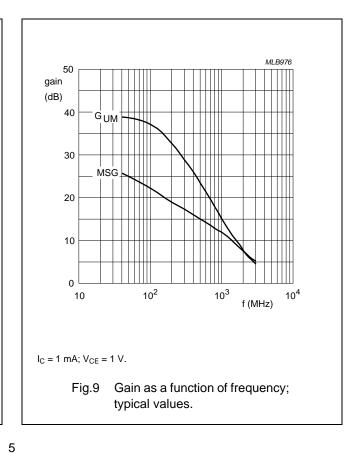
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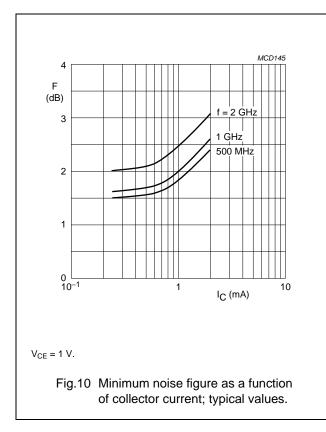


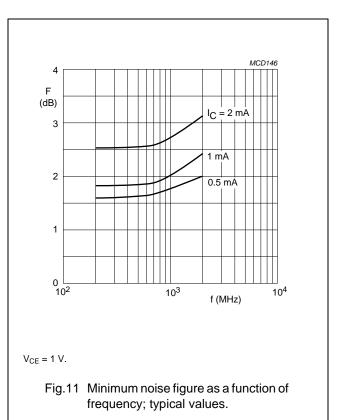


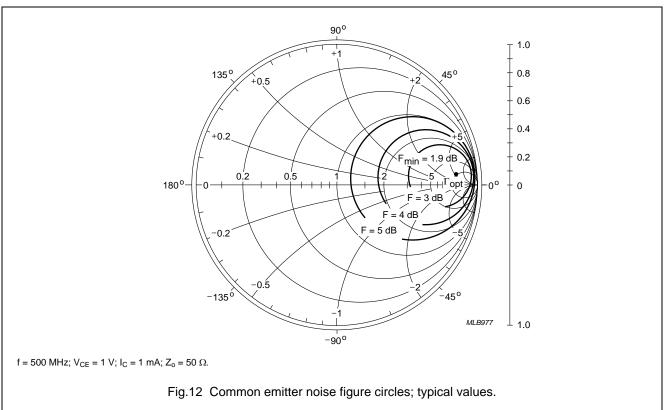


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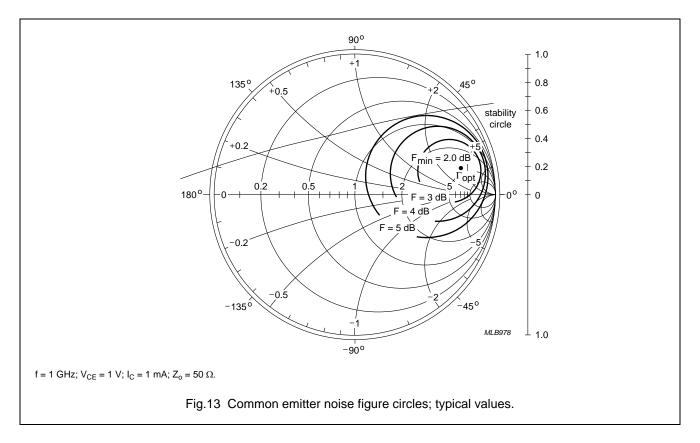


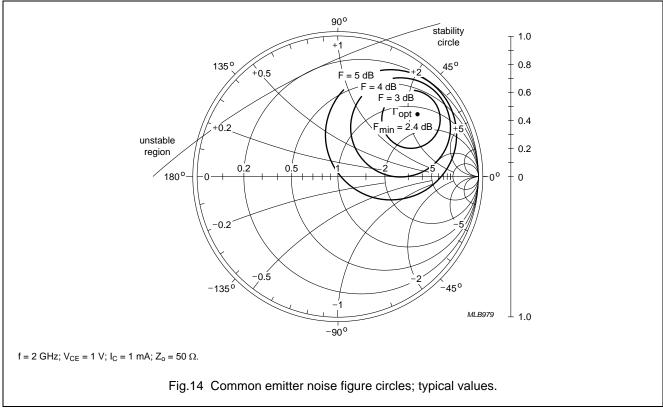




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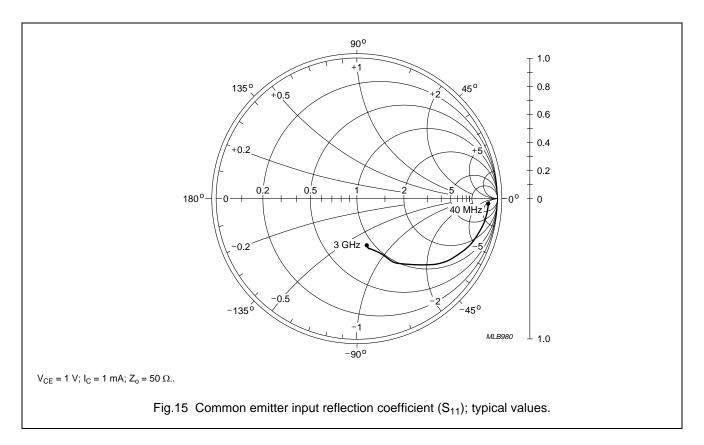
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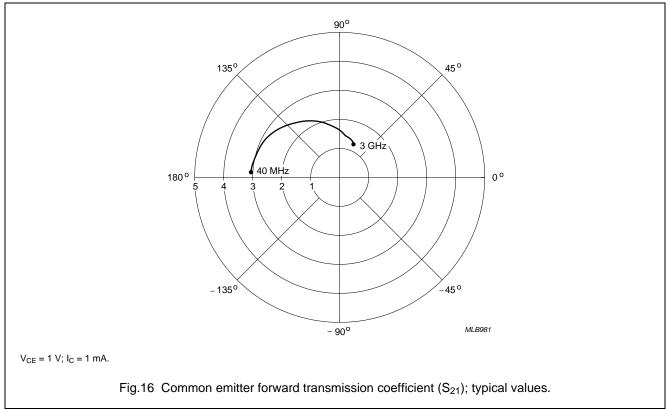




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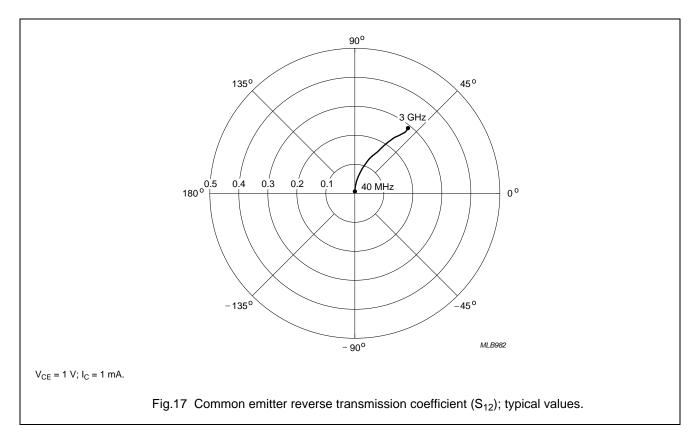
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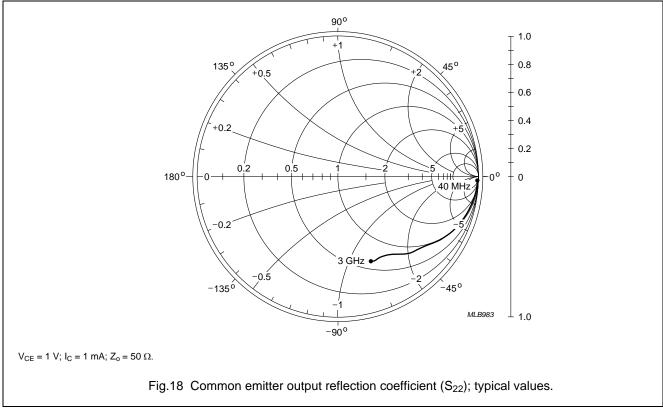




NPN 5 GHz wideband transistors

BFG25AW; BFG25AW/X





9

NPN 5 GHz wideband transistors

BFG25AW; BFG25AW/X

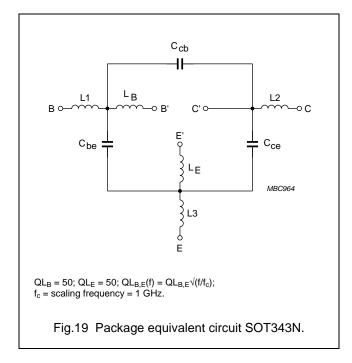
SPICE parameters for the BFG25W crystal

SEQUENCE No.	SEQUENCE No. PARAMETER		UNIT
1	IS	13.77	аА
2	BF	85.65	_
3	NF	0.980	_
4	VAF	50.80	V
5	IKF	10.00	Α
6	ISE	2.199	fA
7	NE	1.857	_
8	BR	16.97	_
9	NR	0.986	_
10	VAR	2.491	V
11	IKR	188.0	mA
12	ISC	205.1	аА
13	NC	1.107	_
14	RB	80.00	Ω
15	IRB	1.000	μΑ
16	RBM	80.00	Ω
17	RE	7.911	Ω
18	RC	5.300	Ω
19 ⁽¹⁾	XTB	0.000	_
20 ⁽¹⁾	EG	1.110	eV
21 ⁽¹⁾	XTI	3.000	_
22	CJE	223.0	fF
23	VJE	669.7	mV
24	MJE	0.060	_
25	TF	5.112	ps
26	XTF	7.909	_
27	VTF	1.338	V
28	ITF	5.662	mA
29	PTF	15.37	deg
30	CJC	229.0	fF
31	VJC	394.7	mV
32	MJC	0.043	
33	XCJC	0.050	_
34	TR	13.26	ns
35 ⁽¹⁾	CJS	0.000	F

SEQUENCE No.	PARAMETER	VALUE	UNIT
36 ⁽¹⁾	VJS	750.0	mV
37 ⁽¹⁾	MJS	0.000	_
38	FC	0.988	-

Note

1. These parameters have not been extracted, the default values are shown.



List of components (see Fig.19)

DESIGNATION	VALUE	UNIT
C _{be}	70	fF
C _{cb}	50	fF
C _{ce}	115	fF
L1	0.34	nH
L2	0.10	nH
L3	0.25	nH
L _B	0.40	nH
L _E	0.40	nH

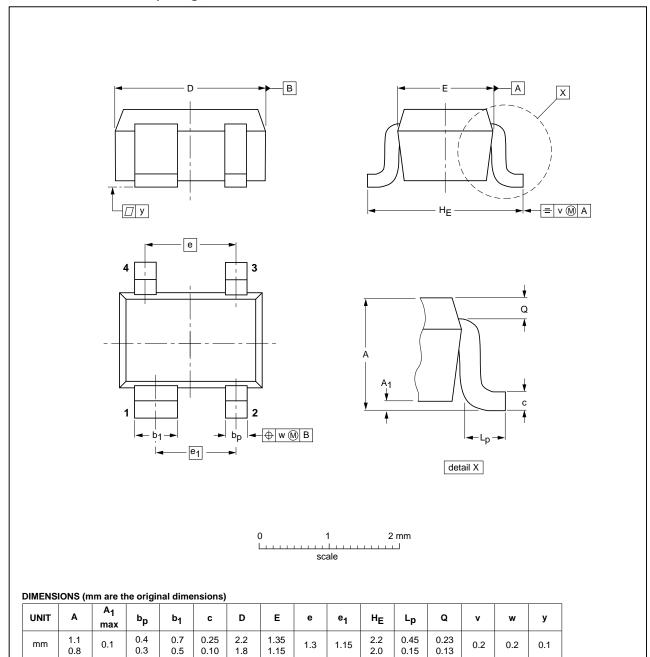
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BFG25AW; BFG25AW/X

PACKAGE OUTLINES

Plastic surface-mounted package; 4 leads

SOT343N



OUTLINE	REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT343N						97-05-21 06-03-16

NPN 5 GHz wideband transistors

BFG25AW; BFG25AW/X

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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NPN 5 GHz wideband transistors

BFG25AW; BFG25AW/X

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Customer notification

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Contact information

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