

2N4391 Single N-Channel JFET switch



Linear Systems replaces discontinued Siliconix 2N4391

The 2N4391 features many of the superior characteristics of JFETs which make it a good choice for demanding analog switching applications and for specialized amplifier circuits.

2N4391 Benefits:

- Low Error Voltage
- High-Speed Analog Circuit Performance
- Negligible "Off-Error," Excellent Accuracy
- Good Frequency Response, Low Glitches
- Eliminates Additional Buffering

2N4391 Applications:

- Analog Switches
- Choppers, Sample-and-Hold
- Normally "On" Switches, Current Limiters

FEATURES					
DIRECT REPLACEMENT FOR SILICONIX 2N4391					
LOW ON RESISTANCE	$r_{DS(on)} \le 30\Omega$				
LOW GATE OPERATING CURRENT	$I_{D(off)} = 5pA$				
FAST SWITCHING	t _(ON) ≤= 15ns				
ABSOLUTE MAXIMUM RATINGS ¹ @ 25°C (unless other	rwise noted)				
Maximum Temperatures					
Storage Temperature	-65°C to +200°C				
Operating Junction Temperature	-55°C to +200°C				
Maximum Power Dissipation					
Continuous Power Dissipation	1800mW				
MAXIMUM CURRENT					
Gate Current (Note 1)	I _G = 50mA				
MAXIMUM VOLTAGES	Ş				
Gate to Drain Voltage / Gate to Source Voltage	-40V				

2N4391 ELECTRICAL CHARACTERISTICS @ 25°C (unless otherwise noted)						
SYMBOL	CHARACTERISTIC	MIN	TYP.	MAX	UNITS	CONDITIONS
BV_GSS	Gate to Source Breakdown Voltage	-40	-			$I_{G} = -1\mu A, V_{DS} = 0V$
V _{GS(off)}	Gate to Source Cutoff Voltage	-4	-	-10	V	$V_{DS} = 20V, I_{D} = 1nA$
$V_{GS(F)}$	Gate to Source Forward Voltage	-	0.7	1		$I_G = 1mA$, $V_{DS} = 0V$
V _{DS(on)}	Drain to Source On Voltage	-	0.25			$V_{GS} = 0V$, $I_D = 3mA$
$V_{DS(on)}$	Drain to Source On Voltage	-	0.3			$V_{GS} = 0V$, $I_D = 6mA$
V _{DS(on)}	Drain to Source On Voltage	-	0.35	0.4		$V_{GS} = 0V, I_D = 12mA$
I _{DSS}	Drain to Source Saturation Current ²	50	-	150	mA	$V_{DS} = 20V, V_{GS} = 0V$
I _{GSS}	Gate Reverse Current	-	-5	-100		$V_{GS} = -20V, \ V_{DS} = 0V$
I _G	Gate Operating Current	-	-5			$V_{DG} = 15V$, $I_D = 10mA$
			5		рА	$V_{DS} = 20V, V_{GS} = -5V$
I _{D(off)}	Drain Cutoff Current	-	5			$V_{DS} = 20V, V_{GS} = -7V$
			5	100		$V_{DS} = 20V, V_{GS} = -12V$
r _{DS(on)}	Drain to Source On Resistance			30	Ω	$V_{GS} = 0V$, $I_D = 1mA$

2N4391 DYNAMIC ELECTRICAL CHARACTERISTICS @ 25°C (unless otherwise noted)						
SYMBOL	CHARACTERISTIC	TYP	MIN	MAX	UNITS	CONDITIONS
g fs	Forward Transconductance	6			mS	$V_{DS} = 20V, I_{D} = 1mA, f = 1kHz$
g os	Output Conductance	25			μS	$V_{DS} = 20V, I_D = 1mA, f = 1kHz$
r _{ds(on)}	Drain to Source On Resistance			30	Ω	$V_{GS} = 0V$, $I_D = 0A$, $f = 1kHz$
C _{iss}	Input Capacitance	12		14		$V_{DS} = 20V$, $V_{GS} = 0V$, $f = 1MHz$
C _{rss}		3.3			pF	$V_{DS} = 0V$, $V_{GS} = -5V$, $f = 1MHz$
C _{rss}	Reverse Transfer Capacitance	3.2			γ.	$V_{DS} = 0V$, $V_{GS} = -7V$, $f = 1MHz$
C _{rss}		2.8		3.5		$V_{DS} = 0V$, $V_{GS} = -12V$, $f = 1MHz$
e _n	Equivalent Input Noise Voltage	3			nV/√Hz	$V_{DS} = 10V, I_D = 10mA, f = 1kHz$

2N4391 SWITCHING ELECTRICAL CHARACTERISTICS @ 25°C (unless otherwise noted)

SYMBOL	CHARACTERISTIC	TYP	MIN	MAX	UNITS	CONDITIONS
t _{d(on)}		2		15		
t _r	Turn On Time	2		5	nc	V - 10V V - 0V
t _{d(off)}		6		20	ns	$V_{DD} = 10V, V_{GS(H)} = 0V$
t _f	Turn Off Time	13		15		

Notes: 1. Absolute ratings are limiting values above which serviceability may be impaired

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TO-18 (Bottom View)

2. Pulse test: PW ≤ 300µs, Duty Cycle ≤ 3%

2N4391 SWITCHING CIRCUIT PARAMETERS

$V_{GS(L)}$	-12V
R_L	800Ω
I _{D(on)}	12mA

Available Packages:

2N4391 in TO-18 2N4391 in bare die.

Contact Micross for full package and die dimensions

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