

# **TFT LCD Tentative Specification**

MODEL NO.: M150X3 -L01

Customer :
Approved by :
Note:

Liqu	id Crystal Display Div	ision
QRA Dept.	RD Dept.	PD Dept.
Approval	Approval	Approval
第 91 4 23 宗嗣	第4.20 忠光	90. 4. 20



# - CONTENTS -

REVISION HISTORY	 3
1. GENERAL DESCRIPTION 1.1 OVERVIEW 1.2 FEATURES 1.3 APPLICATION 1.4 GENERAL SPECIFICATIONS 1.5 MECHANICAL SPECIFICATIONS	 4
2. ABSOLUTE MAXIMUM RATINGS 2.1 ABSOLUTE RATINGS OF ENVIRONMENT 2.2 ELECTRICAL ABSOLUTE RATINGS 2.2.1 TFT LCD MODULE 2.2.2 BACKLIGHT UNIT	 5
3. ELECTRICAL CHARACTERISTICS 3.1 TFT LCD MODULE 3.2 BACKLIGHT UNIT	 7
4. BLOCK DIAGRAM 4.1 TFT LCD MODULE 4.2 BACKLIGHT UNIT	 10
5. INPUT TERMINAL PIN ASSIGNMENT 5.1 TFT LCD MODULE 5.2 BACKLIGHT UNIT 5.3 COLOR DATA INPUT ASSIGNMENT	 11
6. INTERFACE TIMING 6.1 INPUT SIGNAL TIMING SPECIFICATIONS 6.2 POWER ON/OFF SEQUENCE	 13
7. OPTICAL CHARACTERISTICS 7.1 TEST CONDITIONS 7.2 OPTICAL SPECIFICATIONS	 16
8. PRECAUTIONS 8.1 HANDLING PRECAUTIONS 8.2 STORAGE PRECAUTIONS 8.3 OPERATION PRECAUTIONS	 19





# **REVISION HISTORY**

Version	Date	Page (New)	Section	Description
Ver 0.0	April.13'2001		All	Preliminary Specification was first issued.



## 1. GENERAL DESCRIPTION

#### 1.1 OVERVIEW

M150X3-L01 is a 15.0" TFT Liquid Crystal Display module with 4 CCFL Backlight units and 20 pins LVDS interface. This module supports 1024 x 768 XGA mode and can display 16.2M. The optimum viewing angle is at 6 o'clock direction. The inverter module for Backlight is not built in.

#### 1.2 FEATURES

- XGA (1024 x 768 pixels) resolution
- DE (Data Enable) only mode
- LVDS Interface with 1pixels/clock

## 1.3 APPLICATION

- Desktop monitors

## 1.4 GENERAL SPECIFICATIONS

Item	Specification	Unit	Note
Active Area	304.1(H) x 228.1(V) (15.0" diagonal)	mm	(1)
Bezel Opening Area	308.2(H) x 232.1(V)	mm	(1)
Driver Element	a-Si TFT active matrix	-	-
Pixel Number	1024 x R.G.B. x 768	pixel	-
Pixel Pitch	0.297(H) x 0.297(W)	mm	-
Pixel Arrangement	RGB vertical stripe	-	-
Display Colors	16,194,277	color	-
Transmissive Mode	Normally white	-	-

#### 1.5 MECHANICAL SPECIFICATIONS

It	em	Min.	Тур.	Max.	Unit	Note
	Horizontal(H)	-	331.6	-	mm	
Module Size	Vertical(V)	-	254.76	-	mm	(1)
	Depth(D)	-	13.0	-	mm	
We	eight	-	-	(1,350)	g	-

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.



#### 2. ABSOLUTE MAXIMUM RATINGS

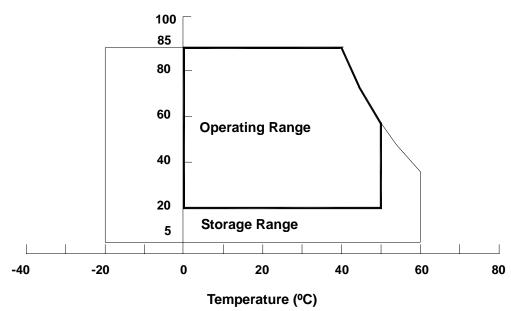
#### 2.1 ABSOLUTE RATINGS OF ENVIRONMENT

Item	Symbol	Va	Unit	Note	
item	Symbol	Min.	Max.	5	NOLE
Storage Temperature	T <sub>ST</sub>	-20	+60	°C	(1)
Operating Ambient Temperature	T <sub>OP</sub>	0	+50	ပွ	(1), (2)
Storage Humidity	H <sub>ST</sub>	5	85	%	-
Operation Humidity	H <sub>OP</sub>	20	85	%	-
Shock (Non-Operating)	S <sub>NOP</sub>	-	50	G	(3), (5)
Vibration (Non-Operating)	$V_{NOP}$	-	2	G	(4), (5)

Note (1) Temperature and relative humidity range is shown in the figure below.

- (a) 85 %RH Max. (Ta  $\leq$  40 °C).
- (b) Wet-bulb temperature should be 39 °C Max. (Ta > 40 °C).
- (c) No condensation of water.

# **Relative Humidity (%RH)**



- Note (2) The temperature of panel surface should be 0 °C Min. and 60 °C Max.
- Note (3) 6ms, 1 time each  $\pm X, \pm Y$  and  $\pm Z$  directions
- Note (4) 10 ~ 500 Hz, 1 cycle/20min. 1.5mm max, 1 hour each X, Y and Z directions
- Note (5) At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.





#### 2.2 ELECTRICAL ABSOLUTE RATINGS

## 2.2.1 TFT LCD MODULE

Itom	Svmbol	Va	lue	Linit	Note
Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	Vcc	Vss-0.3	4.0	V	

#### 2.2.2 BACKLIGHT UNIT

Itom	Symbol	Va	lue	Unit	Note
Item	Symbol	Min.	Max.	Offic	Note
Lamp Voltage	$V_L$	(640)	(800)	$V_{RMS}$	$(1), (2), I_L = (5.5) \text{ mA}$
Lamp Current	ΙL	2.0	6.5	mA <sub>RMS</sub>	(1) (2)
Lamp Frequency	FL	30	80	KHz	(1), (2)

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

Note (2) Specified values are for lamp (Refer to Section 3.2 for further information).



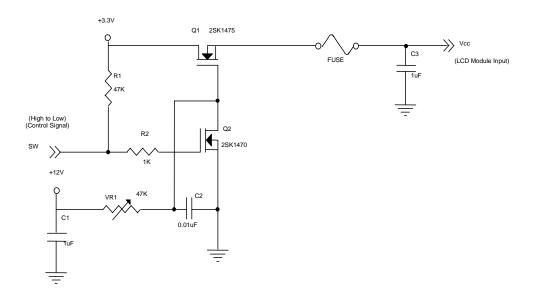
## 3. ELECTRICAL CHARACTERISTICS

## 3.1 TFT LCD MODULE

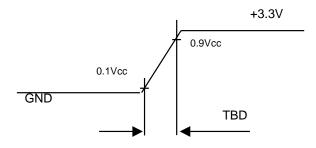
Parameter	Symbol Value				Unit	Note	
Farameter	Symbol	Min.	Тур.	Max.	o iii	NOLE	
Power Supply Voltage		Vcc	3.0	3.3	3.6	V	-
Ripple Voltage		$V_{RP}$	-	TBD		mV	-
Rush Current		I <sub>RUSH</sub>	-	•	TBD	Α	(2)
	White		-	TBD	Ī	mΑ	(3)a
Power Supply Current	Black	lcc	-	TBD	-	mΑ	(3)b
	Vertical Stripe		-	TBD	-	mA	(3)c
Differential Input Voltage for "H" Level		V <sub>IH</sub>	2.64	-	Vcc	mV	-
LVDS Receiver Threshold "L" Level		V <sub>IL</sub>	GND	-	0.6	mV	-
Terminating Resistor		R <sub>T</sub>	-	100	-	Ohm	-

Note (1) The module should be always operated within above ranges.

# Note (2) Measurement Conditions:

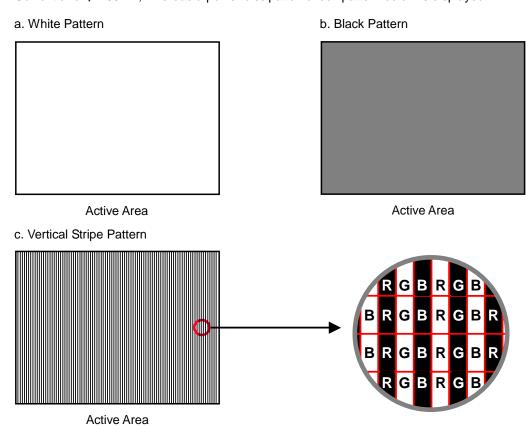


# Vcc rising time is TBD





Note (3) The specified power supply current is under the conditions at Vcc =3.3V, Ta =  $25 \pm 2$  °C, DC Current and  $f_v = 60$  Hz, whereas a power dissipation check pattern below is displayed.

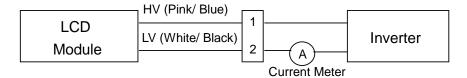


## 3.2 BACKLIGHT UNIT

Ta = 25 ± 2 °C

Parameter	Symbol		Value	Unit	Note		
Farameter	Syllibol	Min.	Тур.	Max.	Offic	Note	
Lamp Input Voltage	$V_L$	(640)	(720)	(800)	$V_{RMS}$	$I_L = 5.5 \text{mA}$	
Lamp Current	ΙL	(2.0)	(5.5)	(6.5)	$mA_{RMS}$	(1)	
Loren Turn On Voltage	Vs	-	-	(1150) (25 °C)	$V_{RMS}$	(2)	
Lamp Turn On Voltage		ı	-	(1500) (0 °C)	$V_{RMS}$	(2)	
Operating Frequency	F∟	(30)	(45)	(80)	KHz	(3)	
Lamp Life Time	$L_BL$	50,000		-	Hrs	(5)	
Power Consumption	$P_L$	-	(9360)	-	mW	$(4), I_L = 5.5 \text{ mA}$	

Note (1) Lamp current is measured by utilizing a high frequency current meter as shown below:



Note (2) The voltage shown above should be applied to the lamp for more than 1 second after startup. Otherwise the lamp may not be turned on.

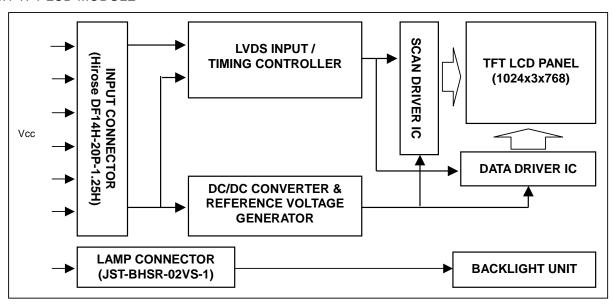


- Note (3) The lamp frequency may generate interference with horizontal synchronous frequency from the display, and this may cause line flow on the display. In order to avoid interference, the lamp frequency should be detached from the horizontal synchronous frequency and its harmonics as far as possible.
- Note (4)  $P_L = I_L \times V_L$
- Note (5) The lifetime of lamp is defined as the time when it continues to operate under the conditions at Ta =  $25 \pm 2$  °C and I<sub>L</sub> =7.0mA<sub>RMS</sub> until one of the following events occurs:
  - (a) When the brightness becomes  $\leq$  50% of its original value.
  - (b) When the effective ignition length becomes  $\leq$  80% of its original value. (Effective ignition length is defined as an area that the brightness is less than 70% compared to the center point.)
- Note (6) The waveform of the voltage output of inverter must be area-symmetric and the design of the inverter must have specifications for the modularized lamp. The performance of the Backlight, such as lifetime or brightness, is greatly influenced by the characteristics of the DC-AC inverter for the lamp. All the parameters of an inverter should be carefully designed to avoid generating too much current leakage from high voltage output of the inverter. When designing or ordering the inverter please make sure that a poor lighting caused by the mismatch of the Backlight and the inverter (miss-lighting, flicker, etc.) never occurs. If the above situation is confirmed, the module should be operated in the same manners when it is installed in your instrument.

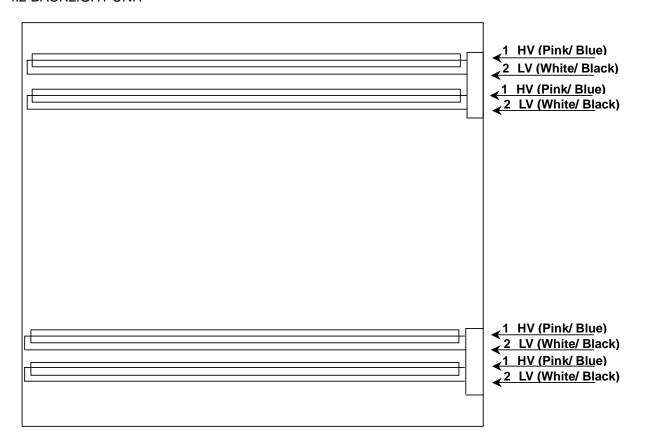


#### 4. BLOCK DIAGRAM

## 4.1 TFT LCD MODULE



#### 4.2 BACKLIGHT UNIT





## 5. INPUT TERMINAL PIN ASSIGNMENT

## 5.1 TFT LCD MODULE

Pin No.	Symbol	Function	Polarity	Note
1	VDD	Power Supply +3.3V(typical)		
2	VDD	Power Supply +3.3V(typical)		
3	GND	Ground		
4	GND	Ground		
5	RX0-	LVDS Differential Data Input	Negative	
6	RX0+	LVDS Differential Data Input	Positive	
7	GND	Ground		
8	RX1-	LVDS Differential Data Input	Negative	
9	RX1+	LVDS Differential Data Input	Positive	
10	GND	Ground		
11	RX2-	LVDS Differential Data Input	Negative	
12	RX2+	LVDS Differential Data Input	Positive	
13	GND	Ground		
14	RXCLK-	LVDS Differential Data Input	Negative	
15	RXCLK+	LVDS Differential Data Input	Positive	
16	GND	Ground		
17	NC	NC		
18	NC	NC		
19	GND	GND		
20	TST	Test		

(1)Connector Part No.: [Hirose] DF14H-20P-1.25H

(2) Matching socket Part No.: [Hirose] DF14-20S-1.25C

#### 5.2 BACKLIGHT UNIT

Pin	Symbol	Description	Color
1	HV1	High Voltage	Pink/ Blue
2	LV	Ground	White/ Black

Note (1) Connector Part No.: BHSR-02VS-1 (JST) or equivalent

Note (2) Matching Connector Part No.: SM02B-BHS-1-TB (JST) or equivalent



## 5.3 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color. The higher the binary input the brighter the color. The table below provides the assignment of color versus data input.

	· · · · · · · · · · · · · · · · · · ·	Data Signal																							
	Color	Red Green Blue																							
		R7	R6	R5	R4	R3	R2	R1	R0	R7	R6	G5	G4		G2	G1	G0	R7	R6	B5	B4	В3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Colors		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Scale	:	:	:	:	:	:	:	:	;	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Of	Red(252)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	Red(252)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(252)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
Grav	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
Scale	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Of	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Green	Green(252)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
0.00	Green(252)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(252)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Scale	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Of	:	:	:	:	:	:	:	:	:	:	:	:		:	:	:	:	:	;	:	:	;	:	:	
Blue	Blue(252)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(252)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(252)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage



# 6. INTERFACE TIMING

# 6.1 INPUT SIGNAL TIMING SPECIFICATIONS

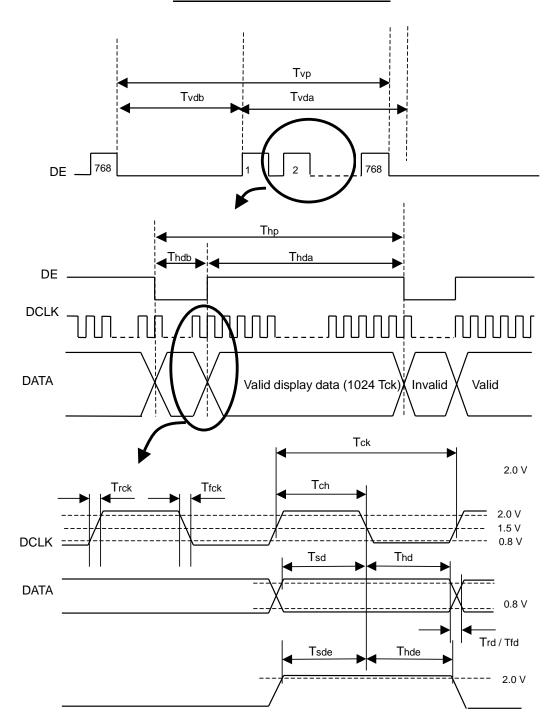
The input signal timing specifications are shown as the following table and timing diagram.

Signal	Parameter	Symbol	Min	Тур	Max	Unit	Remarks
DCLK	Pixel clock Frequency	Fck	-	65	80	MHz	
	Pixel clock period	Tck	20	15	12.5	ns	
	Duty ratio (%Tch)	-	45	50	55	%	Tch/Tck
	High time	Tckh	5	-	- ns		
	Low time	Tckl	5	1	-	ns	
DATA	Setup time	Tsd	4	ı	-	ns	
DE	Hold time	Thd	4	•	-	ns	
	Setup time	Tsde	4	ı	-	ns	
	Hold time	Thde	4	-	-	ns	
	Vertical Frequency	Fv	-	60	75	Hz	
Vertical	Vertical display active period	Tvda	768	768	768	Thp	
	Vertical display blank period	Tvdb	1	38	-	Thp	
	Vertical period	Tvp	769	806	-	Thp	
	Horizontal display active	Thda	1024	1024	1024	Tck	
Horizontal	period	mua	1024	1024	1024	ICK	
	Horizontal display blank period	Thdb	76	320	776	Tck	
	Horizontal period	Thp	1100	1344	1800	Tck	

Note (1) Because this module is operated by DE only mode, Hsync and Vsync input signals should be set to low logic level or ground. Otherwise, this module would operate abnormally.

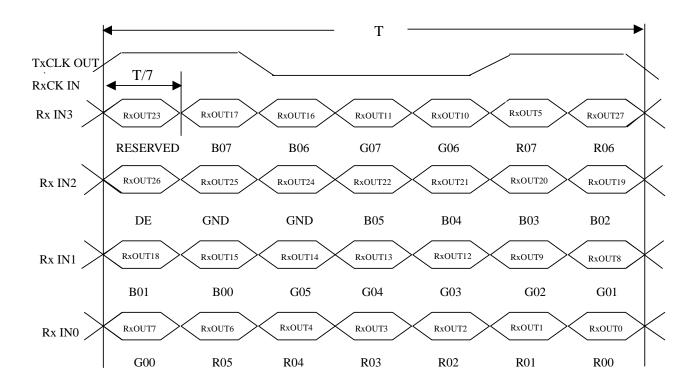


# **INPUT SIGNAL TIMING DIAGRAM**



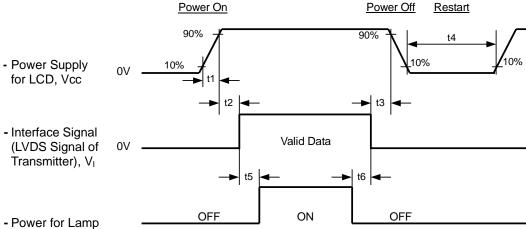


# **TIMING DIAGRAM of LVDS**





#### 6.2 POWER ON/OFF SEQUENCE



Timing Consideration

Timing Specifications:

 $0 < t1 \leq 10 \text{ msec}$ 

 $0 < t2 \le 50 \text{ msec}$ 

 $0 < t3 \leq 50 \, \text{msec}$ 

 $t4 \ge 1 sec$ 

 $t5 \ge 100 \text{ msec}$ 

 $t6 \ge 100 \text{ msec}$ 

Note (1) Please avoid floating state of interface signal at invalid period.

Note (2) When the interface signal is invalid, be sure to pull down the power supply of LCD Vcc to 0 V.

Note (3) The Backlight inverter power must be turned on after the power supply for the logic and the interface signal is valid. The Backlight inverter power must be turned off before the power supply for the logic and the interface signal is invalid.



## 7. OPTICAL CHARACTERISTICS

# 7.1 TEST CONDITIONS

ltem	Symbol	Value	Unit					
Ambient Temperature	Ta	25±2	°C					
Ambient Humidity	На	50±10	%RH					
Supply Voltage	V <sub>CC</sub>	5.0	V					
Input Signal	According to typical value in "3. ELECTRICAL CHARACTERISTICS"							
Inverter Current	Ι <sub>L</sub>	5.5	mA					

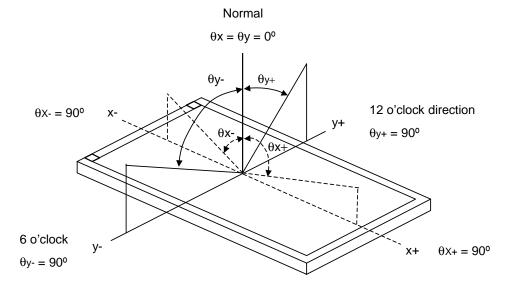
The measurement methods of optical characteristics are shown in Section 7.2. The following items should be measured under the test conditions described in Section 7.1 and stable environment shown in Note (4).

# 7.2 OPTICAL SPECIFICATIONS

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note	
Contrast Ratio		CR		250	(300)	-	-	(2), (4)	
Response Time		$T_R$		-	10	20	ms	(2)	
		$T_F$		-	20	30	ms	(3)	
Center Luminan	ce of White	L		200	(250)	-	cd/m <sup>2</sup>	(4)	
	Red	Rx		-	(0.623)	-	-		
	Reu	Ry	$\theta_{x}=0^{\circ}, \ \theta_{Y}=0^{\circ}$	-	(0.357)	-	-		
	Green	Gx	Viewing Normal Angle	-	(0.284)	-	-		
Color		Gy		-	(0.594)	-	-		
Chromaticity	Blue	Bx		-	(0.143)	-	-		
		Ву		-	(0.091)	-	-	(1), (4)	
		Wx		-	0.298	-	-		
		Wy		-	0.322	-	-		
	Horizontal	$\theta_x$ +		50	60	-			
Viewing Angle	HOHZOHIAI	$\theta_{x}$ -	OD>40	50	60	-	Dog		
	Vertical	θ <sub>Y</sub> +	CR≥10	30	40	-	Deg.		
	Vertical	θ <sub>Y</sub> -		50	60	-			



#### Note (1) Definition of Viewing Angle ( $\theta x$ , $\theta y$ ):



# Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

Contrast Ratio (CR) = L63 / L0

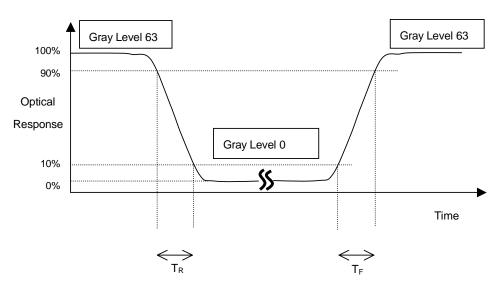
L63: Luminance of gray level 63

L 0: Luminance of gray level 0

CR = CR (5)

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (5).

# Note (3) Definition of Response Time (T<sub>R</sub>, T<sub>F</sub>):

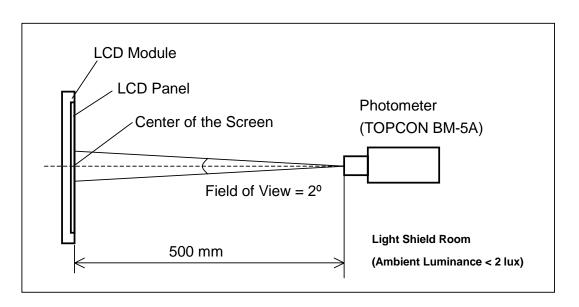


18 / 20

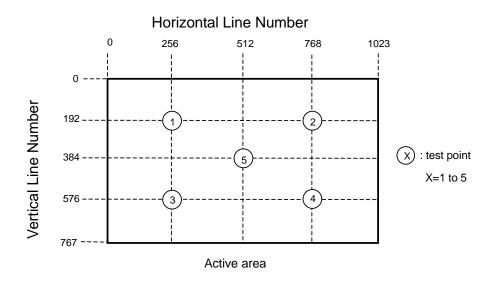


# Note (4) Measurement Setup:

The LCD module should be stabilized at given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room.



Note (5) Definition of luminance measured points:



Horizontal Line Number [pixel]



#### 8. PRECAUTIONS

#### 8.1 HANDLING PRECAUTIONS

- (1) The module should be assembled into the system firmly by using every mounting hole. Be careful not to twist or bend the module.
- (2) While assembling or installing modules, it can only be in the clean area. The dust and oil may cause electrical short or damage the polarizer.
- (3) Use fingerstalls or soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (4) Do not press or scratch the surface harder than a HB pencil lead on the panel because the polarizer is very soft and easily scratched.
- (5) If the surface of the polarizer is dirty, please clean it by some absorbent cotton or soft cloth. Do not use Ketone type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanently damage the polarizer due to chemical reaction.
- (6) Wipe off water droplets or oil immediately. Staining and discoloration may occur if they left on panel for a long time.
- (7) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contacting with hands, legs or clothes, it must be washed away thoroughly with soap.
- (8) Protect the module from static electricity, it may cause damage to the C-MOS Gate Array IC.
- (9) Do not disassemble the module.
- (10) Do not pull or fold the lamp wire.
- (11) Pins of I/F connector should not be touched directly with bare hands.

#### **8.2 STORAGE PRECAUTIONS**

- (1) High temperature or humidity may reduce the performance of module. Please store LCD module within the specified storage conditions.
- (2) It is dangerous that moisture come into or contacted the LCD module, because the moisture may damage LCD module when it is operating.
- (3) It may reduce the display quality if the ambient temperature is lower than 10 °C. For example, the response time will become slowly, and the starting voltage of lamp will be higher than the room temperature.

#### 8.3 OPERATION PRECAUTIONS

- (1) Do not pull the I/F connector in or out while the module is operating.
- (2) Always follow the correct power on/off sequence when LCD module is connecting and operating. This can prevent the CMOS LSI chips from damage during latch-up.
- (3) The startup voltage of Backlight is approximately 1000 Volts. It may cause electrical shock while assembling with inverter. Do not disassemble the module or insert anything into the Backlight unit.

