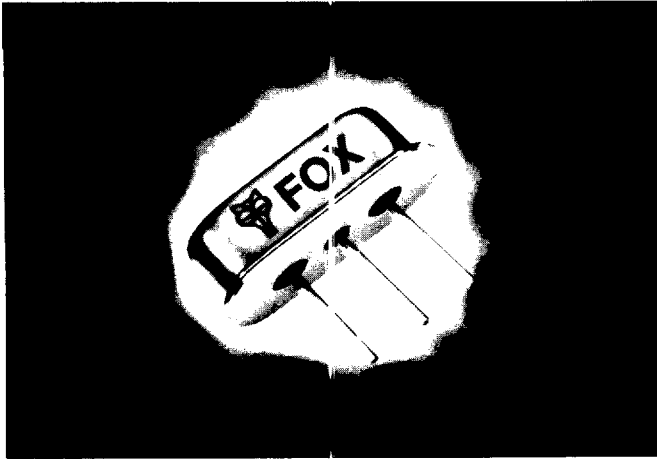


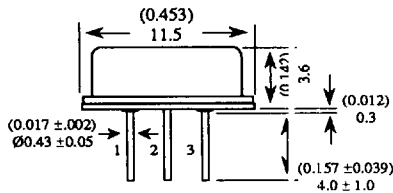
F3240 & F3260 / ULTRA SMALL OSCILLATORS



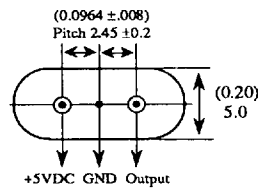
The FOX F3240 & F3260 represent a substantially reduced volume over the 14 pin dip and can drive both HCMOS or TTL loads in applications such as notebook or palmtop computers. The F3240 is designed for driving HCMOS loads of up to 50 pF. The F3260 is designed for applications for smaller loads and features tighter symmetry and lower input current.

FEATURES

Industry Compatible Pinout
Miniature Size



Metric dimensions shall govern
All dimensions are in millimeters & parenthetically in inches



Pin Connections

#1 +5VDC #3 Output
#2 GND (Case)

• F3240 / F3260 OPERATING CONDITIONS

Frequency Range	(Fo)	F3240 1.8432 - 67.000 MHz
		F3260 1.8432 - 50.000 MHz
Temperature Range - Operating	(TA)	-10 to +70 °C
Storage	(TSTG)	-55 to +125 °C
Supply Voltage	(VDD)	5.0 ± 0.5 V

• PART NUMBER SELECTION

FREQUENCY STABILITY	PART NUMBER	
±100PPM (STD)	F3240	F3260
±50PPM	F3245	F3265

• ELECTRICAL CHARACTERISTICS (TA = 25°C, VDD = 5.0V, CL = max load)

PARAMETERS	FREQUENCY RANGES	CONDITION	F3240			F3260			UNITS
			MIN	TYP	MAX	MIN	TYP	MAX	
Frequency Stability		All Conditions * TA = 25°C TA = -10 to +70°C	-100	±20 ±40	100	-100	±20 ±40	100	PPM
Input Current (I _{DD})	1.8432 ~ 25.000 25.000+ ~ 50.000 50.000+ ~ 67.000	Maximum Load		17 33 45	25 45 60		13 25 ---	20 35 ---	mA
Output Symmetry		50% VDD Level	40	50 ± 5	60	45	50 ± 3	55	%
Rise Time (T _R)		10% ~ 90% VDD Level		4	7		5	10	ns
Fall Time (T _F)		90% ~ 10% VDD Level		4	7		5	10	ns
Output Voltage (V _{OL})		'0' Logic Level			0.5			0.5	V
(V _{OH})		'1' Logic Level	4.5			4.5			V
Output Current (I _{OL})		'0' Logic Level	16			4			mA
(I _{OH})		'1' Logic Level			-16			-4	mA
Output Load		TTL			10			10 LS	TTL
		HCMOS			50			15	pF
Start-up Time (T _s)		0.0V to 5.0V		3	10		3	10	ms

* Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration.
See page 43 for environmental/mechanical specifications, test circuits, and output waveforms.
All specifications subject to change without notice. Rev. 1/30/93