

2STA1695

High power PNP epitaxial planar bipolar transistor

General features

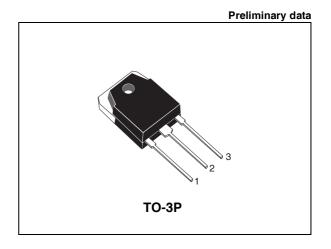
- High breakdown voltage V_{CEO} = -140V
- Complementary to 2STC4468
- Typical f_t =20MHz
- Fully characterized at 125 °C

Applications

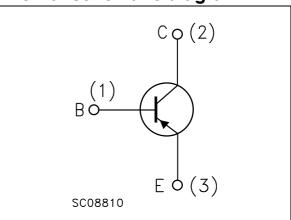
Audio power amplifier

Description

The device is a PNP transistor manufactured using new BiT-LA (Bipolar transistor for linear amplifier) technology. The resulting transistor shows good gain linearity behaviour. Recommended for 70W to 100W high fidelity audio frequency amplifier output stage.



Internal schematic diagram



Order codes

Part Number	Marking	Package	Packaging
2STA1695	2STA1695	TO-3P	Tube

Electrical ratings

Table 1. Absolute maximum rating

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-emitter voltage (I _E = 0)	-140	V
V _{CEO}	Collector-emitter voltage (I _B = 0)	-140	V
V _{EBO}	Collector-base voltage (I _C = 0)	-6	V
I _C	Collector current	-10	Α
I _{CM}	Collector peak current (t _P < 5ms)	-20	Α
P _{TOT}	Total dissipation at T _c = 25°C	100	W
T _{stg}	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 2. Thermal data

Symbol	Parameter	Value	Unit
R _{thj-case}	Thermal resistance junction-case max	1.25	°C/W



2STA1695 Electrical characteristics

1 Electrical characteristics

 $(T_{CASE} = 25^{\circ}C; unless otherwise specified)$

Table 3. Electrical characteristics

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I _{CBO}	Collector cut-off current (I _E = 0)	V _{CB} = -140V			-0.1	μА
I _{EBO}	Emitter cut-off current (I _C = 0)	V _{EB} = -6V			-0.1	μА
V _{(BR)CEO} ⁽¹⁾	Collector-emitter breakdown voltage (I _B = 0)	I _C = -50mA	-140			V
	Collector-emitter breakdown voltage (I _E = 0)	I _C = -100μA	-140			V
V _{(BR)EBO} ⁽¹⁾	Collector-emitter breakdown voltage (I _C = 0)	I _E = -1mA	-6			V
V _{CE(sat)} (1)	Concotor crimitor cataration	$I_C = -5A$ $I_B = -500 \text{mA}$ $I_C = -7A$ $I_B = -700 \text{mA}$			-0.5 -0.7	V V
V _{BE} ⁽¹⁾	Base-emitter voltage	$V_{CE} = -5V$ $I_C = -5A$			-1.3	V
h _{FE}	DC current gain	$I_{C} = -3A$ $V_{CE} = -4V$ $I_{C} = -5A$ $V_{CE} = -4V$	70 50		140	
f _T	Transition frequency	$I_C = -0.5A$ $V_{CE} = -12V$		20		MHz
C _{CBO}	Collector-base capacitance	$I_E = 0$ $V_{CB} = -10V$ $f = 1MHz$		225		pF
	Resistive load					
t _{on}	Turn-on time	$I_C = -5A$ $V_{CC} = -60V$		0.24		μs
t _{stg}	Storage time	$I_{B1} = -I_{B2} = -0.5A$		1.2		μs
t _{off}	Fall time			0.24		μs

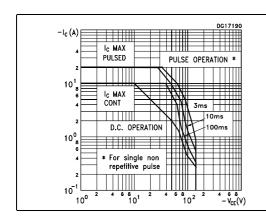
Note: 1 Pulsed duration = 300 μ s, duty cycle \leq 1.5%

Electrical characteristics 2STA1695

1.1 Electrical characteristics (curves)

Figure 1. Safe operating area

Figure 2. Output characteristics



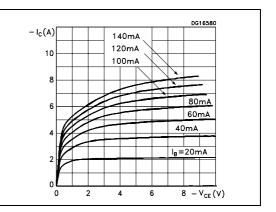
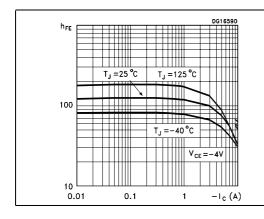


Figure 3. DC current gain

Figure 4. Collector-emitter saturation voltage



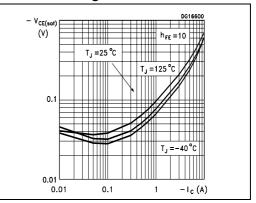
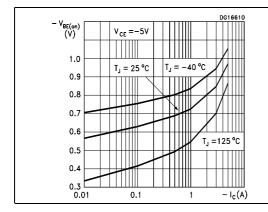
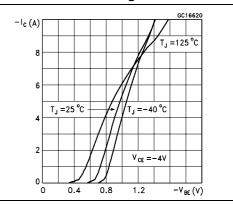


Figure 5. Base-emitter on voltage

Figure 6. Collector current vs baseemitter voltage

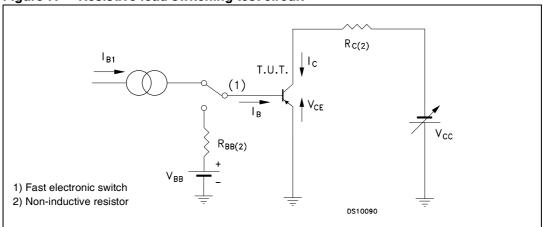




2STA1695 Electrical characteristics

1.2 Test circuit

Figure 7. Resistive load switching test circuit



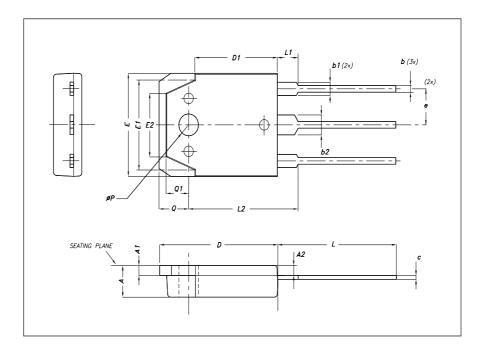
Package mechanical data 2STA1695

2 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

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DIM.	mm.			
	MIN.	TYP	MAX.	
A	4.6		5	
A1	1.45	1.50	1.65	
A2	1.20	1.40	1.60	
b	0.80	1	1.20	
b1	1.80		2.20	
b2	2.80		3.20	
С	0.55	0.60	0.75	
D	19.70	19.90	20.10	
D1		13.90		
E	15.40		15.80	
E1		13.60		
E2		9.60		
е	5.15	5.45	5.75	
L	19.50	20	20.50	
L1		3.50		
L2	18.20	18.40	18.60	
Р	3.10		3.30	
Q		5		
Q1		3.80		



Revision history 2STA1695

3 Revision history

Table 4. Revision history

Date	Revision	Changes
18-May-2007	1	Initial release

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