CMOS Data Communication Circuits

Asynchronous Communications Interface Adapter (ACIAs)

Туре	Function	Features	Package No. of Pins*
CDP65C51	ACIAs	Compatible with 8-bit microprocessors	28
		Full duplex operation with buffered receiver and transmitter	D
		Data set/modem control functions	E
		Internal baud rate generator with 15 programmable baud rates (50 to 19,200)	М
CDP6853		Program-selectable internally or externally controlled receiver rate	28
	1	Programmable word lengths, number of stop bits and parity bit generation	D
		and detection	E
		Operates at baud rates up to 250,000 via proper crystal or clock selection	
	1	Single 3V to 6V power supply	
		Programmable interrupt control	
	1	Program reset	
		Program-selectable serial echo mode	
	1	Two chip selects	,
		One chip enable (CDP6853 only)	
		4MHz, 2MHz or 1MHz operation:	,
		► CDP65C51-4, CDP65C51-2, CDP65C51-1, respectively	
		Multiplexed address/data bus	
		Operating temperature range (maximum rating):	
		► Ceramic package (D): -55°C to +125°C	
		► Plastic package (E and M): -40°C to +85°C	
CDP68C51A	ACIA	Compatible with 8-bit microprocessors	28
		Full duplex operation with buffered receiver and transmitter	D
		Data set/modern control functions	E
		Internal baud rate generator with 15 programmable baud rates (50 to 19,200)	М
		Program-selectable internally or externally controlled receiver rate	
		Operates at baud rates up to 250,000 via proper crystal or clock selection	
		Progammable word lengths, humber of stop bits & parity bit generation & detection	
		Programmable interrupt control	
		Program reset	
		Program-selectable serial echo mode	
		Two chip selects	
		4MHz, 2MHz or 1MHz operation:	
		► CDP65C51A-4, CDP65C51A-2, CDP65C51A-1, respectively	
		Single 3V to 6V power supply	}
		• Full TTL compatibility	
		Synchronous CTS operation	

58E D

^{*} See interpretation guide and packaging section