



**VTC  
Incorporated**

**VA733**

**DIFFERENTIAL  
VIDEO AMPLIFIER**

T-74-07-01

#### FEATURES

- 80MHz Bandwidth at Av = 400
- Selectable Gains of 10, 100 and 400
- No Frequency Compensation Required
- Low Input Noise: Broadband Noise 4 $\mu$ Vrms  
Spot Noise 0.9nV/ $\sqrt{\text{Hz}}$

#### DESCRIPTION

The VA733 is a two-stage differential input, differential output wideband video amplifier. It offers jumper selectable gain configurations of 10, 100 and 400 or adjustable gains from 10 to 400 with the use of an external resistor. The amplifier has a bandwidth in excess of 80MHz for all three gain configurations without any gain compensating components.

The VA733 is intended for use as a high quality video and pulse processing amplifier for the communications and computer industries. For example, the large gain bandwidth product and low input noise level of 4 $\mu$ Vrms makes the amplifier an excellent choice as a read amplifier in tape and disk data recovery applications.

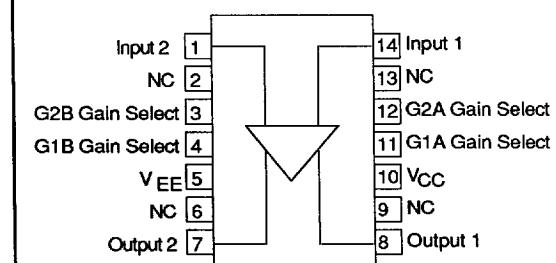
#### ABSOLUTE MAXIMUM RATINGS

Supply Voltages . . . . .	$\pm 8V$
Differential Input Voltage . . . . .	$\pm 5V$
Common Mode Input Voltage . . . . .	$\pm 6V$
Output Current . . . . .	10mA
Junction Temperature . . . . .	150°C
Power Dissipation ( $T_A=70^\circ\text{C}$ , Note 1) . . . . .	550mW
Operating Temperature Range:	
Commercial (733 J) . . . . .	0°C to 70°C
Storage Temperature Range . . . . .	-65°C to +150°C
Lead Temperature (Soldering to 60 Sec.) . . . . .	300°C

Note 1: Power derating above  $T_A = 70^\circ\text{C}$  to be based on a maximum junction temperature of 150°C and the thermal resistance factors of  $\theta_{JC} = 75^\circ\text{C/W}$  and  $\theta_{JA}=145^\circ\text{CW}$

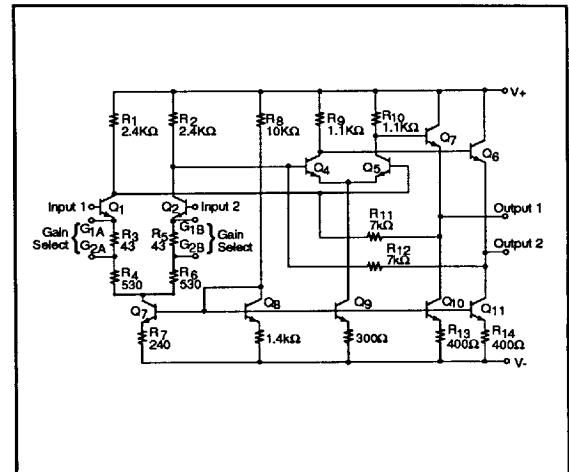
#### CONNECTION DIAGRAM

14-Lead SOIC Package



Top View

#### SIMPLIFIED SCHEMATIC



#### PACKAGE TYPES AVAILABLE

- 14-Pin SOIC

# DISCONTINUED

VA733

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**ELECTRICAL CHARACTERISTICS**  $V_{CC} = +6.0\text{VDC}$ ,  $V_{EE} = -6.0\text{Vdc}$ , at  $T_A = 25^\circ\text{C}$  (unless otherwise stated)

PARAMETER	SYM	TEST CONDITIONS	VA733J			UNITS
			MIN	TYP	MAX	
Differential Voltage Gain, Gain 1 (Note 1) Gain 2 (Note 2) Gain 3 (Note 3)	Avd	$R_{OD}=2\text{k}\Omega$ $V_{OD}=1\text{V}$	250 75 7.5	380 85 8.5	600 110 11	V/V
Bandwidth Gain 1 Gain 2 Gain 3	BW	$R_S=50\Omega$		80 80 90		MHz
Rise Time Gain 1 Gain 2 Gain 3	$t_{TLH}$ $t_{THL}$	$R_S=50\Omega$ $V_{OUT}=1\text{V p-p}$		6.0 5.0 5.0	12	ns
Propagation Delay Gain 1 Gain 2 Gain 3	$t_{PLH}$ $t_{PHL}$	$R_S=50\Omega$ $V_{OUT}=1\text{V p-p}$		4.8 4.8 4.3	10	ns
Input Resistance Gain 1 Gain 2 Gain 3	$R_{IN}$		4	1.5 7 70		k $\Omega$
Input Capacitance (Gain 2)	$C_{IN}$			9		pF
Input Offset Current (Gain 3)	$ I_{IO} $			0.4	10	$\mu\text{A}$
Input Bias Current (Gain 3)	$ I_{IB} $			45	100	$\mu\text{A}$
Input Noise Voltage Broadband (Gain 1)	$V_n$	$R_S=50\Omega$ BW = 1kHz to 10MHz		4		$\mu\text{V(rms)}$
Input Noise Voltage Spot Noise (Gain 1)		BW = 20Hz $f = 1\text{KHz to } 20\text{MHz}$		0.9		nV/ $\sqrt{\text{Hz}}$
Input Voltage Range (Gain 3)	$V_{IN}$		$\pm 1.0$	+1.4 -3.2		V
Common-Mode Rejection Ratio Gain 2 Gain 2	CMRR	$V_{CM} \pm 1\text{V}, f < 100\text{kHz}$ $V_{CM} \pm 1\text{V}, f = 5\text{MHz}$	60	88 50		dB
Supply Voltage Rejection Ratio Gain 2	PSRR	$(\Delta V_S = \pm 0.5\text{V})$	50	70		dB
Output Offset Voltage Gain 1 Gain 2 and Gain 3	$V_{OO}$			0.2 0.2	1.5 1	V V
Output Common-Mode Voltage Gain 3	$V_{CMO}$		2.4	2.9	3.4	V
Output Voltage Swing (Gain 2)	$V_O$		3.0	4.0		Vp-p
Output Sink Current (Gain 2)	$I_O$		2.5	3.6		mA
Output Resistance	$R_{OUT}$			35		$\Omega$
Power Supply Current (Gain 2)	$I_D$			17	24	mA

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**ELECTRICAL CHARACTERISTICS (continued)**V<sub>CC</sub> = +6.0Vdc, V<sub>EE</sub> = -6.0Vdc, at T<sub>A</sub> = T<sub>HIGH</sub> to T<sub>LOW</sub> (unless otherwise stated) (Note 4)

PARAMETER	SYMBOL	VA733J			UNITS
		MIN	TYP	MAX	
Differential Voltage Gain Gain 1 (Note 1) Gain 2 (Note 2) Gain 3 (Note 3)	A <sub>vd</sub>	250 65 6.5		600 110 11	V/V
Input Resistance (Gain 2)	R <sub>in</sub>	3K			kΩ
Input Offset Current (Gain 3)	I <sub>IO</sub>			20	μA
Input Bias Current (Gain 3)	I <sub>B</sub>			200	μA
Input Voltage Range (Gain 3)	V <sub>IN</sub>	±1.0			V
Common Mode Rejection Ratio	CMRR	50			dB
Supply Voltage Rejection Ratio Gain 2 (AV <sub>S</sub> = ± 0.5V)	PSRR	50			dB
Output Offset Voltage Gain 1 Gain 2 and Gain 3	V <sub>OO</sub>			1.5 1.5	V
Output Voltage Swing (Gain 2)	V <sub>O</sub>	2.5			V <sub>p-p</sub>
Output Sink Current (Gain 2)	I <sub>O</sub>	2.5			mA
Power Supply Current (Gain 2)	I <sub>D</sub>			27	mA

Notes: 1. Gain select pins G<sub>1A</sub> and G<sub>1B</sub> connected together2. Gain select pins G<sub>2A</sub> and G<sub>2B</sub> connected together

3. All gain select pins open

4. T<sub>LOW</sub>=0°CT<sub>HIGH</sub>=70°C

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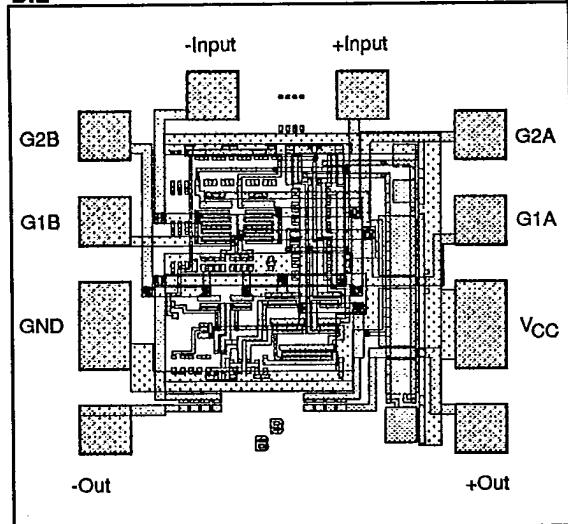
**DIE INFORMATION**

WAFER TEST LIMITS		VA733			
PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS
Differential Voltage Gain Gain 1 (Note 1) Gain 2 (Note 2) Gain 3 (Note 3)	Avd	300 75 7.5	380 85 8.5	500 100 10	V/V
Input Resistance (Gain 2)	R <sub>IN</sub>	4	7		kΩ
Input Offset Current (Gain 3)	I <sub>IO</sub>			10	μA
Input Bias Current (Gain 3)	I <sub>IB</sub>			100	μA
Input Voltage Range (Gain 3)	V <sub>IN</sub>	±1.0			V
Common Mode Rejection Ratio	CMRR	50			dB
Supply Voltage Rejection Ratio Gain 2 ( $\Delta V_S = \pm 0.5V$ )	PSRR	50			dB
Output Offset Voltage Gain 1 Gain 2 and Gain 3	V <sub>OO</sub>			1.5 1.2	V
Output Voltage Swing (Gain 2)	V <sub>O</sub>	2.5			V <sub>p-p</sub>
Output Sink Current (Gain 2)	I <sub>O</sub>	2.2			mA
Power Supply Current (Gain 2)	I <sub>D</sub>		17	24	mA

Notes: 1. Gain Option 1, Gain-adjust pin G<sub>1A</sub> is connected to pin G<sub>1B</sub>, and pins G<sub>2A</sub> and G<sub>2B</sub> are open.

2. Gain Option 2, Gain-adjust pin G<sub>1A</sub> and G<sub>1B</sub> are open, and pin G<sub>2A</sub> is connected to pin G<sub>2B</sub>.

3. Gain Option 3, All four gain-adjust pins are open.

**DIE**

Die size = 0.046 X 0.046 inch (2116 sq mils)  
= 1.17 X 1.17mm (1.37sq mm)

**DICE POLICY****Electrical Characteristics**

Each die is electrically tested to the commercial or military grade DC parameters to guard band limits at 25°C to guarantee operation over the full temperature range.

**Quality Assurance**

All dice are 100% visually inspected to the requirement of MIL-STD-883C, Method 2010.2, Condition 3.

All dice are glass passivated with only the bonding pads exposed to provide scratch protection.

All dice are provided with gold backing.

**Shipping Packages/Order Information**

All dice are packaged in die crates with individual compartments which prevent damage to the die during shipping. Minimum order for dice is 100, supplied only in multiples of 100.