

PE77D Series 2.5 V PECL Clock Oscillators

October 2006



- Pletronics' PE77D Series is a quartz crystal controlled precision square wave generator with a PECL output.
- The package is designed for high density surface mount designs.
- Low cost mass produced oscillator.
- Tape and Reel or Tube packaging is available.
- 40 to 250 MHz
- 5 x 7 mm LCC Ceramic Package
- Enable/Disable Function on pad 1
- Disable function includes low standby power mode
- 3rd Overtone Crystals used
- Improved circuit to minimize oscillator issues such as multi-mode output signal.
- Low Jitter

Pletronics Inc. certifies this device is in accordance with the RoHS 6/6 (2002/95/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's Weight of the Device: 0.16 grams Moisture Sensitivity Level: 1 As defined in J-STD-020C Second Level Interconnect code: e4

Absolute Maximum Ratings:

| Parameter | Unit |
|--------------------------------|---------------------------------|
| V _{cc} Supply Voltage | -0.5V to +7.0V |
| Vi Input Voltage | -0.5V to V _{cc} + 0.5V |
| Vo Output Voltage | -0.5V to V _{cc} + 0.5V |

Thermal Characteristics

The maximum die or junction temperature is 155°C

The thermal resistance junction to board is 30 to 50°C/Watt depending on the solder pads, ground plane and construction of the PCB.



PE77D Series 2.5 V PECL Clock Oscillators October 2006

Part Number:

| PE77 | 45 | D | E | w | -125.0M | -XX | | Part Marking: |
|------|----|---|---|---|---------|-----|--|---|
| | | | | | | | Packaging code or blank T250 = 250 per Tape and Reel T500 = 500 per Tape and Reel T1K = 1000 per Tape and Reel | PLE PE77 <i>FF.FFF</i> M • <i>YMDXX</i> or |
| | | | | | | | Frequency in MHz | PE7XYWWXX |
| | | | | | | | Supply Voltage V _{cc} W = 2.5V <u>+</u> 10% | FF.FFF M • PLE XXX |
| | | | | | | | Optional Enhanced OTR E = Temperature range -40 to +85°C | |
| | | | | | | | Series Model | |
| | | | | | | | Frequency Stability 45 = ± 50 ppm 44 = ± 25 ppm 20 = ± 20 ppm | |
| | | | | | | | Series Model | |

Marking Legend:

PLE = Pletronics

FF.FFFM = Frequency in MHz

YYWW or YWW or YMD = Date of Manufacture (year and week, or year-month-day) All other marking is internal factory codes

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Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from the marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

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| Code | 6 | | 1 | 2 | 5 | | 9 | 0 | | 1 | | 2 | | | |
|-------|-----|-----|------|------|----|-----|-----|-------|----|-----|-----|------|-----|-----|-----|
| Year | 200 |)6 | 2007 | 20 | 08 | 20 | 009 | 2010 | | 201 | 1 | 2012 | | | |
| | | | | | | | | | | | | | | | |
| Code | | Α | В | C | | D | Е | F | Ģ | ì | н | J | к | L | М |
| Month | ۱ | JAN | FEE | 3 MA | R | APR | MAY | ′ JUN | JU | IL | AUG | SEP | OCT | NOV | DEC |
| | | | | | | | | | | | | | | | |
| Code | 1 | | 2 | 3 | 4 | | 5 | 6 | 7 | | 8 | 9 | Α | В | С |
| Day | 1 | | 2 | 3 | 4 | | 5 | 6 | 7 | | 8 | 9 | 10 | 11 | 12 |
| Code | D | | Е | F | G | | Н | J | K | | L | М | Ν | Р | R |
| Day | 13 | | 14 | 15 | 16 | 3 | 17 | 18 | 19 | | 20 | 21 | 22 | 23 | 24 |
| Code | Т | | U | V | W | 1 | Х | Y | Z | | | | | | |
| Day | 25 | | 26 | 27 | 28 | 3 | 29 | 30 | 31 | | | | | | |

Codes for Date Code YMD



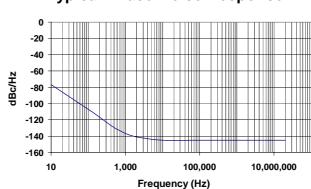
PE77D Series 2.5 V PECL Clock Oscillators October 2006

Electrical Specification for 2.50V ±5% over the specified temperature range

| Item | Min | Max | Unit | Condition | | | |
|---|--------|---------|--------|---|--|--|--|
| Frequency Range | 40 | 250 | MHz | | | | |
| Frequency Accuracy "45" | -50 | +50 | ppm | For all supply voltages, load changes, aging for 1 | | | |
| "44" | -25 | +25 | | year, shock, vibration and temperatures | | | |
| "20" | -20 | +20 | | | | | |
| Output Waveform | | PECL /E | CL | | | | |
| Output High Level (0°C to 85°C) | 1.475 | 1.760 | volts | Referenced to Ground, V_{cc} = 2.5 V | | | |
| | 0.975 | 1.260 | volts | Referenced to termination voltage, V_{cc} = 2.5 V | | | |
| | -1.025 | -0.740 | volts | Referenced to Vcc, V _{cc} = 2.5 V | | | |
| Output High Level (-40°C) | 1.415 | 1.620 | volts | Referenced to Ground, V_{cc} = 2.5 V | | | |
| | 0.915 | 1.12 | volts | Referenced to termination voltage, V_{cc} = 2.5 V | | | |
| | -1.085 | -0.88 | volts | Referenced to Vcc, V _{cc} = 2.5 V | | | |
| Output Low Level (0°C to 85°C) | 0.690 | 1.095 | volts | Referenced to Ground, V_{cc} = 2.5 V | | | |
| | 0.190 | 0.595 | volts | Referenced to termination voltage, V_{cc} = 2.5 V | | | |
| | -1.810 | -1.405 | volts | Referenced to Vcc, V _{cc} = 2.5 V | | | |
| Output Low Level (-40°C) | 0.670 | 1.195 | volts | Referenced to Ground, V_{cc} = 2.5 V | | | |
| | 0.170 | 0.695 | volts | Referenced to termination voltage, V_{cc} = 2.5 V | | | |
| | -1.830 | -1.305 | volts | Referenced to Vcc, V _{cc} = 2.5 V | | | |
| Output Symmetry | 45 | 55 | % | at 50% point of V_{cc} (See load circuit) | | | |
| Jitter | - | 0.6 | pS RMS | 12 KHz to 20 MHz from the output frequency | | | |
| | - | 2.8 | pS RMS | 10 Hz to 1 MHz from the output frequency | | | |
| Output T _{RISE} and T _{FALL} | - | 0.7 | nS | Vth is 20% and 80% of waveform | | | |
| V _{cc} Supply Current (I _{cc}) | - | 90 | mA | | | | |
| Enable/Disable Internal Pull-up | 50 | - | Kohm | to V _{cc} | | | |
| V disable | - | 0.6 | volts | Referenced to pad 3 | | | |
| V enable | 1.7 | - | volts | Referenced to pad 3 | | | |
| Output leakage V _{OUT} = V _{CC} | -10 | +10 | uA | Pad 1 low, device disabled | | | |
| V _{OUT} = 0V | -10 | +10 | uA | | | | |
| Enable time | - | 10 | nS | Time for output to reach a logic state | | | |
| Disable time | - | 10 | nS | Time for output to reach a high Z state | | | |
| Start up time | - | 10 | mS | Time for output to reach specified frequency | | | |
| Operating Temperature Range | 0 | +70 | °C | Standard Temperature Range | | | |
| | - 40 | +85 | °C | Extended Temperature Range "E" Option | | | |
| Storage Temperature Range | -55 | +125 | °C | | | | |
| Standby Current I _{cc} | - | 3 | uA | Pad 1 low, device disabled | | | |

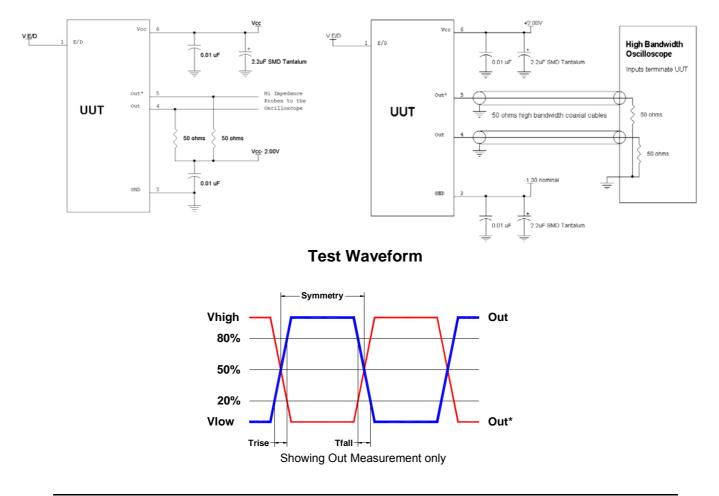
Specifications with Pad 1 E/D open circuit





Typical Phase-Noise Response

Load Circuit





Reliability: Environmental Compliance

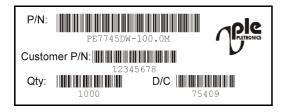
| Parameter | Condition |
|------------------|--------------------------------------|
| Mechanical Shock | MIL-STD-883 Method 2002, Condition B |
| Vibration | MIL-STD-883 Method 2007, Condition A |
| Solderability | MIL-STD-883 Method 2003 |
| Thermal Shock | MIL-STD-883 Method 1011, Condition A |

ESD Rating

| Model | Minimum Voltage | Conditions | | | |
|----------------------|-----------------|-------------------------|--|--|--|
| Human Body Model | 1500 | MIL-STD-883 Method 3115 | | | |
| Charged Device Model | 1000 | JESD 22-C101 | | | |

Package Labeling

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII



Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant

2nd LvL Interconnect Category=e4 Max Safe Temp=260C for 10s 2X Max



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October 2006

mm

7.00 <u>+</u>0.15

5.00 +0.15

1.70 max

0.96

5.08

0.10

1.27

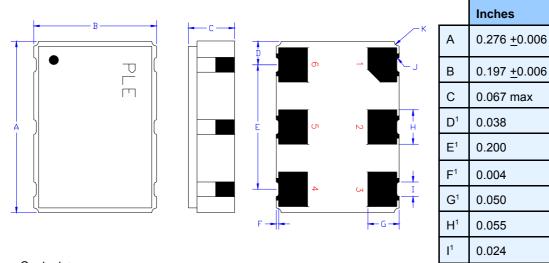
1.40

0.60

0.10R

0.20R

Mechanical:



Contacts: Gold 11.8 µinches 0.3 µm minimum over Nickel 50 to 350 µinches 1.27 to 8.89 µm

| ¹ Typical dimensions |
|---------------------------------|
| Not to Scale |

 J^1

 K^1

0.004R

0.008R

| Pad | Function | Note |
|-----|--------------------------------------|---|
| 1 | Output Enable/Disable | When this pad is not connected the oscillator shall operate. When this pad is <0.30 volts, the output will be inhibited (high impedance state.) Recommend connecting this pad to V_{CC} if the oscillator is to be always on. |
| 2 | No connect | There is no internal connection to this pad |
| 3 | Ground (GND) | |
| 4 | Output | Both outputs must be terminated and biased for proper operation. The ideal |
| 5 | Output* | termination is 50 ohms connected to 2.0V below the Supply Voltage. |
| 6 | Supply Voltage (V _{cc}) | Recommend connecting appropriate power supply bypass capacitors as close as possible. |

Layout and application information

Recommend connecting Pad 1 and Pad 2 together to permit the design to accept Enable/Disable input on either pad

For Optimum Jitter Performance, Pletronics recommends:

- a ground plane under the device
- no large transient signals (both current and voltage) should be routed under the device
- do not layout near a large magnetic field such as a high frequency switching power supply
- do not place near piezoelectric buzzers or mechanical fans.

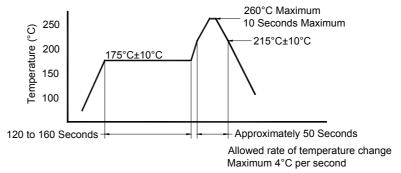






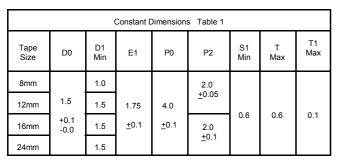
October 2006

Reflow Cycle (typical for lead free processing)



The part may be reflowed 2 times without degradation.

Tape and Reel: available for quantities of 250 to 1000 per reel



| Variable Dimensions Table 2 | | | | | | | | | |
|-----------------------------|------|-------|------------------|------------------|-----|------|--------|--|--|
| Tape Size | | | | | | | | | |
| 16 mm | 12.1 | 14.25 | 7.5 <u>+</u> 0.1 | 8.0 <u>+</u> 0.1 | 8.0 | 16.3 | Note 1 | | |

Note 1: Embossed cavity to conform to EIA-481-B

COVER TAPE

1

Ao

T2

Dimensions in mm

ØDo

 \oplus

D1

10 PITCHES CUMULATIVE TOLERANCE ON TAPE +/- 0.2 mm

-P2

-Pc





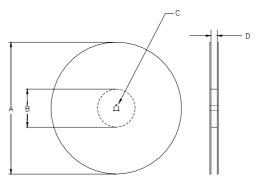
Ε1

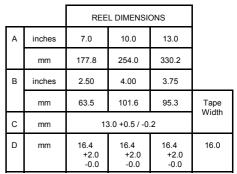
Ð

-ØD1

EMBOSSMENT FOR CAVITY SIZE SEE NOTE 1

E2





USER DIRECTION OF UNREELING -

Reel dimensions may vary from the above



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October 2006

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