

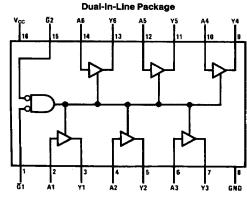
## **DM54365 Hex TRI-STATE® Buffers**

#### **General Description**

This device contains six independent gates each of which performs a non-inverting buffer function. The outputs have the TRI-STATE feature. When enabled, the outputs exhibit the low impedance characteristics of a standard TTL output with additional drive capability to permit the driving of bus lines without external resistors. When disabled, both the

output transistors are turned off presenting a high-impedance state to the bus line. Thus the output will act neither as a significant load nor as a driver. To minimize the possibility that two outputs will attempt to take a common bus to opposite logic levels, the disable time is shorter than the enable time of the outputs.

#### **Connection Diagram**



Order Number DM54365J or DM54365W See NS Package Number J16A or W16A

TL/F/6570-1

#### **Function Table**

Y = A							
	Input						
Ğ1	<b>G</b> 2	Α	Υ				
L	L	L	L				
L	L	н	Н				
Н	x	х	Hi-Z				
X	Н	Х	Hi-Z				

H = High Logic Level

L = Low Logic Level

X = Either Low or High Logic Level

Hi-Z = TRI-STATE (Outputs are disabled)

### **Absolute Maximum Ratings (Note)**

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V Input Voltage 5.5V

Operating Free Air Temperature

Range -55°C to +125°C Storage Temperature Range -65°C to +150°C Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

## **Recommended Operating Conditions**

Symbol	Parameter		Units		
		Min	Nom	Max	
V <sub>CC</sub>	Supply Voltage	4.5	5	5.5	٧
V <sub>IH</sub>	High Level Input Voltage	2			V
V <sub>IL</sub>	Low Level Input Voltage			0.8	V
loн	High Level Output Current			-2	mA
loL	Low Level Output Current			32	mA
TA	Free Air Operating Temperature	-55		125	°C

## Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max	Units
$V_{l}$	Input Clamp Voltage	V <sub>CC</sub> = Min, I <sub>I</sub> = -12 mA				-1.5	٧
V <sub>OH</sub>	High Level Output Voltage	$V_{CC} = Min, I_{OH} = Max$ $V_{IL} = Max, V_{IH} = Min$		2.4	3.1		٧
V <sub>OL</sub>	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IH} = Min, V_{IL} = Max$			0.2	0.4	<b>v</b>
l <sub>l</sub>	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$				1	mA
lıн	High Level Input Current	V <sub>CC</sub> = Max, V <sub>1</sub> = 2.4V				40	μΑ
l <sub>lL</sub> Low l	Low Level Input Current	V <sub>CC</sub> = Max V <sub>I</sub> = 0.5V (Note 4)	A			-40	
		V <sub>CC</sub> = Max V <sub>I</sub> = 0.4V (Note 5)	Α			-1.6	mA
		V <sub>CC</sub> = Max V <sub>I</sub> = 0.4V	G			-1.6	
lozh	Off-State Output Current with High Level Output Voltage Applied	$V_{CC} = Max, V_O = 2.4V$ $V_{IH} = Min, V_{IL} = Max$				40	μΑ
lozL	Off-State Output Current with Low Level Output Voltage Applied	$V_{CC} = Max, V_O = 0.4V$ $V_{IH} = Min, V_{IL} = Max$				-40	μА
los	Short Circuit Output Current	V <sub>CC</sub> = Max (Note 2)		-40		-115	mA
lcc	Supply Current	V <sub>CC</sub> = Max (Note 3)			59	85	mA

Note 1: All typicals are at  $V_{CC} = 5V$ ,  $T_A = 25$ °C.

Note 2: Not more than one output should be shorted at a time.

Note 3: I<sub>∞</sub> is measured with the data inputs grounded, and the output controls at 4.5V.

Note 4: Both G inputs are at 2V.

Note 5: Both G inputs are at 0.4V.

# **Switching Characteristics** at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$ (See Section 1 for Test Waveforms and Output Load)

Symbol	Parameter	$R_L = 400\Omega$				1
		C <sub>L</sub> = 5 pF		C <sub>L</sub> = 50 pF		Units
		Min	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay Time Low to High Level Output				16	ns
t <sub>PHL</sub>	Propagation Delay Time High to Low Level Output				22	ns
<sup>†</sup> PZH	Output Enable Time to High Level Output				35	ns
<sup>†</sup> PZL	Output Enable Time to Low Level Output				37	ns
<sup>†</sup> PHZ	Output Disable Time from High Level Output		11			ns
tpLZ	Output Disable Time from Low Level Output		27			ns