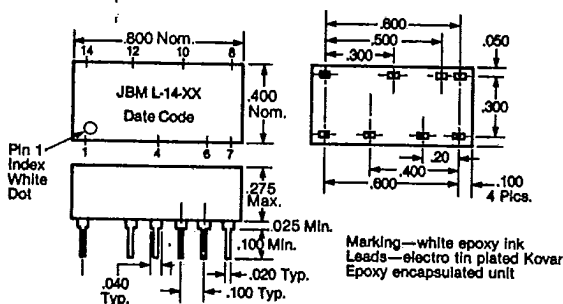
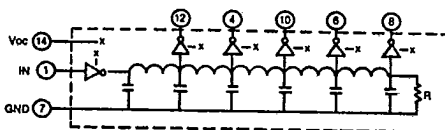
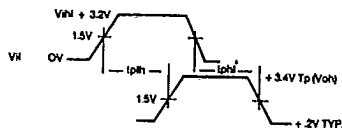
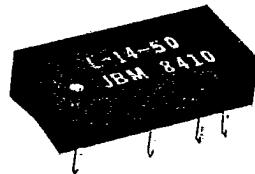


Active Delay Lines

T-47-13

NEW
Low Power/High Drive

Delay Range 25 thru 300 nsec
STYLE L-14 (14-pin DIP)
5 equally-spaced taps



FEATURES

- Fast rise time
- Low supply current
- State-of-the-art integrated circuit
- Improved noise margin
- High drive capability

Delay Line Characteristics

Input Characteristics:

Vih logic "1" input voltage	2.0V min.
Vll logic "0" input voltage	0.8V max.
Iih logic "1" input current	.20μA max.
Iil logic "0" input current	− 0.6mA max.

Output Characteristics:

Voh logic "1" output voltage	2.7V min.
Vol logic "0" output voltage	0.5V max.
Ioh logic "1" output current	− 1.0mA max.
Iol logic "0" output current	20mA max.

Test Conditions:

Ambient temperature	25°C
Supply voltage (Vcc)	5.0 ± .05Vdc
Input pulse amplitude	3.2V nom.
Input pulse width (tw)	2 x TD min.
Input pulse duty cycle	< 50%
Input pulse rise time	3 nsec (10% − 90%)
Output loads	500 ohms in parallel with 50pF

Environment:

Operating temperature	0°C to +70°C
Storage temperature	−55°C to +125°C
Operating supply voltage	4.75 to 5.25Vdc

DELAY SPECIFICATIONS (all times in nanoseconds)

JBM part #	Tap 1 Pin 12	Tap 2 Pin 4	Tap 3 Pin 10	Tap 4 Pin 6	Output Pin 8
L-14-050	lead 5 ± 3 trail 5 ± 3	10 ± 3 10 ± 3	15 ± 3 15 ± 3	20 ± 3 20 ± 3	25 ± 2 25 ± 3
L-14-051	lead 6 ± 3 trail 6 ± 3	12 ± 3 12 ± 3	18 ± 3 18 ± 3	24 ± 3 24 ± 3	30 ± 2 30 ± 3
L-14-052	lead 7 ± 3 trail 7 ± 3	14 ± 3 14 ± 3	21 ± 3 21 ± 3	28 ± 3 28 ± 3	35 ± 2 35 ± 3
L-14-053	lead 8 ± 3 trail 8 ± 3	16 ± 3 16 ± 3	24 ± 3 24 ± 3	32 ± 3 32 ± 3	40 ± 2 40 ± 4
L-14-054	lead 9 ± 3 trail 9 ± 3	18 ± 3 18 ± 3	27 ± 3 27 ± 3	36 ± 3 36 ± 3	45 ± 2.5 45 ± 4
L-14-055	lead 10 ± 3 trail 10 ± 3	20 ± 3 20 ± 3	30 ± 3 30 ± 3	40 ± 3 40 ± 4	50 ± 2.5 50 ± 4
L-14-056	lead 15 ± 3 trail 15 ± 3	30 ± 3 30 ± 3	45 ± 3 45 ± 4	60 ± 4 60 ± 5	75 ± 4 75 ± 5
L-14-057	lead 20 ± 3 trail 20 ± 3	40 ± 3 40 ± 4	60 ± 4 60 ± 5	80 ± 4 80 ± 5	100 ± 5 100 ± 6
L-14-058	lead 30 ± 3 trail 30 ± 3	60 ± 4 60 ± 5	90 ± 5 90 ± 6	120 ± 6 120 ± 9	150 ± 7.5 150 ± 10
L-14-059	lead 40 ± 3 trail 40 ± 4	80 ± 4 80 ± 5	120 ± 6 120 ± 8	160 ± 8 160 ± 10	200 ± 10 200 ± 12
L-14-060	lead 50 ± 4 trail 50 ± 5	100 ± 5 100 ± 6	150 ± 8 150 ± 10	200 ± 10 200 ± 12	250 ± 12.5 250 ± 15
L-14-061	lead 60 ± 4 trail 60 ± 5	120 ± 6 120 ± 8	180 ± 9 180 ± 11	240 ± 12 240 ± 15	300 ± 15 300 ± 18

Delays may change 2% or 1ns for a respective 5% increase or decrease in supply voltage.

Consult factory for other delays, tolerances, and logic families.