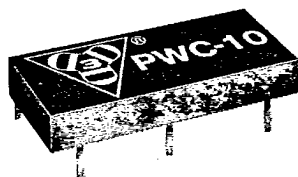


Pulse Width Controller

SERIES: PWC-10

**ECL Interfaced
16 pin DIP**

**data
delay
devices, inc.**



Features:

- Exact control of pulse width
- Rising edge trigger
- Auto-insertable
- ECL input & outputs
- High speed

Specifications:

- Input signal: ECL.
- Output fan-out: 70 ECL loads.
- Trigger inherent delay: 5 ns typ.
- Pulse-width tolerance: See table.
- Supply voltage: -5.2 Vdc.
- Supply current: 56 ma typ.
- Operating temperature: -30°C to $+85^{\circ}\text{C}$.
- Temperature coefficient: 100 PPM/ $^{\circ}\text{C}$.
- Output rise-time: 2 ns typ.

Part No.	Pulse Width (In ns)	Maximum Pulse Rate (In Mhz)
PWC-10-5	5 ± 1	98
PWC-10-6	6 ± 1	80
PWC-10-7	7 ± 1	70
PWC-10-8	8 ± 1	60
PWC-10-9	9 ± 1	54
PWC-10-10	10 ± 1	49
PWC-10-15	15 ± 1	32
PWC-10-20	20 ± 1	24
PWC-10-25	25 ± 1	19
PWC-10-30	30 ± 1	15
PWC-10-35	35 ± 1.5	13
PWC-10-40	40 ± 1.5	11
PWC-10-45	45 ± 1.5	10
PWC-10-50	50 ± 1.5	9
PWC-10-60	60 ± 1.5	8
PWC-10-70	70 ± 2	7
PWC-10-75	75 ± 2	6
PWC-10-80	80 ± 2	6
PWC-10-90	90 ± 3	5
PWC-10-100	100 ± 3	4

