

GENERAL DESCRIPTION

JRC

NJM2362 is the current mode flyback type, swiching supply voltage IC.

Totempole type output, which can drive MOSFET in high speed operation directly.

As to the functional protection, internalizing UVLO that can prevent from miss motion at low input voltage operation, pulse by pulse current limit for primary current of transformer, remote off circuit that can stop the entire motions by the external voltage, the over saturate protection circuit that stops the output by detecting the saturuted state of the transformer.

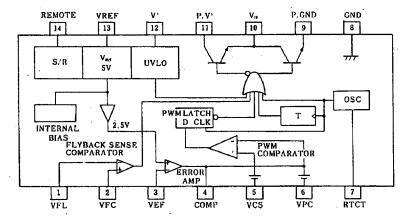
- FEATURES
- Operating Voltage (9V~20V)
- Latching PWM for Cycle-By-Cycle Current Limiting
- Under Voltage Lockout (UVLO)
- Infernal Remote Shut-Down Circuit
- Package Outline DIP14
- Bipolar Technology

BLOCK DIAGRAM & PIN CONFIGURATION.

PACKAGE OUTLINE



NJM2362D



NJM23620

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ABSOLUTE MAXIMUM RATI	ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	RATINGS	UNIT		
Supply Voltage	V*	20	v		
Power Dissipation	Po	700	mW		
Operating Temperature Range	Topr	-20~+75	C		
Storage Temperature Range	Tstg	-40~+125	C		

ELECTRICAL CHARACTERISTICS

(Ta=25℃, V*=15V)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Current	lcc	_	-	14.0	18.0	mA
Start up Current	⁻ I _{CS}	V+=6.5V	—	1.0	1.5	mA

REFERENCE VOLTAGE

SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
VR	I _{REF} =1mA	4.80	5.00	5.20	v
$\Delta V_R - V^+$	10≦V+≦20V	-	12.0	35.0	mV
$\Delta V_R - I_O$	$1 \leq I_0 \leq 20 \text{mA}$	· - ·	6.0	25.0	mν
TR	0≦T _a ≦75°C		0.5		mV/°C
Is	*1	50	80	120	mA
		$\begin{array}{c c} V_{R} & I_{REF} = 1 \text{ mA} \\ \Delta V_{R} - V^{+} & I0 \leq V^{+} \leq 20V \\ \Delta V_{R} - I_{O} & I \leq I_{O} \leq 20\text{ mA} \\ T_{R} & 0 \leq T_{a} \leq 75^{\circ}\text{C} \end{array}$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $

UVLO

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT .
Start Threshold Voltage	V _{STH}		7.80	8.40		v
Minimum Operating Voltage	V _{MIN}		7.00	7.60	8.20	v
Hysterisis width	V _{HIS}		—	0.8	—	v

OSILLATION

PARAMETER	SYMBOL	TEST CONDITION	MIN.	·TYP.	MAX.	UNIT
Osillating Frequency	Fo	Ct=3.3NF, Rt=10.4k Ω	45	50	55	kHz
Temperature Stability	∆Fo−Ta	0 \leq T _a \leq 75°C		5	—	%
Output Amplitude	V _F	7Pin Peak TO Peak		1.7	—	V

ERROR AMPLIFIER

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT.
Input Bias Current Open Loop Gain Gain Bandwidth	I _{EB} A _{EO} GB _E	*I 2≦V₀≤4V	_	0.3 90	2.0	uA dB MHz
Output Sink Current Output Source Current	I _{EK} I _{ES}	$V_{EF}=2.7V$, COMP=1.1V $V_{EF}=2.3V$, COMP=5.0V	2.0 0.5	6.0 0.8	_	mA mA

CURRENT DETECTOR

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Bias Current	I _{CB}	*]	_	2.0	10.0	μA
Gain	Ge	$V_{EF} = OV, *2$	2.85	3.00	3.15	v
Current Limit Voltage Input Capacitance Range	Vclr	$V_{PC} = 0.5V, R_S = 0.5\Omega, I_S = 5A$	0.42	0.50	0.58	v

FLYBACK COMPARATOR

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Bias Current Input Voltage Range	I _{FB} V _{FR}	*	0	0.3 —	2.0 V+-2	μΑ V

■ PWM

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Maximum Duty Cycle Minimum Duty Cycle	T _{PMAX} T _{PMIN}		-	48	0	% %

REMOTE

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Remote Off Threshold Remote Off Operating Supply Current	Vrth Irq	*3 V _{RTH} =2V			2.0 2.0	V mA

OUTPUT

SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
V _{OL} (1)	lsink=20mA	_	0.1	0.4	v
V _{OL} (2)	lsink=200mA		1.5	2.2	v
Vон(1)	Isource=20mA	13.0	13.5		v
V _{OH} (2)	lsource=200mA	12.0	13.5	_	v
tor	CL=1000PF		100		nS
tor	CL=1000PF	—	100	-	nS
	VoL(1) VoL(2) VoH(1) VoH(2) tor	$V_{OL}(1) Isink=20mA$ $V_{OL}(2) Isink=200mA$ $V_{OH}(1) Isource=20mA$ $V_{OH}(2) Isource=200mA$ $t_{or} CL=1000PF$	$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$	$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$

*1 Displaying by the direction, Coming from IC.

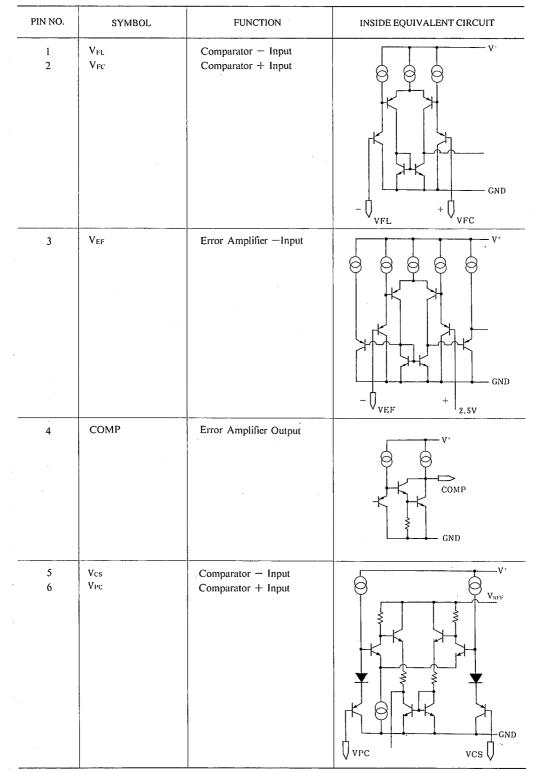
*2 Gc= $\Delta VCOMP/\Delta VCS$; OV < VCS < 0.8V

*3 Please apply the remote thershold Voltage under 20V, on normal operation.

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TEMINAL FUNCTION



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■ TEMINAL FUNCTION

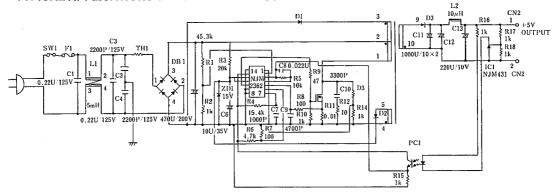
PIN NO.	SYMBOL	FUNCTION	INSIDE EQUIVALENT CIRCUIT
7	RTCT	Connect the resistor, and capacitor and decide the oscillating Frequency Fo=1.72/(RT * CT)	RTCT RTCT GND
8	GND	Ground	······································
9	P. GND	Ground Connect to pin 8 when operation	
10	VO	Output It is totempole type output, and it can drive MOS FET directly	V- V- Vo Vo GND
11	V+	Supply Voltage	
12	P. V+	Supply Voltage Please Connect to pin 11 when application	
13	VREF	5V Reference Voltage Output	V ⁺

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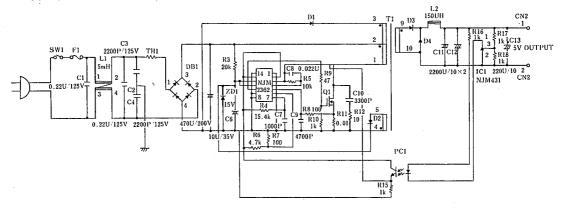
TEMINAL FUNCTION

IN NO.	SYMBOL	FUNCTION	INSIDE EQUIVALENT CIRCUIT
14	Remote	Remote	
			REMOTE
			GND

TYPICAL APPLICATIONS 1 FLYBACK FORM CONVERTER

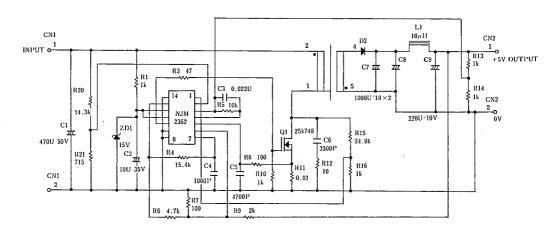


■ TYPICAL APPLICATIONS 2 FORWARD FORM CONVERTER



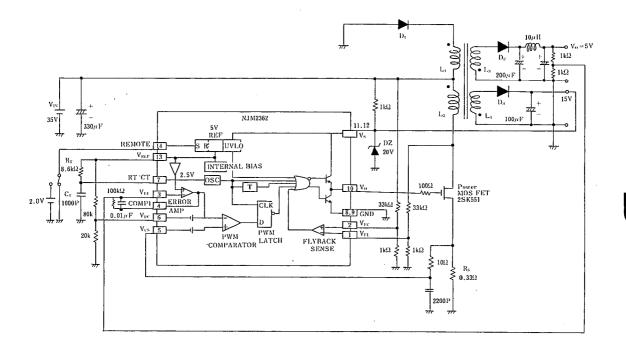
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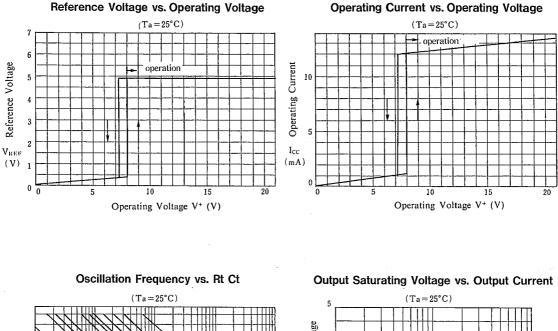


TYPICAL APPLICATIONS 3 DC/DC/ CONVERTER

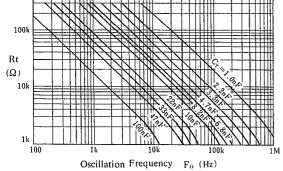
■ TYPICAL APPLICATIONS 4 2-OUTPUT TYPE DC/DC CONVERTER

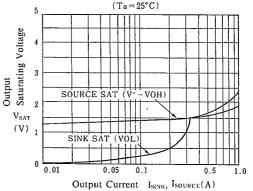


TYPICAL CHARACTERISTICS



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MEMO

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