

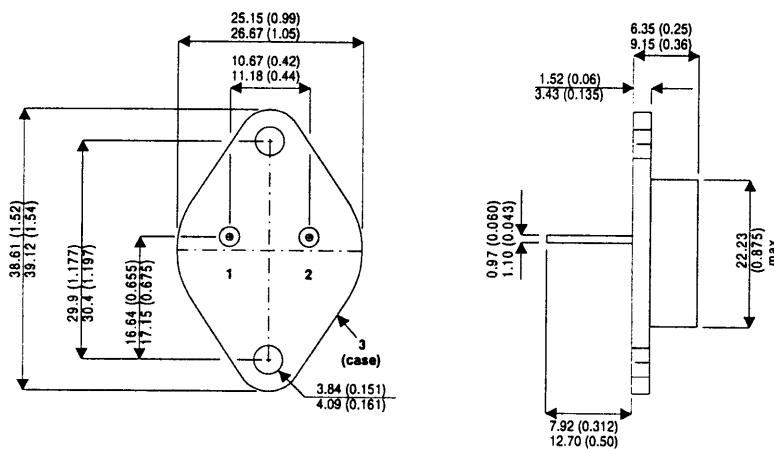


**SEME  
LAB**

664-091

BFD88

TO-3 (TO-204AA) Package Outline.  
Dimensions in mm (inches)



Pin 1 – Gate

Pin 2 – Source

Case – Drain

## 4TH GENERATION MOSFET

**N-CHANNEL  
ENHANCEMENT MODE  
HIGH VOLTAGE  
POWER MOSFETS**

**V<sub>DSS</sub>**      **400V**  
**I<sub>D</sub>(cont)**      **17.0A**  
**R<sub>DS(on)</sub>**      **0.30Ω**

### ABSOLUTE MAXIMUM RATINGS ( $T_{case} = 25^\circ\text{C}$ unless otherwise stated)

$V_{DSS}$	Drain – Source Voltage	400	V
$I_D$	Continuous Drain Current	17	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	68	A
$V_{GS}$	Gate – Source Voltage	$\pm 30$	V
$P_D$	Total Power Dissipation @ $T_{case} = 25^\circ\text{C}$	198	W
	Derate Linearly	1.584	$\text{W}/^\circ\text{C}$
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to 150	$^\circ\text{C}$
$T_L$	Lead Temperature : 0.063" from Case for 10 Sec.	300	

### STATIC ELECTRICAL RATINGS ( $T_{case} = 25^\circ\text{C}$ unless otherwise stated)

	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain – Source Breakdown Voltage	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	400			V
$I_{DSS}$	Zero Gate Voltage Drain Current ( $V_{GS} = 0\text{V}$ )	$V_{DS} = V_{DSS}$			250	$\mu\text{A}$
		$V_{DS} = 0.8V_{DSS}, T_C = 125^\circ\text{C}$			1000	
$I_{GSS}$	Gate – Source Leakage Current	$V_{GS} = \pm 30\text{V}, V_{DS} = 0\text{V}$			$\pm 100$	nA
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 1.0\text{mA}$	2		4	V
$I_{D(ON)}$	On State Drain Current <sup>2</sup>	$V_{DS} > I_{D(ON)} \times R_{DS(ON)} \text{ Max}$ $V_{GS} = 10\text{V}$	17			A
$R_{DS(ON)}$	Drain – Source On State Resistance <sup>2</sup>	$V_{GS} = 10\text{V}, I_D = 0.5 I_D [\text{Cont.}]$			0.30	$\Omega$

1) Repetitive Rating: Pulse Width limited by maximum junction temperature.

2) Pulse Test: Pulse Width < 380 $\mu\text{s}$ , Duty Cycle < 2%



# SEME LAB

BFD88

## DYNAMIC CHARACTERISTICS

	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
$C_{iss}$	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 25V$ $f = 1MHz$		1500	1800	pF
$C_{oss}$	Output Capacitance			385	540	
$C_{rss}$	Reverse Transfer Capacitance			160	240	
$Q_g$	Total Gate Charge <sup>3</sup>	$V_{GS} = 10V$ $V_{DD} = 0.5 V_{DSS}$ $I_D = I_D [Cont.] @ 25^\circ C$		71	105	nC
$Q_{gs}$	Gate – Source Charge			8	12	
$Q_{gd}$	Gate – Drain ("Miller") Charge			36	54	
$t_{d(on)}$	Turn-on Delay Time	$V_{GS} = 15V$ $V_{DD} = 0.5 V_{DSS}$ $I_D = I_D [Cont.] @ 25^\circ C$		13	25	ns
$t_r$	Rise Time			24	47	
$t_{d(off)}$	Turn-off Delay Time			50	75	
$t_f$	Fall Time	$R_G = 1.8\Omega$		20	52	

## SOURCE – DRAIN DIODE RATINGS AND CHARACTERISTICS

	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current	(Body Diode)			17	A
$I_{SM}$	Pulsed Source Current <sup>1</sup>				68	
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	$V_{GS} = 0V, I_S = -I_D [Cont.]$			1.3	V
$t_{rr}$	Reverse Recovery Time	$I_S = -I_D [Cont.], dI_S / dt = 100A/\mu s$	142	284	568	ns
$Q_{rr}$	Reverse Recovery Charge	$I_S = -I_D [Cont.], dI_S / dt = 100A/\mu s$	2.2	4.5	8	$\mu C$

## SAFE OPERATING AREA CHARACTERISTICS

	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
SOA1	Safe Operating Area	$V_{DS} = 0.4V_{DSS}, t = 1 Sec.$ $I_{DS} = P_D / 0.4V_{DSS}$	198			W
SOA2	Safe Operating Area	$V_{DS} = P_D / I_D [Cont.]$ $I_{DS} = I_D [Cont.], t = 1 Sec.$	198			W
$I_{LM}$	Inductive Current Clamped		68			A

## THERMAL CHARACTERISTICS

	Characteristic	Min.	Typ.	Max.	Unit
$R_{\theta JC}$	Junction to Case			0.63	$^{\circ}C/W$
$R_{\theta JA}$	Junction to Ambient			30	

1) Repetitive Rating: Pulse Width limited by maximum junction temperature.

2) Pulse Test: Pulse Width < 380 $\mu$ s, Duty Cycle < 2%

3) See MIL-STD-750 Method 3471



CAUTION — Electrostatic Sensitive Devices. Anti-Static Procedures Must Be Followed.