

SERIES 635 14 PIN TWO OUTPUTS H-CMOS DIGITAL DELAY MODULES

- 14 Pin DIP Auto-Insertable • H-Cmos Logic

Specifications:

- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Voltage : 3.2v
- Logic 0 Input Voltage : 0.9v
- Logic 1 Output Voltage : 4.5v
- Logic 0 Output Voltage : 0.1v
- Input Current : 1 μ A Max
- Supply Current : ICCH = 5mA, ICCL = 20 μ A
- Fan-Out : 10 LSTTL Loads Min
- Rise-Time : 8NS TYP. (measured from 0.8v to 2.0v)
- Operating Temp Range : 0°C to 70°C
- Temp Coefficient : 300 PPM/°C



Electrical Specifications at 25°C (Measured with no Loads on Taps)

Part Number	Total Delay Nanosecond (1)	Tap Delay NSEC (1)
635-10	10 $\pm$ 2	5
635-12	12 $\pm$ 2	6
635-16	16 $\pm$ 2	8
635-25	25 $\pm$ 2	12.5
635-30	30 $\pm$ 2	15
635-35	35 $\pm$ 2	17.5
635-40	40 $\pm$ 2	20
635-45	45 $\pm$ 2.2	22.5
635-50	50 $\pm$ 2.5	25
635-60	60 $\pm$ 3	30
635-75	75 $\pm$ 3.5	37.5
635-100	100 $\pm$ 5.0	50
635-125	125 $\pm$ 5.5	62.5
635-150	150 $\pm$ 6.0	75
635-175	175 $\pm$ 8.7	87.5
635-200	200 $\pm$ 10	100
635-250	250 $\pm$ 12.5	125

Note: (1) Measured at 1.5v level leading edge, Tot. $\pm$ 5% or $\pm$ 2NS, whichever is greater.

