

REVISIONS																								
LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED																					

  

REV	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
SHEET	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
REV	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
SHEET	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8

  

PMIC N/A  <b>STANDARDIZED MILITARY DRAWING</b>  THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE  AMSC N/A	PREPARED BY <i>Rick Offin</i> CHECKED BY <i>Charles E. Besore</i> APPROVED BY <i>[Signature]</i> DRAWING APPROVAL DATE 91-08-01 REVISION LEVEL	DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444  MICROCIRCUIT, LINEAR, LOW NOISE, 8TH ORDER, CLOCK SWEEPABLE CAUER LOW PASS FILTER, MONOLITHIC SILICON  <table style="width: 100%;"> <tr> <td style="width: 15%;">SIZE <b>A</b></td> <td style="width: 35%;">CAGE CODE <b>67268</b></td> <td style="width: 50%;">5962-90649</td> </tr> </table>	SIZE <b>A</b>	CAGE CODE <b>67268</b>	5962-90649
SIZE <b>A</b>	CAGE CODE <b>67268</b>	5962-90649			
		SHEET    1                      OF    8                      1			

DESC FORM 193  
SEP 87

• U.S. GOVERNMENT PRINTING OFFICE: 1987 — 748-129/60911

DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

5962-E085

## 1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part or Identifying Number (PIN). The complete PIN shall be as shown in the following example:

5962-90649	01	C	X
Drawing number	Device type (1.2.1)	Case outline (1.2.2)	Lead finish per MIL-M-38510

1.2.1 Device type(s). The device type(s) shall identify the circuit function as follows:

Device type	Generic number	Circuit function
01	LTC1064-4	Low noise, 8th order, clock sweepable, Cauer low pass filter

1.2.2 Case outline(s). The case outline(s) shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter	Case outline
C	D-1 (14-lead, .785" x .310" x .200"), dual-in-line package
2	C-2 (20-terminal, .358" x .358" x .100"), square chip carrier package

## 1.3 Absolute maximum ratings.

Total supply voltage ( $V_+$ to $V_-$ )	16.5 V dc
Input voltage at any pin ( $V_{IN}$ )	$V_- - 0.3 \text{ V} \leq V_{IN} \leq V_+ + 0.3 \text{ V}$
Power dissipation ( $P_D$ )	400 mW
Junction temperature	+175°C
Lead temperature (soldering, 10 seconds)	+300°C
Storage temperature	-65°C to +150°C
Thermal resistance, junction-to-case ( $\theta_{JA}$ )	See MIL-M-38510, appendix C
Thermal resistance, junction-to-ambient ( $\theta_{JA}$ ): Cases C and 2	90°C/W

## 1.4 Recommended operating conditions.

Ambient operating temperature range ( $T_A$ )	-55°C to +125°C
Supply voltage ( $V_S$ )	$\pm 7.5 \text{ V dc}$
Clock frequency ( $f_{CLK}$ )	1 MHz
Corner frequency ( $f_C$ )	20 kHz

<b>STANDARDIZED MILITARY DRAWING</b> DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE <b>A</b>	5962-90649
	REVISION LEVEL	SHEET 2

DESC FORM 193A  
SEP 87

U S GOVERNMENT PRINTING OFFICE 1988-350-547

## 2. APPLICABLE DOCUMENTS

2.1 Government specification, standard, and bulletin. Unless otherwise specified, the following specification, standard, and bulletin of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

### SPECIFICATION

#### MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

### STANDARD

#### MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

### BULLETIN

#### MILITARY

MIL-BUL-103 - List of Standardized Military Drawings (SMD's).

(Copies of the specification, standard, and bulletin required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

## 3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.2 herein.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 1.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full ambient operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

<b>STANDARDIZED MILITARY DRAWING</b> DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE <b>A</b>		5962-90649
		REVISION LEVEL	SHEET 3

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions <sup>1/</sup> -55°C ≤ T <sub>A</sub> ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Total power supply voltage range	V+ to V-		1,2,3	01	4.74	15.0	V
Output dc offset		V <sub>S</sub> = ±7.5 V, T <sub>A</sub> = +25°C	1	01	-160	+160	mV
Power supply current	I <sub>S</sub>	V <sub>S</sub> = ±2.37 V, f <sub>CLK</sub> = 1 MHz	1,2,3	01		22	mA
		V <sub>S</sub> = ±5 V, f <sub>CLK</sub> = 1 MHz	1	01		23	
			2,3			26	
		V <sub>S</sub> = ±7.5 V, f <sub>CLK</sub> = 1 MHz	1	01		28	
			2,3			32	
Input impedance	Z <sub>IN</sub>	T <sub>A</sub> = +25°C <sup>2/</sup>	4	01	9		kΩ
Output voltage swing and operating input voltage range		V <sub>S</sub> = ±5 V	4,5,6	01	-3.1	+3.1	V
		V <sub>S</sub> = ±7.5 V			-5.0	+5.0	
Passband gain		Referenced to 0 dB, 1Hz to 1 kHz	4,5,6	01	-0.5	0.1	dB
Gain at passband edge frequency		Referenced to passband gain	4,5,6	01	-0.4	0.7	dB
Passband ripple		Referenced to passband gain, 0.95 f <sub>C</sub>	4,5,6	01	0	0.75	dB
Stopband attenuation		At 1.7 f <sub>CUTOFF</sub>	4,5,6	01	-56		dB

<sup>1/</sup> Unless otherwise specified, V<sub>S</sub> = ±7.5 V, f<sub>CLK</sub> = 1 MHz, 50:1 (ratio at V+), f<sub>C</sub> = 20 kHz, and R<sub>L</sub> = 10 kΩ.

<sup>2/</sup> If not tested, shall be guaranteed to the limits specified in table I herein.

<b>STANDARDIZED MILITARY DRAWING</b> DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE <b>A</b>		5962-90649
		REVISION LEVEL	SHEET 4

DESC FORM 193A  
SEP 87

U S GOVERNMENT PRINTING OFFICE: 1988-550-547

Device type	01	
Case outlines	C	2
Terminal number	Terminal symbol	
1	INV C	INV C
2	V <sub>IN</sub>	NC
3	AGND	V <sub>IN</sub>
4	V+	AGND
5	AGND	V+
6	COMP1	AGND
7	INV A	NC
8	NC	COMP1
9	V <sub>OUT</sub>	NC
10	RATIO	INV A
11	f <sub>CLK</sub>	NC
12	V <sub>CLK</sub>	V <sub>OUT</sub>
13	COMP2	NC
14	R (H, I)	RATIO
15	---	f <sub>CLK</sub>
16	---	V <sub>CLK</sub>
17	---	NC
18	---	COMP2
19	---	NC
20	---	R (H, I)

NC = No connection

R (H, I) = High rejection band output

FIGURE 1. Terminal connections.

<b>STANDARDIZED MILITARY DRAWING</b> DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE <b>A</b>		5962-90649
		REVISION LEVEL	SHEET 5

DESC FORM 193A  
SEP 87

U S GOVERNMENT PRINTING OFFICE: 1988-880-547

3.5 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked as listed in MIL-BUL-103 (see 6.6 herein).

3.6 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-BUL-103 (see 6.6 herein). The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

3.7 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.9 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

#### 4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition C using the circuit submitted with the certificate of compliance (see 3.6 herein).

(2)  $T_A = +125^{\circ}\text{C}$ , minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

##### 4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 7, 8, 9, 10, and 11 in table I, method 5005 of MIL-STD-883 shall be omitted.

<b>STANDARDIZED MILITARY DRAWING</b> DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	<b>SIZE</b> <b>A</b>		5962-90649
		<b>REVISION LEVEL</b>	<b>SHEET</b> 6

DESC FORM 193A  
SEP 87

• U. S. GOVERNMENT PRINTING OFFICE: 1985-880-547

TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	---
Final electrical test parameters (method 5004)	1*,2,3,4*,5,6
Group A test requirements (method 5005)	1,2,3,4,5,6
Groups C and D end-point electrical parameters (method 5005)	1,4

\* PDA applies to subgroups 1 and 4.

4.3.2 Groups C and D inspections.

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test conditions, method 1005 of MIL-STD-883.
  - (1) Test condition C using the circuit submitted with the certificate of compliance (see 3.6 herein).
  - (2)  $T_A = +125^{\circ}\text{C}$ , minimum.
  - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

<b>STANDARDIZED MILITARY DRAWING</b> DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE <b>A</b>		5962-90649
		REVISION LEVEL	SHEET 7

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-481 using DD Form 1693, Engineering Change Proposal (Short Form).

6.4 Record of users. Military and industrial users shall inform Defense Electronics Supply Center when a system application requires configuration control and the applicable SMD. DESC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DESC-ECS, telephone (513) 296-6022.

6.5 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone (513) 296-5375.

6.6 Approved sources of supply. Approved sources of supply are listed in MIL-BUL-103. The vendors listed in MIL-BUL-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DESC-ECS.

<b>STANDARDIZED MILITARY DRAWING</b> DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	<b>SIZE</b> <b>A</b>		5962-90649
		<b>REVISION LEVEL</b>	<b>SHEET</b> 8

DESC FORM 193A  
SEP 87

U S GOVERNMENT PRINTING OFFICE: 1985-550-547

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