TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

# 2SK3051

# Chopper Regulator DC-DC Converter, and Motor Drive Applications

• Low drain-source ON resistance :  $R_{DS (ON)} = 24 \text{ m}\Omega \text{ (typ.)}$ 

• High forward transfer admittance : |Y<sub>fs</sub>| = 27 S (typ.)

Low leakage current : I<sub>DSS</sub> = 100 μA (max) (V<sub>DS</sub> = 50 V)

• Enhancement mode :  $V_{th} = 1.5 \text{ to } 3.0 \text{ V } (V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA})$ 

### **Absolute Maximum Ratings (Ta = 25°C)**

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	50	V	
Drain-gate voltage (Ro	<sub>SS</sub> = 20 kΩ)	$V_{DGR}$	50	V	
Gate-source voltage		$V_{GSS}$	±20	V	
Drain current	DC (Note 1)	I <sub>D</sub>	45	Α	
	Pulse (Note 1)	I <sub>DP</sub>	135	Α	
Drain power dissipation	n (Tc = 25°C)	P <sub>D</sub>	40	W	
Single pulse avalanche	e energy (Note 2)	E <sub>AS</sub>	115	mJ	
Avalanche current		I <sub>AR</sub>	45	Α	
Repetitive avalanche e	nergy (Note 3)	E <sub>AR</sub>	4	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature ra	ange	T <sub>stg</sub>	-55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	3.125	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	83.3	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

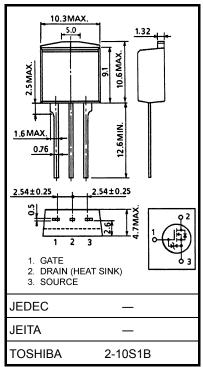
Note 2:  $V_{DD}$  = 25 V,  $T_{ch}$  = 25°C (initial), L = 71  $\mu$ H,  $R_{G}$  = 25  $\Omega$ ,  $I_{AR}$  = 45 A

Note 3: Repetitive rating;: pulse width limited by maximum channel temperature.

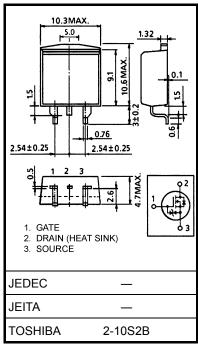
This transistor is an electrostatic-sensitive device.

Please handle with caution.

Unit: mm



Weight: 1.5 g (typ.)



Weight: 1.5 g (typ.)

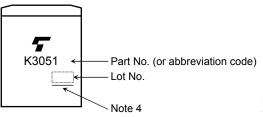
# **Electrical Characteristics (Ta = 25°C)**

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	50	_	_	V
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.5	_	3.0	V
Drain-source O	N resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 25 A	_	24	30	mΩ
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 25 A	15	27	_	S
Input capacitano	ce	C <sub>iss</sub>		_	1250	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	250	_	pF
Output capacitance		Coss			700	_	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{\text{oV}} \stackrel{I_D=25\text{A}}{\text{oV}} \stackrel{\text{OUT}}{\text{OUT}} \stackrel{\text{RL}=1\Omega}{\text{V}} \stackrel{\text{RL}=25\text{V}}{\text{OUT}}$	_	20	_	
	Turn-on time	ton		_	30	_	ns
	Fall time	t <sub>f</sub>		_	40	_	115
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_W = 10 \mu s$	_	120	_	
Total gate charge (Gate-source plus gate-drain)		Qg		_	36	_	nC
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 40 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 45 \text{ A}$	_	22	_	
Gate-drain ("miller") charge		Q <sub>gd</sub>			14	_	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	45	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	135	Α
Forward voltage (diode)	$V_{DSF}$	I <sub>DR</sub> = 45 A, V <sub>GS</sub> = 0 V	1	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 45 A, V <sub>GS</sub> = 0 V	1	75	_	ns
Reverse recovery charge	$Q_{rr}$	dI <sub>DR</sub> / dt = 50 A / μs	_	75	_	nC

## Marking



Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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