



DPLS4140E

LOW $V_{\text{CE(SAT)}}$ PNP SURFACE MOUNT TRANSISTOR

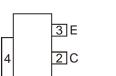
Features

- Epitaxial Planar Die Construction
- Ideally Suited for Automated Assembly Processes
- Ideal for Medium Power Switching or Amplification Applications
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

Mechanical Data

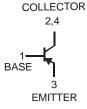
- Case: SOT-223
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Matte Tin annealed over Copper Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.115 grams (approximate)





<u>1</u>]B

TOP VIEW



Schematic and Pin Configuration

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	-180	V
Collector-Emitter Voltage	V _{CEO}	-140	V
Emitter-Base Voltage	V_{EBO}	-7	V
Continuous Collector Current	Ic	-4	A
Peak Pulse Current	I _{CM}	-10	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @ T _A = 25°C	P_{D}	1	W
Thermal Resistance, Junction to Ambient Air (Note 3) @ T _A = 25°C	$R_{ heta JA}$	125	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes:

- No purposefully added lead.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
- 3. Device mounted on FR-4 PCB; pad layout as shown on page 4 or in Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.



Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)						
Collector-Base Breakdown Voltage	V _{(BR)CBO}	-180	-230	_	V	$I_C = -100 \mu A, I_E = 0$
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	-140	-190	_	V	$I_C = -10 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	-7	-8.5	_	V	$I_E = -100 \mu A, I_C = 0$
Collector Cutoff Current	I _{CBO}	_		-20 -0.5	nA μA	$V_{CB} = -150V, I_{E} = 0$ $V_{CB} = -150V, I_{E} = 0,$ $T_{A} = 100^{\circ}C$
Emitter Cutoff Current	I _{EBO}	_		-10	nA	$V_{EB} = -6V, I_{C} = 0$
ON CHARACTERISTICS (Note 4)						
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	_ _ _	-40 -50 -75 -175	-60 -80 -120 -360	mV	I _C = -0.1A, I _B = -5mA I _C = -0.5A, I _B = -50mA I _C = -1A, I _B = -100mA I _C = -3A, I _B = -300mA
Base-Emitter Saturation Voltage	V _{BE(SAT)}	_	-910	-1040	mV	$I_C = -3A$, $I_B = -300mA$
Base-Emitter Turn-On Voltage	V _{BE(ON)}	_	-810	-930	mV	I _C = -3A, V _{CE} = -5V
DC Current Gain	h _{FE}	100 100 45	 12	300 — —	_	I _C = -10mA, V _{CE} = -5V I _C = -1A, V _{CE} = -5V I _C = -3A, V _{CE} = -5V I _C = -10A, V _{CE} = -5V
SMALL SIGNAL CHARACTERISTICS						
Current Gain-Bandwidth Product	f _T	_	150	_	MHz	$I_C = -100 \text{mA}, V_{CE} = -10 \text{V},$ f = 100 MHz
Output Capacitance	C _{obo}		55		pF	V _{CB} = -10V, f = 1MHz
SWITCHING CHARACTERISTICS						
Switching Times	t _{on} t _{off}		85 430		ns	I _C = -1A, I _{B1} = -100mA I _{B2} = 100mA, V _{CC} = -50V

4. Measured under pulsed conditions. Pulse width = $300\mu s$. Duty cycle $\leq 2\%$

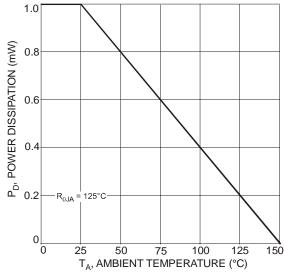


Fig. 1 Max Power Dissipation vs. Ambient Temperature

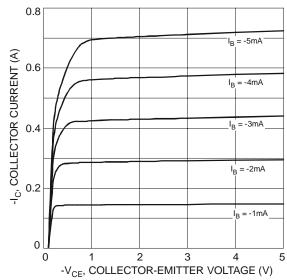


Fig. 2 Typical Collector Current vs. Collector-Emitter Voltage



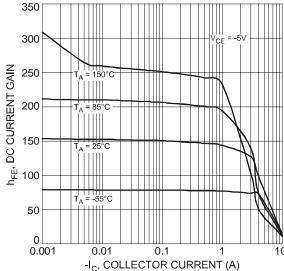


Fig. 3 Typical DC Current Gain vs. Collector Current

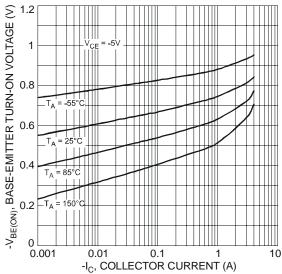


Fig. 5 Typical Base-Emitter Turn-On Voltage vs. Collector Current

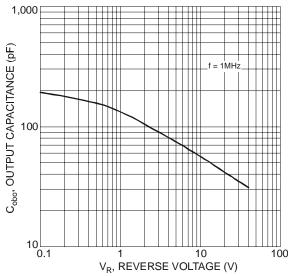


Fig. 7 Typical Output Capacitance Characteristics

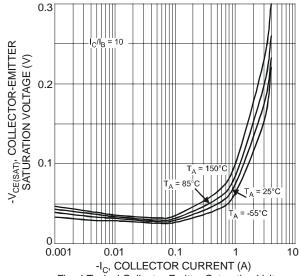


Fig. 4 Typical Collector-Emitter Saturation Voltage vs. Collector Current

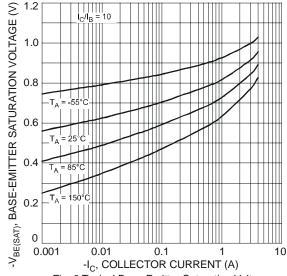


Fig. 6 Typical Base-Emitter Saturation Voltage vs. Collector Current

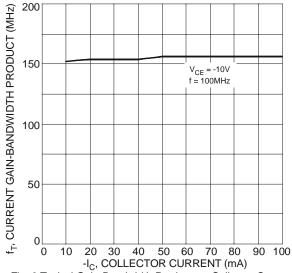


Fig. 8 Typical Gain-Bandwidth Product vs. Collector Current

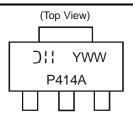


Ordering Information (Note 5)

Device	Packaging	Shipping	
DPLS4140E -13	SOT-223	2500/Tape & Reel	

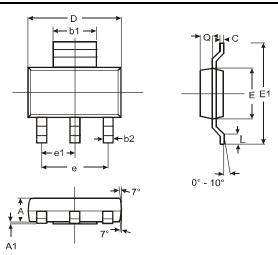
5. For packaging details, go to our website at http://www.diodes.com/ap02007.pdf.

Marking Information



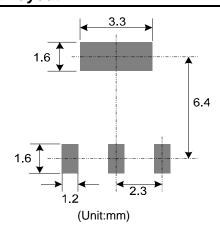
P414A = Product Type Marking Code YWW = Date Code Marking Y = Last digit of year ex: 7 = 2007 WW = Week code 01 - 52

Package Outline Dimensions



SOT-223					
Dim	Min	Max	Тур		
Α	1.55	1.65	1.60		
A 1	0.010	0.15	0.05		
b1	2.90	3.10	3.00		
b2	0.60	0.80	0.70		
С	0.20	0.30	0.25		
D	6.45	6.55	6.50		
Е	3.45	3.55	3.50		
E1	6.90	7.10	7.00		
е	_	_	4.60		
e1			2.30		
L	0.85	1.05	0.95		
Q	0.84	0.94	0.89		
All Dimensions in mm					

Suggested Pad Layout:



IMPORTANT NOTICE

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to any product herein. Diodes Incorporated does not assume any liability arising out of the application or use of any product described herein; neither does it convey any license under its patent rights, nor the rights of others. The user of products in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on our website, harmless against all damages.

LIFE SUPPORT

Diodes Incorporated products are not authorized for use as critical components in life support devices or systems without the expressed written approval of the President of Diodes Incorporated.