GSCLP2951

100mA Low-Dropout Voltage Regulator

Description

The GSCLP2951 is a low power voltage regulator. This device excellent choice for use in battery powered application such as cordless telephone, radio control systems, and portable computers. The device features very low quiescent current (75uA Typ.) and very low drop output voltage (Typ. 40mV at light load and 380mV at 100mA). This includes a tight initial tolerance of 0.5% typ., extremely good load and line regulation of 0.05% typ., and very low output temperature coefficient, marking the part useful as a low-power voltage reference. The error flag output feature is used as power-on reset for warns of a low output voltage, due to following batteries on input. Other feature is the logic-compatible shutdown input which enable the regulator to be switched on and off. The regulator output voltage may be pin-strapped for fixed output voltage or programmed from 1.24V to 29V with external pair of resistors.

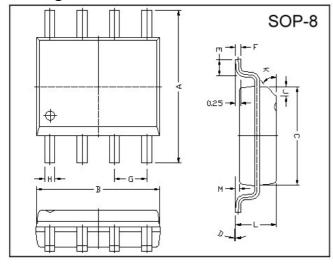
Features

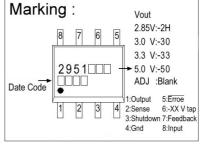
- · High accuracy output voltage
- · Extremely low quiescent current and dropout voltage
- Extremely tight load and line regulation
- · Current and thermal Limiting
- Very low temperature coefficient
- · Error flag warns of output dropout
- · Logic-Controlled electronic shutdown

Applications

- Battery powered equipment
- Cellular Phones
- Portable/Palm top/Notebook computers

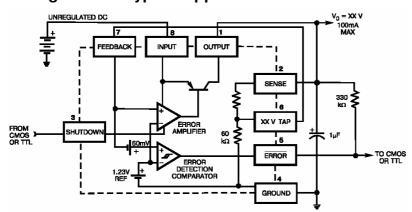
Package Dimensions





REF.	Millimeter		REF.	Millimeter		
	Min.	Max.	NEF.	Min.	Max.	
Α	5.80	6.20	М	0.10	0.25	
В	4.80	5.00	Н	0.35	0.49	
С	3.80	4.00	L	1.35	1.75	
D	0°	8°	J	0.375 REF.		
Е	0.40	0.90	K	45°		
F	0.19	0.25	G	1.27 TYP.		

Block Diagram and Typical Applications



GSCLP2950 Page: 1/4 **Absolute Maximum Ratings**

Parameter	Symbol	Ratings	Unit	
Input Voltage	Vcc	-0.3 ~ +30	V	
Output Current	Іоит	100	mA	
Feedback Input Voltage	VFB	-1.5 ~ +30	V	
Shutdown Input Voltage	VsD	-0.3 ~ +30	V	
Error Comparator Output	Vco	-0.3 ~ +30	V	
Storage Temperature	Tstg	-65 ~ +150	°C	
Maximum Junction Temperature	Tjmax	150	$^{\circ}\!\mathbb{C}$	
Operating Junction Temperature	Tj	-55 ~ +150	°C	

Electrical Characteristics (Ta=25°C, VIN=15V, unless otherwise specified) (Note2)

Parameter	Condition		Min	TYP	Max	Unit	
Output Voltage	GLP2950-2H		2.793	2.85	2.907		
	GLP2950-30	100µA≤Io≤100mA	2.940	3.00	3.060		
	GLP2950-33	TJ≤TJMAX	3.234	3.30	3.366	V	
	GLP2950-50		4.900	5.00	5.100		
Line Regulation (Note3)	Vo+1≤ViN≤30V (Note4)		-	0.04	0.4	%	
Load Regulation (Note3)	100µA≤Io≤100mA		-	0.1	0.3	%	
Current Limit	Vout=0		-	160	200	mA	
Output Voltage Temperature Coefficient	(Note1)		-	50	150	ppm/°C	
Dropout Voltago (News)	Io=100μA		-	50	80	mV	
Dropout Voltage (Note5)	Io=100mA		-	380	450		
Ground Current	Ιο=100μΑ		-	75	120	μΑ	
Ground Current	Io=100mA		-	8	12	mA	
Dropout Ground Current	V _{IN} =V _O -0.5V, I _O =100μA		-	110	170	μΑ	
Thermal Regulator	·		-	0.05	0.2	%/W	
Output Voltage Noise	C _L =1µF		-	430	-	μVrms	
f=10Hz~100kHz	CL=200µF	-	160	-			
I = 101 IZ * 100KI IZ	CL=3.3µF (Bypass=0.01µF pins 7 to 1)		-	100	-		
Reference Voltage			1.210	1.235	1.260	V	
Reference Voltage Over Temperature(Not		ture(Note6)	1.185		1.285	V	
Feedback pin Bias Current			-	20	40	nA	
Reference Voltage Temperature Coefficient	Note1)		-	50	-	ppm/°C	
Feedback pin Bias Current Temperature Coefficient			-	0.1	-	n A /℃	
Error Comparator							
Output Leakage Current	Voh=30V		-	0.01	1.0	μΑ	
Output Low Voltage	VIN=4.5V, IOL=400µA		-	150	250	mV	
Upper Threshold Voltage	(Note7)		40	60	-	mV	
Low Threshold Voltage	(Note7)		-	75	95	mV	
Hysteresis	(Note7)		-	15	-	mV	
Shutdown Input							
Input Logic Voltage	, ,		1.3	0.7	V		
Input Logic Voltage	High (Regulat	or OFF)	2			٧	
Shutdown pin Input Current	Vs=2.4V			30	50	μA	
Shatdown pin input ourient	Vs=30V		-	450	600	i00 PA	
Dogulator Output Compant in Chartele	Vout=5.0V		-	3	10	0 μΑ	
Regulator Output Current in Shutdown (Note8)	3.3V≤Vouт<5.		-	-	20		
(2.0V≤Vouт<3.30V		-	-	30		

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- Note 1: Output or reference voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature range.
- Note 2: Unless otherwise specified all limits guaranteed for Tj=25°C, Vin =Vo+1V, IL=100uA and CL=1uF. Additional conditions are feedback tied to GSCLP2951-XX tap and output tied to Sense (Vout=XX V) and Vshutdown≤0.8V.
- Note 3: Regulations is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specification for thermal regulation.
- Note 4: Line regulation for GSCLP2951-XX is tested at 150° C for IL=1mA. For IL=100uA and Tj=125 $^{\circ}$ C, line regulation is guaranteed by design to 0.2%. See typical performance characteristics for line regulation versus temperature and load current.
- Note 5: Dropout voltage is defined as the input to output differential at which the output voltage drops 100mV below its nominal value measured at 1V differential. At very low values of programmed output voltage, the minimum input supply voltage of 2V (2.3V over temperature) must be taken into account.
- Note 6: Vref \leq Vout \leq (Vin-1V), 2.3V \leq Vin \leq 30V, 100uA \leq IL \leq 100mA, Tj \leq Tjmax.
- Note 7: Comparator thresholds are expressed in terms of a voltage differential at the feedback terminal below the nominal reference voltage measured at Vo + 1V input. To express these thresholds in terms of output voltage change, multiply by the error amplifier gain = Vout/Vref = (R1+R2)/R2. For example, at a programmed output voltage of 5V, the error output is guaranteed to go low when the output drops by 95mV x 5V/1.235V=384mV. Thresholds remain constant as a percent of Vout as Vout is varied, with the dropout warning occurring at typical 5% below nominal, 7.5% guaranteed.
- Note 8: Vshutdown ≥ 2V, Vin ≤ 30V, Vout=0, Feed-back pin tied to -XX V Tap.

Characteristics Curve

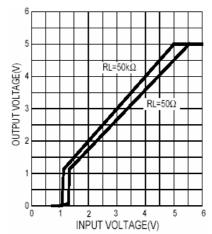


Fig 1. Dropout Characteristics

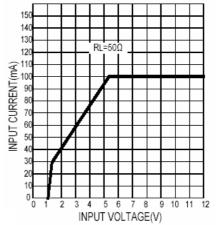


Fig 2. Input Current

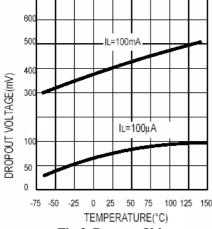


Fig 3. Dropout Voltage

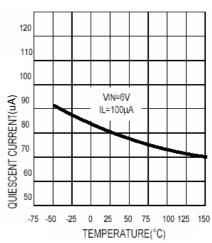


Fig 4. Ground Pin Current

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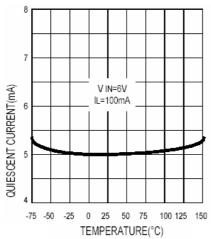


Fig 5. Ground Pin Current

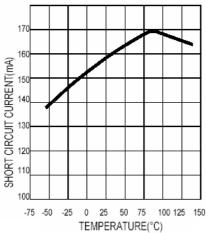


Fig 7. Short Circuit Current

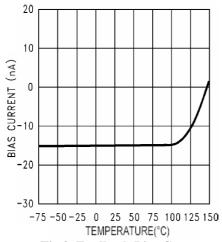


Fig 9. Feedback Bias Current

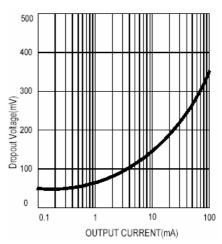


Fig 6. Dropout Voltage

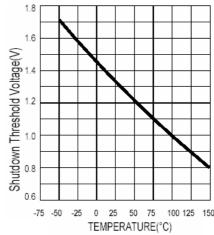


Fig 8. Shutdown Threshold Voltage

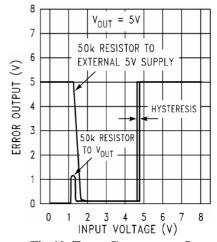


Fig 10. Error Comparator Output

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