

September 2010 SupreMOSTM

FCB36N60N

N-Channel MOSFET 600V, 36A, $90m\Omega$

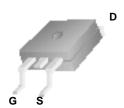
Features

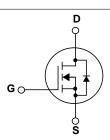
- $R_{DS(on)} = 81 \text{m}\Omega$ (Typ.)@ $V_{GS} = 10 \text{V}$, $I_D = 18 \text{A}$
- Ultra low gate charge (Typ. Qg = 86nC)
- · Low effective output capacitance
- 100% avalanche tested
- · RoHS compliant

Description

The SupreMOS MOSFET, Fairchild's next generation of high voltage super-junction MOSFETs, employs a deep trench filling process that differentiates it from preceding multi-epi based technologies. By utilizing this advanced technology and precise process control, SupreMOS provides world class Rsp, superior switching performance and ruggedness.

This SupreMOS MOSFET fits the industry's AC-DC SMPS requirements for PFC, server/telecom power, FPD TV power, ATX power, and industrial power applications.





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted*

Symbol		Parameter		FCB36N60N	Units
V _{DSS}	Drain to Source Voltage			600	V
V _{GSS}	Gate to Source Voltage			±30	V
	Dunin Comment	-Continuous (T _C = 25°C)		36	
ID	Drain Current	-Continuous (T _C = 100°C)		22.7	A
I _{DM}	Drain Current	- Pulsed	(Note 1)	108	Α
E _{AS}	Single Pulsed Avalanche Energy (Note 2)			1800	mJ
I _{AR}	Avalanche Current			12	Α
E _{AR}	Repetitive Avalanche Energy			3.12	mJ
-l/-l4	MOSFET dv/dt Ruggedness			100	V/ns
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	20	V/ns
D	Dawar Dissipation	$(T_C = 25^{\circ}C)$		312	W
P_{D}	Power Dissipation	- Derate above 25°C		2.6	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C	
T _L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds			300	°C

^{*}Drain current limited by maximum junction temperature

Thermal Characteristics

Symbol	Parameter	FCB36N60N	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.4	
R _{0JA} *	Thermal Resistance, Junction to Ambient *	40	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient 62.5		

^{*}When mounted on the minmium pad size recommended (PCB Mount)

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FCB36N60N	FCB36N60N	D ² -PAK	330mm	24mm	800

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter Test Conditions		Min.	Тур.	Max.	Units
Off Charac	eteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 1 \text{mA}, V_{GS} = 0 \text{V}, T_C = 25^{\circ} \text{C}$	600	-	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I _D = 1mA, Referenced to 25°C	-	0.7	-	V/°C
1	Zero Gate Voltage Drain Current	$V_{DS} = 480V, V_{GS} = 0V$	-	-	10	
IDSS	Zero Gate Voltage Drain Current	$V_{DS} = 480V, V_{GS} = 0V, T_{C} = 125^{\circ}C$	-	-	100	μΑ
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 30V, V_{DS} = 0V$	-	-	±100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250 \mu A$	2.0	-	4.0	V
R _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = 10V, I_D = 18A$	-	81	90	mΩ
9 _{FS}	Forward Transconductance	$V_{DS} = 40V, I_{D} = 18A$	-	41	-	S

Dynamic Characteristics

C _{iss}	Input Capacitance	V _{DS} = 100V, V _{GS} = 0V f = 1MHz		3595	4785	pF
C _{oss}	Output Capacitance			149	200	pF
C _{rss}	Reverse Transfer Capacitance			4	6	pF
C _{oss}	Output Capacitance	$V_{DS} = 380V, V_{GS} = 0V, f = 1MHz$	-	80	-	pF
C _{oss} eff.	Effective Output Capacitance	$V_{DS} = 0V \text{ to } 380V, V_{GS} = 0V$		361	-	pF
Q _{g(tot)}	Total Gate Charge at 10V		-	86	112	nC
Q _{gs}	Gate to Source Gate Charge	$V_{DS} = 380V, I_{D} = 18A,$	-	15.4	-	nC
Q_{gd}	Gate to Drain "Miller" Charge	$V_{GS} = 10V$ (Note 4)	-	26.4	-	nC
ESR	Equivalent Series Resistance (G-S)	Drain Open	-	1	-	Ω

Switching Characteristics

t _{d(on)}	Turn-On Delay Time		-	23	56	ns
t _r	Turn-On Rise Time	$V_{DD} = 380V, I_D = 18A$	-	22	54	ns
t _{d(off)}	Turn-Off Delay Time	$R_G = 4.7\Omega$	-	94	198	ns
t _f	Turn-Off Fall Time	(Note 4)	-	4	18	ns

Drain-Source Diode Characteristics

Is	Maximum Continuous Drain to Source Diode Forward Current		-	-	36	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	108	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _{SD} = 18A	-	-	1.2	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _{SD} = 18A	-	574	-	ns
Q _{rr}	Reverse Recovery Charge	dI _F /dt = 100A/μs		10	-	μС

Notes

- Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. I_{AS} = 12A, R_G = 25 Ω , Starting T_J = 25 $^{\circ}$ C
- 3. I $_{SD} \leq$ 36A, di/dt \leq 200A/ μ s, V $_{DD}$ = 380V, Starting T $_{J}$ = 25°C
- 4. Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

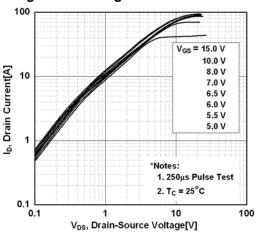


Figure 3. On-Resistance Variation vs.
Drain Current and Gate Voltage

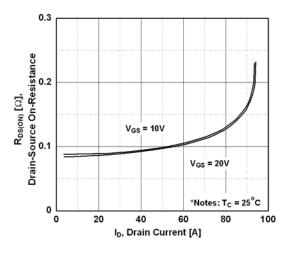


Figure 5. Capacitance Characteristics

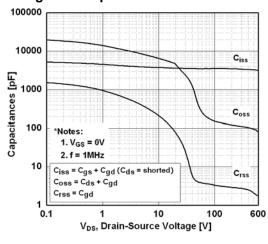


Figure 2. Transfer Characteristics

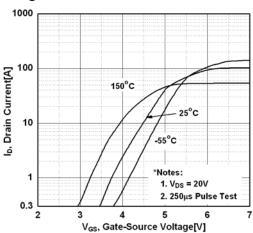


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

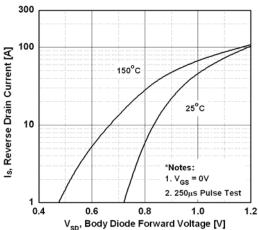
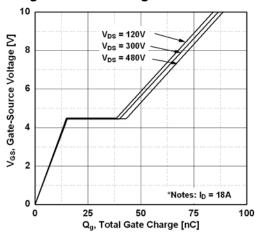


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

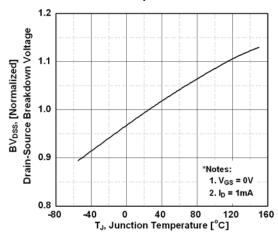


Figure 8. On-Resistance Variation vs. Temperature

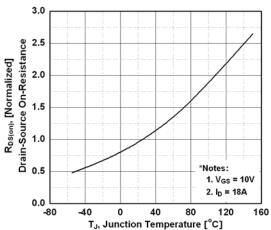


Figure 9. Maximum Safe Operating Area

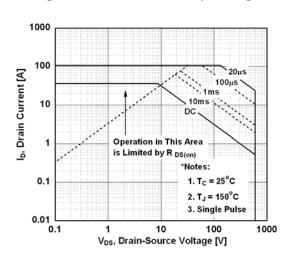


Figure 10. Maximum Drain Current vs. Case Temperature

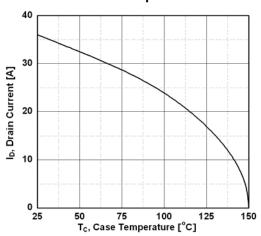
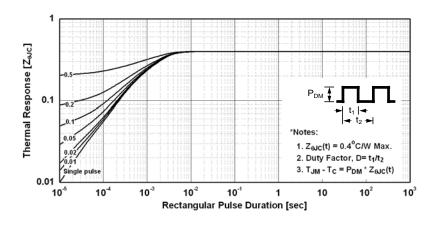
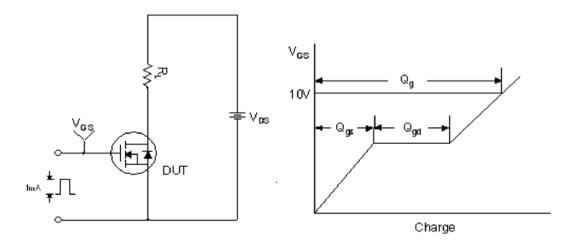


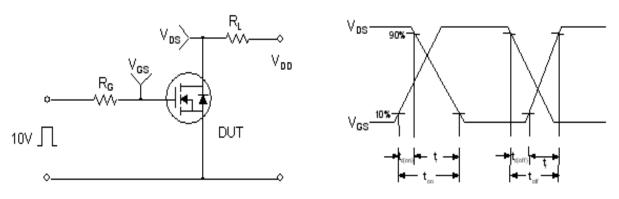
Figure 11. Transient Thermal Response Curve



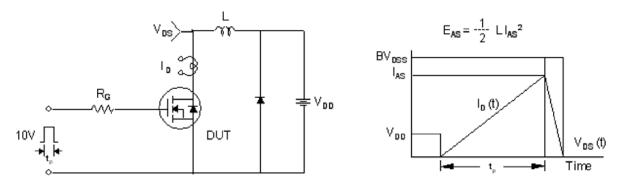
Gate Charge Test Circuit & Waveform



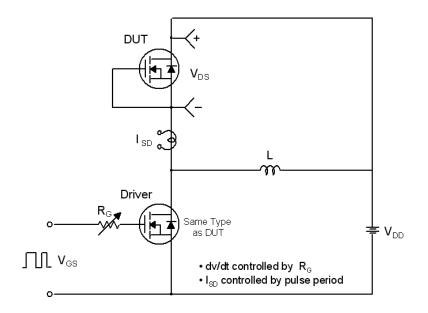
Resistive Switching Test Circuit & Waveforms

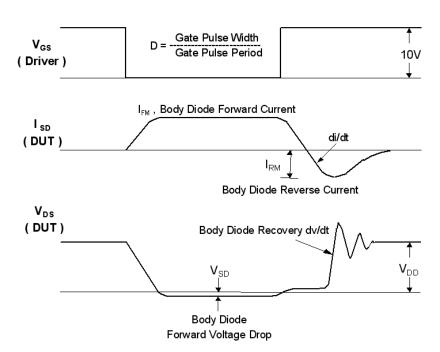


Unclamped Inductive Switching Test Circuit & Waveforms



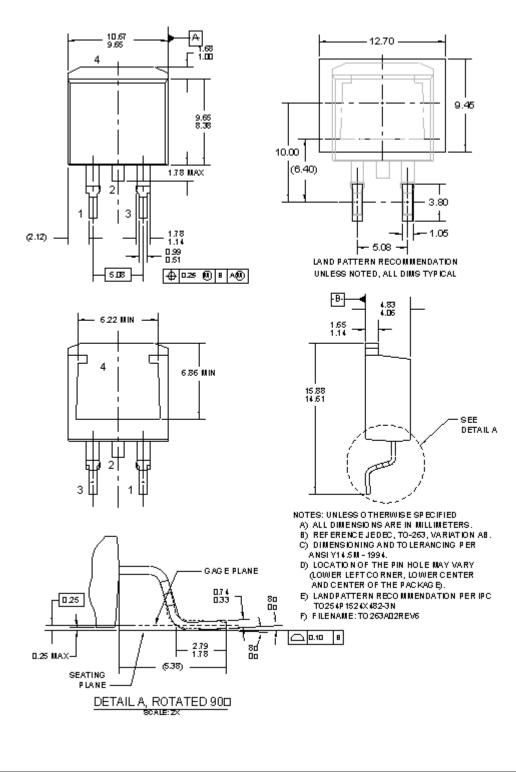
Peak Diode Recovery dv/dt Test Circuit & Waveforms





Mechanical Dimensions

D²PAK





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SYSTEM 9*

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