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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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semiconductors may lead to personal injury, fire or property damage.
 Remember to give due consideration to safety when making your circuit designs, with appropriate
measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or
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2SK3212

Silicon N Channel MOS FET High Speed Power Switching

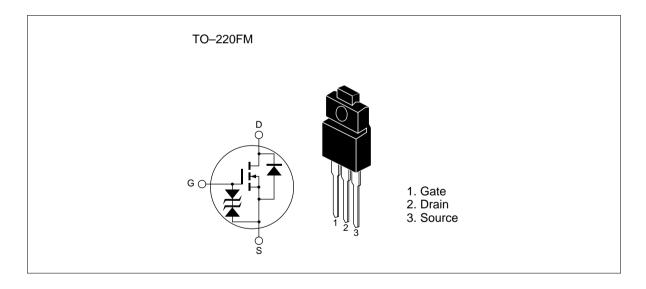


ADE-208-752A (Z) 2nd. Edition Mar. 2001

Features

- Low on-resistance $R_{DS} = 0.1 \Omega \text{ typ.}$
- High speed switching
- 4V gate drive device can be driven from 5V source

Outline



2SK3212

Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	100	V	
Gate to source voltage	$V_{\sf GSS}$	±20	V	
Drain current	I _D	10	А	
Drain peak current	Note1 D(pulse)	40	А	
Body-drain diode reverse drain current	I _{DR}	10	А	
Avalanche current	I Note3	10	А	
Avalanche energy	E _{AR} Note3	10	mJ	
Channel dissipation	Pch Note2	20	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

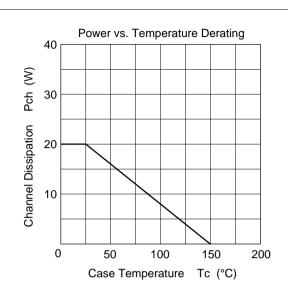
- Note: 1. PW \leq 10 μ s, duty cycle \leq 1 %
 - 2. Value at Tc = 25°C
 - 3. Value at Tch = 25° C, Rg $\geq 50\Omega$

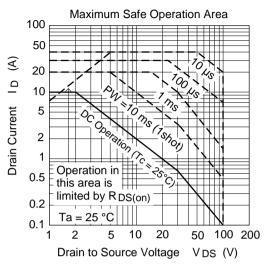
Electrical Characteristics (Ta = 25°C)

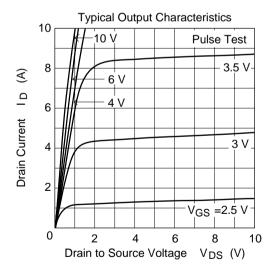
ons
_{SS} = 0
$V_{DS} = 0$
' _{DS} = 0
$I_{GS} = 0$
= 10V
10V ^{Note4}
4V Note4
10V Note4
10V
= 0
= 0 ıs

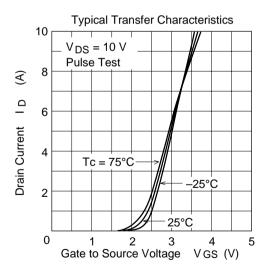
Note: 4. Pulse test

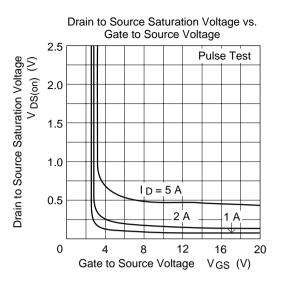
Main Characteristics

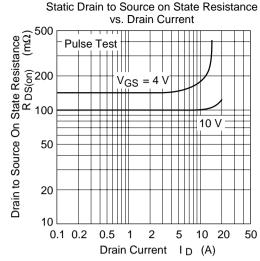


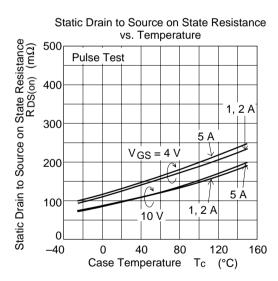


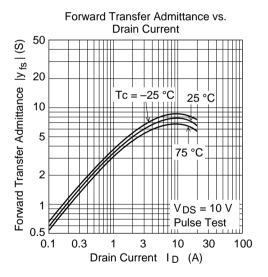


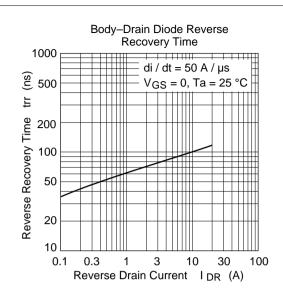


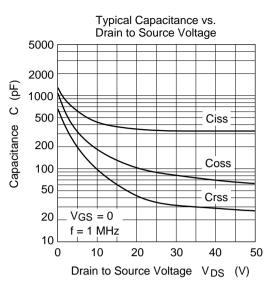


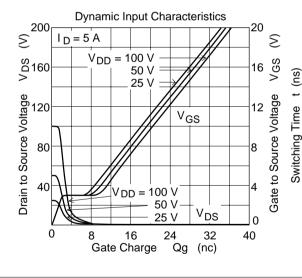


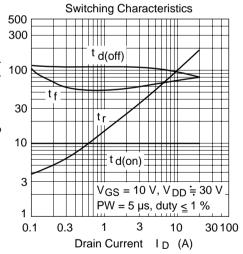


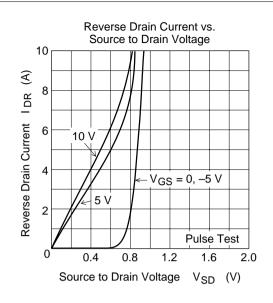


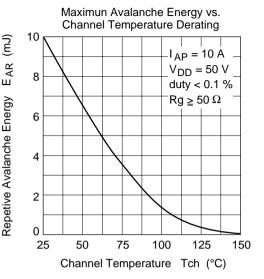




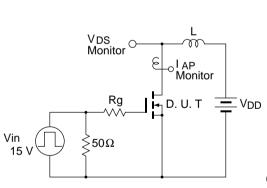






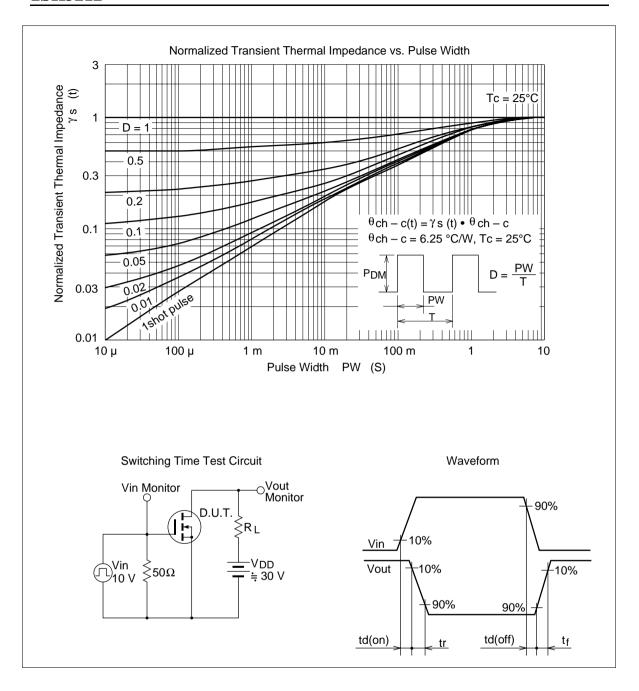


Avalanche Test Circuit

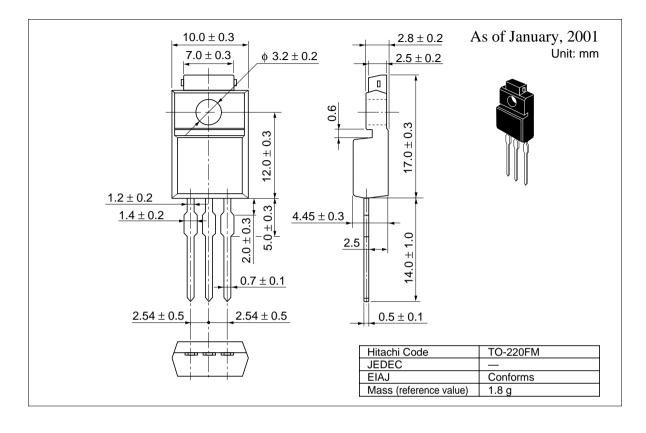


Avalanche Waveform

 $E_{AR} = \frac{1}{2} \cdot L \cdot I_{AP}^2 \cdot$



Package Dimensions



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